Dasher-2 Block Diagram

LDB-2 91.4RA01.001

Mar. 09, 2012

PCB Layer Stackup

L1:Component
L2:GND
L3:Signal 1
L4:VCC
L5:Signal 2
L6:Signal 3
L7:GND
L8:Component

XDP Conn. **11**

Keyboard Light **34**

12.5'' HD LCD **34**

CRT SELECTION **35**

CRT Port **36**

AC Coupling **38**

Display Port **36**

Express Card Solt **56**

Camera (LCD Conn) **34**

Media Card Reader **54**

4-in-1 Slot **55**

Mini PCI-E WWAN Card LPM Card **53**

Mini PCI-E WLAN Card **53**

Docking **59**

RJ45 Conn **52**

DC-IN **72**

Battery Input **73**

Intel GLAN LEWISVILLE **49**

GBE Switch PI3L500AZFEX **50**

Intel Ivy Bridge

DDR3 1600MHz
INTEGRATED GRAHPICS

3,4,5,6,7,8,9,10

UNBUFFERED DDR3 SODIMM **12**

Channel A DDR3 1600

204-PIN DDR3 SODIMM

UNBUFFERED DDR3 SODIMM **13**

Channel B DDR3 1600

DMI x4 — **FDI**

LVDS
RGB CRT
Display Port
USB 2.0 CH5
USB 2.0 CH13
PCI Express
SATA USB 2.0 CH3
PCI Express USB 2.0 CH12
PCI Express

Intel Panther Point

LVDS   CRT
Display Port
USB 2.0 (14 ports)
USB 3.0 (4 ports)
LAN Connect I/F (LCI)
Serial ATA 300MB/s
HDA Interface
    ACPI 2.0
    LPC I/F
    PCI Rev 2.3
    PCI Express
    INT. RTC
24,25,26,27,28,29,30,31,32

SATA HDD

SATA CONN **40**

Serial ATA I/F Port0

Docking **59**

Serial ATA I/F Port1

Audio Codec **43**

Azalia bus

I2C Bus / SM Bus Bus Switch IC **69**

SMBus

USB 3.0 **42**

USB 3.0 **42**

CH0,1 **USB3.0**

USB2.0(AOU) **42**

CH9

Finger Print **64**

CH10

USB 2.0

Bluetooth **42**

CH11

CH2,7

SPI

SMBus

SPI-FLASH **58**

Asset ID **69**

EC MEC1619 **60,61,62**

Thinker Engine **70,71**

G-Sensor **67**

Touch Pad **64**

Int. KB Track point IV **63**

FAN **66**

LPC Bus / 33MHz

Docking **59**

LPC Debug Board Conn **58**

TCPA ST33ZP24AR28PVSC **68**

Battery Charger/Selector
BQ24760 **74**

INPUTS	OUTPUTS
DOCK_PWR20	CHARGER_OUT12

System DC/DC
TPS51220ARSN **78**

| VINT20 | VCC5M |
| | VCC3M |

CPU DC/DC
VT1318M/VT1324S **79**

| VCC5M_OUT | VCCCPUCORE |

GMCH GFX CORE
VT1324S **80**

| VCC5M_OUT | VCCGFXCORE |

VCC1R5A
VT357FCX **85**

| VCC5M_OUT | VCC1R5A |

VCC0R75B
TPS51200DRCR **86**

| VCC1R5A | VCC0R75B |

VCC1R8B
BD9139 **89**

| VCC5M_OUT | VCC1R8B |

VCC1R05AMT
VT356FCX **84**

| VCC5M_OUT | VCC1R05LAN |

VCC1R05B_VTT
VT356 **83**

| VCC5M_OUT | VCC1R05B_VTT |

VCCSA
VT370 **90**

| VCC5M_OUT | VCCSA |
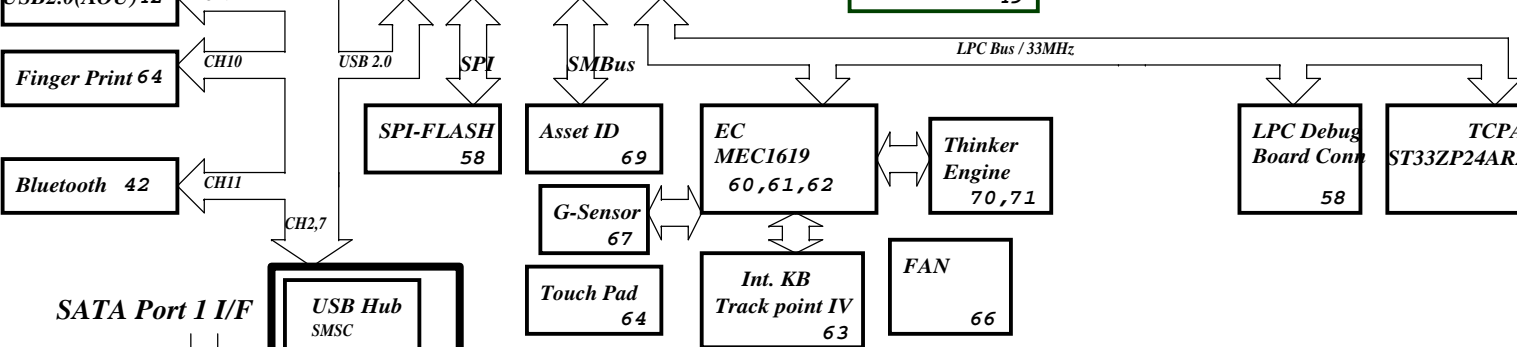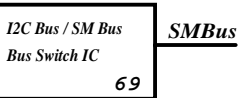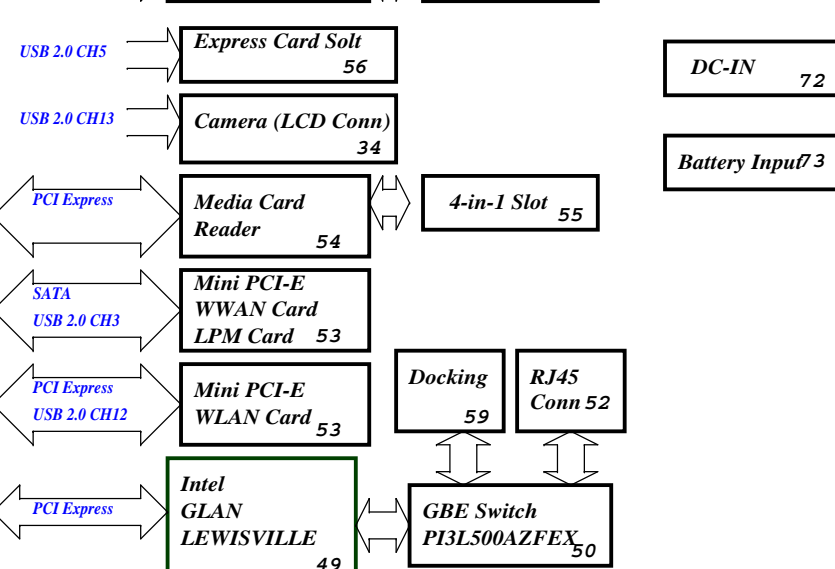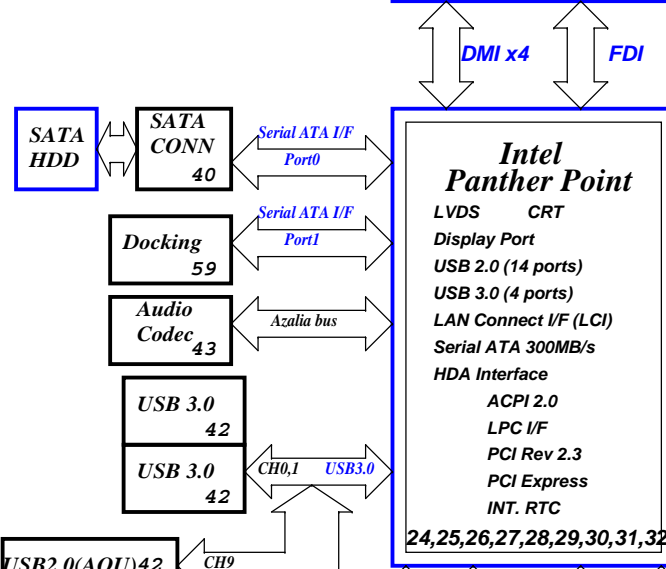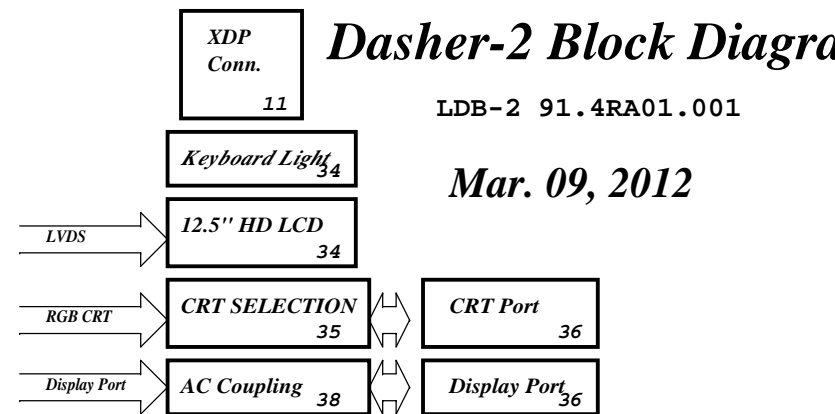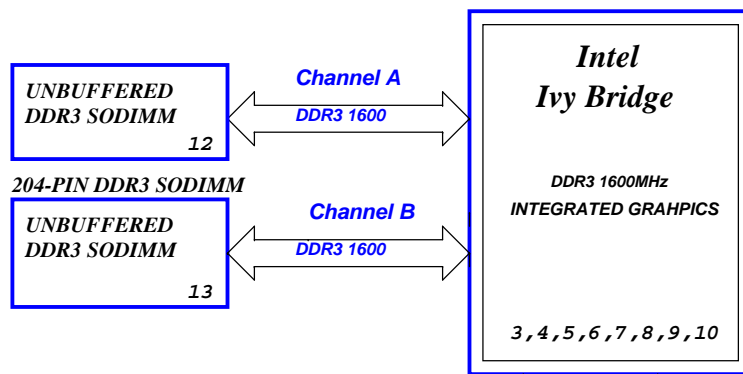
SATA Port 1 I/F

USB Hub SMSC USB2514B

HP OUT

MIC IN

SATA

USB x 4 **Docking**

Display Port 1   CRT   RJ45
Display Port 2

DC-IN

RESISTOR

Symbol name	Value	Tolerance	Rating	Size
		(J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210

The naming rule is value + R + size + tolerance
For the value, it can be read by the number before R. (R means resistor)
For the tolerance, it can be read from the last letter.
For the rating, we don't show on the symbol name.
For the size, R2=>0402, R3=>0603, R5=>0805,....

CAPACITOR

Symbol name	Value	Tolerance	Rating	Size
		(M: +/-20, K: +/-10, Z: +80/-20)		2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceremic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

IBEXPEAK-M	39	38	48	49	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0	Dasher-2 initial	N/A
	0	0	0	1	Dasher-2 PreDV	SA
	0	0	1	0	Dasher-2 SDV	SA
	0	0	1	1	Dasher-2 FVT	SB
	0	1	0	0	Dasher-2 PreSIT	SC
	0	1	0	1	Dasher-2 SIT	SD
	0	1	1	0	Dasher-2 SVT	-1
	0	1	1	1		
	1	0	0	0		
	1	0	0	1		
	1	0	1	0		

EC HISTORY

Stage	Date	EC No.	Page	Note

U1A

DMI

Signal	Pin
26 DMI_TXN[3..0]	
DMI_TXN0	M2 — DMI_RX#0
DMI_TXN1	P6 — DMI_RX#1
DMI_TXN2	P1 — DMI_RX#2
DMI_TXN3	P10 — DMI_RX#3
26 DMI_TXP[3..0]	
DMI_TXP0	N3 — DMI_RX0
DMI_TXP1	P7 — DMI_RX1
DMI_TXP2	P3 — DMI_RX2
DMI_TXP3	P11 — DMI_RX3
26 DMI_RXN[3..0]	
DMI_RXN0	K1 — DMI_TX#0
DMI_RXN1	M8 — DMI_TX#1
DMI_RXN2	N4 — DMI_TX#2
DMI_RXN3	R2 — DMI_TX#3
26 DMI_RXP[3..0]	
DMI_RXP0	K3 — DMI_TX0
DMI_RXP1	M7 — DMI_TX1
DMI_RXP2	P4 — DMI_TX2
DMI_RXP3	T3 — DMI_TX3

Intel(R) FDI

26 FDI_TXN[7..0]	
FDI_TXN0	U7 — FDI0_TX#0
FDI_TXN1	W11 — FDI0_TX#1
FDI_TXN2	W1 — FDI0_TX#2
FDI_TXN3	AA6 — FDI0_TX#3
FDI_TXN4	W6 — FDI1_TX#0
FDI_TXN5	V4 — FDI1_TX#1
FDI_TXN6	Y2 — FDI1_TX#2
FDI_TXN7	AC9 — FDI1_TX#3
26 FDI_TXP[7..0]	
FDI_TXP0	U6 — FDI0_TX0
FDI_TXP1	W10 — FDI0_TX1
FDI_TXP2	W3 — FDI0_TX2
FDI_TXP3	AA7 — FDI0_TX3
FDI_TXP4	W7 — FDI1_TX0
FDI_TXP5	T4 — FDI1_TX1
FDI_TXP6	AA3 — FDI1_TX2
FDI_TXP7	AC8 — FDI1_TX3
26 FDI_FSYNC0	AA11 — FDI0_FSYNC
26 FDI_FSYNC1	AC12 — FDI1_FSYNC
26 FDI_INT	U11 — FDI_INT
26 FDI_LSYNC0	AA10 — FDI0_LSYNC
26 FDI_LSYNC1	AG8 — FDI1_LSYNC

eDP

eDP_COMP_CPU	AF3 — EDP_COMPIO
eDP_HPD_CPU	AD2 — EDP_ICOMPO
	AG11 — EDP_HPD#
	AG4 — EDP_AUX#
	AF4 — EDP_AUX
	AC3 — EDP_TX#0
	AC4 — EDP_TX#1
	AE11 — EDP_TX#2
	AE7 — EDP_TX#3
	AC1 — EDP_TX0
	AA4 — EDP_TX1
	AE10 — EDP_TX2
	AE6 — EDP_TX3

IVY-BRIDGE-GP-NF

PCI EXPRESS -- GRAPHICS

VCC1R05B_VTT_CPU

PEG_ICOMPI — G3
PEG_ICOMPO — G1
PEG_RCOMPO — G4
PEG_COMP_CPU — R1 1 — 24D9R2F-L-GP

PEG_RX#0	H22
PEG_RX#1	J21
PEG_RX#2	B22
PEG_RX#3	D21
PEG_RX#4	A19
PEG_RX#5	D17
PEG_RX#6	B14
PEG_RX#7	D13
PEG_RX#8	A11
PEG_RX#9	B10
PEG_RX#10	G8
PEG_RX#11	A8
PEG_RX#12	B6
PEG_RX#13	H8
PEG_RX#14	E5
PEG_RX#15	K7
PEG_RX0	K22
PEG_RX1	K19
PEG_RX2	C21
PEG_RX3	D19
PEG_RX4	C19
PEG_RX5	D16
PEG_RX6	C13
PEG_RX7	D12
PEG_RX8	C11
PEG_RX9	C9
PEG_RX10	F8
PEG_RX11	C8
PEG_RX12	C5
PEG_RX13	H6
PEG_RX14	F6
PEG_RX15	K6
PEG_TX#0	G22
PEG_TX#1	C23
PEG_TX#2	D23
PEG_TX#3	F21
PEG_TX#4	H19
PEG_TX#5	C17
PEG_TX#6	K15
PEG_TX#7	F17
PEG_TX#8	F14
PEG_TX#9	A15
PEG_TX#10	J14
PEG_TX#11	H13
PEG_TX#12	M10
PEG_TX#13	F10
PEG_TX#14	D9
PEG_TX#15	J4
PEG_TX0	F22
PEG_TX1	A23
PEG_TX2	D24
PEG_TX3	E21
PEG_TX4	G19
PEG_TX5	B18
PEG_TX6	K17
PEG_TX7	G17
PEG_TX8	E14
PEG_TX9	C15
PEG_TX10	K13
PEG_TX11	G13
PEG_TX12	K10
PEG_TX13	G10
PEG_TX14	D8
PEG_TX15	K4

VCC1R05B_VTT_CPU

VCC1R05B_VTT_CPU

DY
R3
10KR2J-3-GP

R2 1 — 24D9R2F-L-GP

Table -PROC_SELECT(-PROC_IVY)

Sandy Bridge	High
Ivy Bridge	Low

U1B

2 OF 9

MISC
CLOCKS
THERMAL
DDR3 MISC
PWR MANAGEMENT
JTAG & BPM

IVY-BRIDGE-GP-NF

29 -PROC_IVY   F49 PROC_SELECT#
VCC1R05B_VTT_CPU   C57 PROC_DETECT#
R5 62R2J-GP   C49 CATERR#
62 PECI   A48 PECI
62,79 -PROCHOT   R7 1 56R2J-4-GP PROCHOT_CPU C45 PROCHOT#
VCC1R5_VDDQ   D45 THERMTRIP#
29 -THERMTRIP   TP1 TPAD40-GP
R12 200R2J-L1-GP
26 PM_SYNC   C48 PM_SYNC
11,29 CPUPWRGD   B46 UNCOREPWRGOOD
26,32,86 DRAMPWRG   R73 130R2J-GP SM_DRAMPWROK BE45 SM_DRAMPWROK
R15 10KR2J-3-GP
-CPURST   D44 RESET#

BCLK   J3   CPU_CLK_100M 25
BCLK# H2   -CPU_CLK_100M 25
DPLL_REF_CLK   AG3 DPLL_REF_CLK   R70 1 1KR2J-1-GP
DPLL_REF_CLK# AG1 -DPLL_REF_CLL R72 1 1KR2J-1-GP
VCC1R05B_VTT_CPU

SM_DRAMRST# AT30
SM_RCOMP0 BF44 SM_RCOMP0_CPU R6   1 140R2F-GP
SM_RCOMP1 BE43 SM_RCOMP1_CPU R10 1 25D5R2F-GP
SM_RCOMP2 BG43 SM_RCOMP2_CPU R11 1 200R2F-L-GP
R8 51R2J-2-GP   R9 51R2J-2-GP
VCC1R05B_VTT_CPU

PRDY# N53   -XDP_PRDY 11
PREQ# N55   -XDP_PREQ 11
TCK   L56   XDP_TCK   11
TMS   L55   XDP_TMS   11
TRST# J58   -XDP_TRST 11
TDI   M60   XDP_TDI   11
TDO   L59   XDP_TDO   11
DBR#   K58   -XDP_DBR 11,26
R13 51R2J-2-GP

BPM#0 G58
BPM#1 E55
BPM#2 E59
BPM#3 G55
BPM#4 G59
BPM#5 H60
BPM#6 J59 1 TPAD40-GP TP2
BPM#7 J61 1 TPAD40-GP TP39

VCC1R05B_VTT_CPU
R1309 75R2J-1-GP
-CPURST_R
R1308 1 43R2J-GP
VCC3B
R14 1KR2J-1-GP
LSK3541G1ET2L-GP Q4
LSK3541G1ET2L-GP
11,28,49,53 -PLTRST_FAR Q5

SM_DRAMRST_CPU
VCC1R5A
DY
12,28 DRAMRST_GATE   Q2 LSK3541G1ET2L-GP
R17 0R2J-2-GP   R18 5K1R2J-4-GP   R19 1KR2J-1-GP
DY
SCD047U25V2KX-GP C1
-DRAMRST_R   R88 1 1KR2J-1-GP   -DRAMRST 12,13
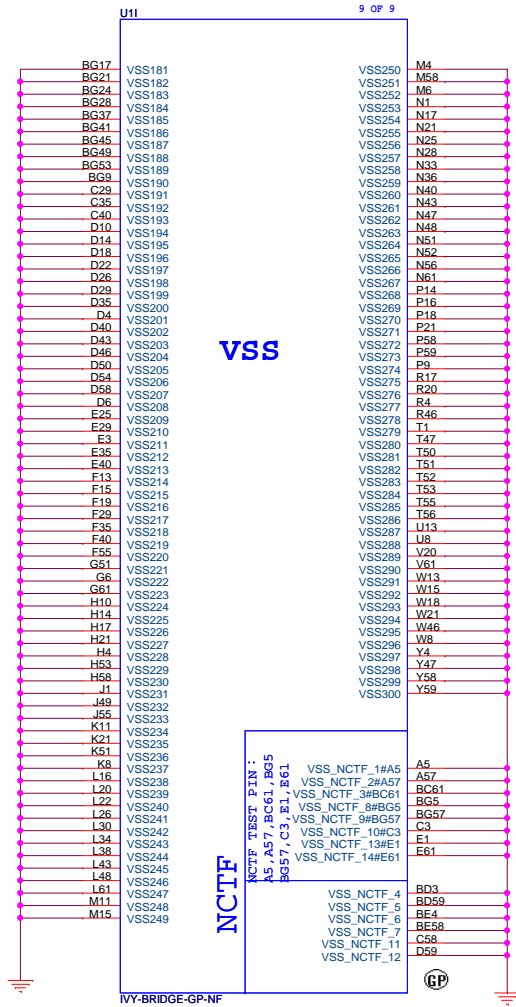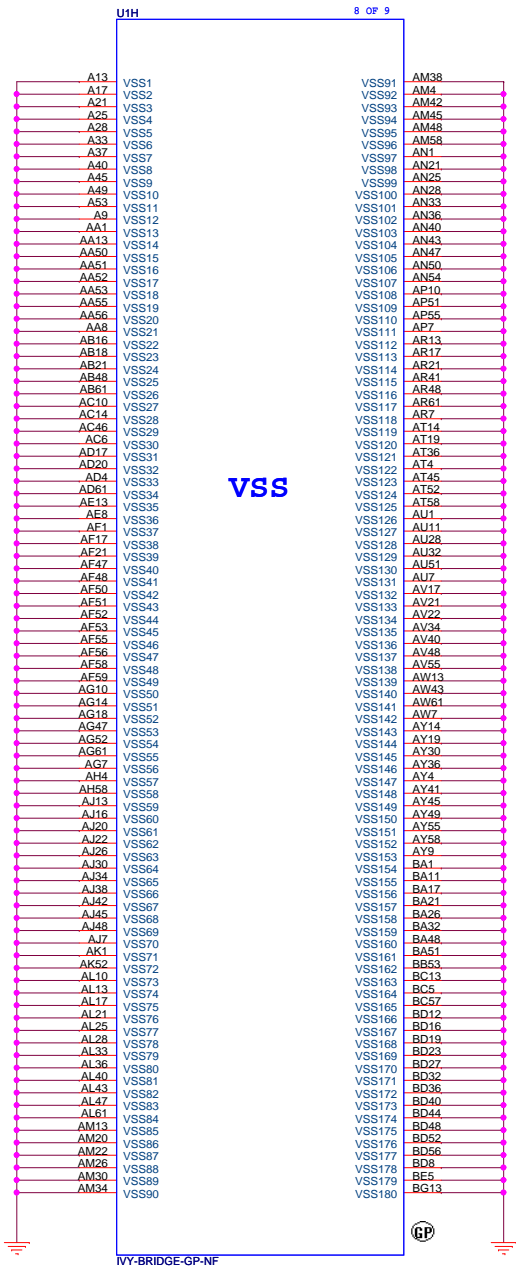
Place near DIMM connector.

<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
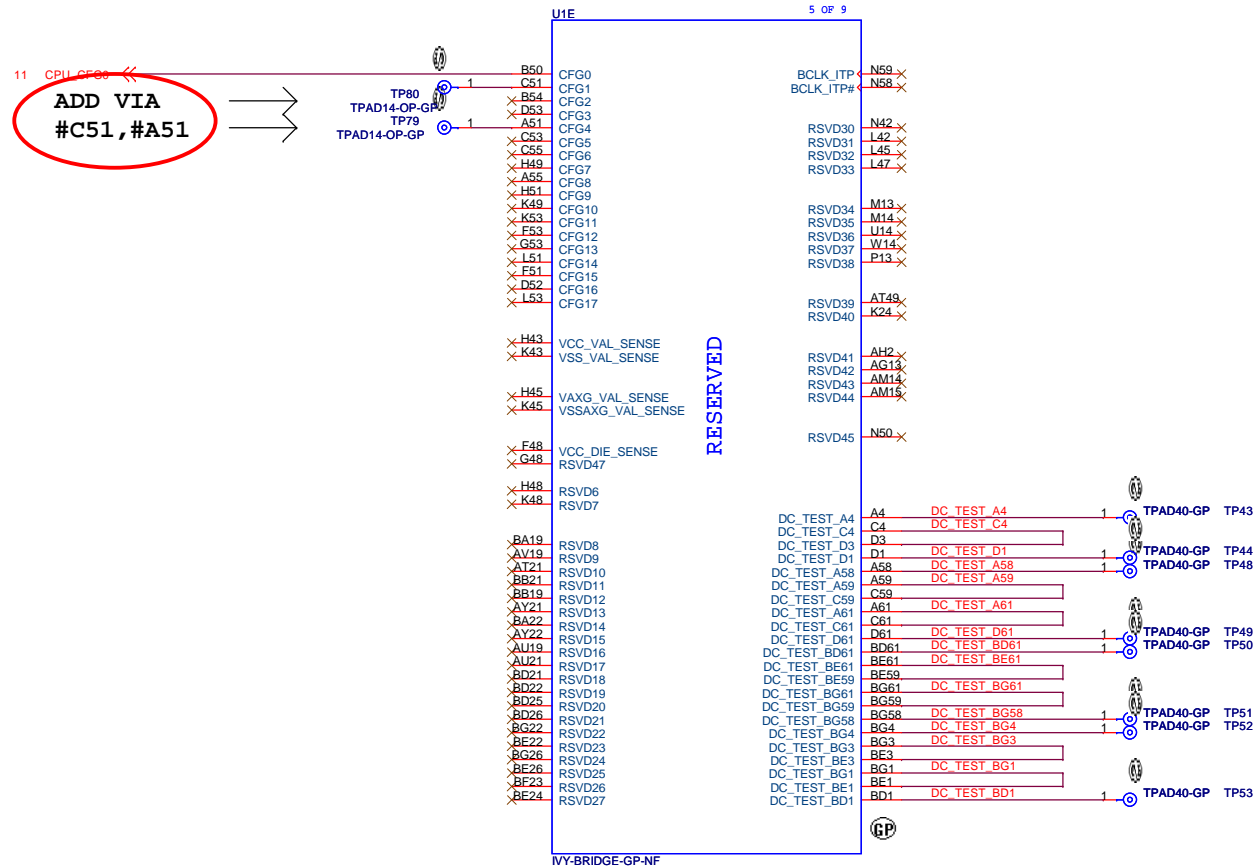Taipei Hsien 221, Taiwan, R.O.C.

Title: CPU(2/8):CLK/MISC/JTAG
Size A3
Document Number: Dasher-2
Rev -1
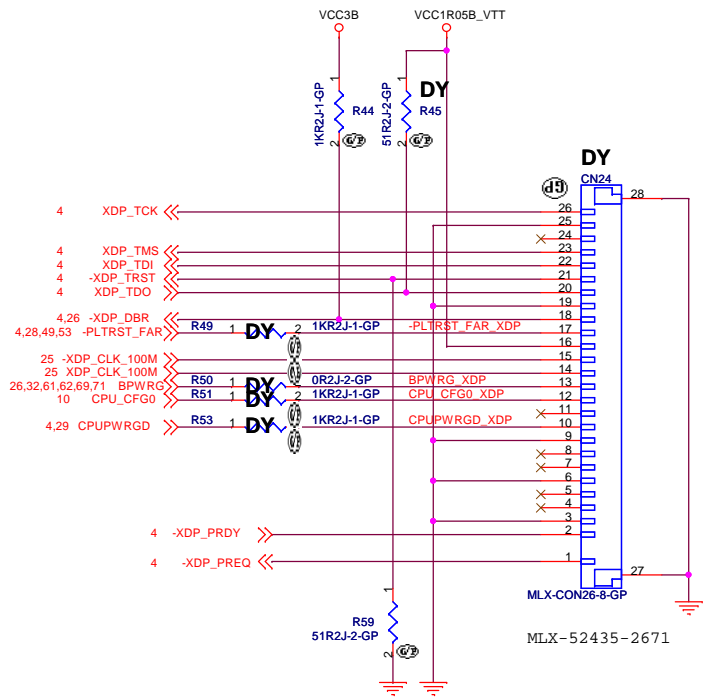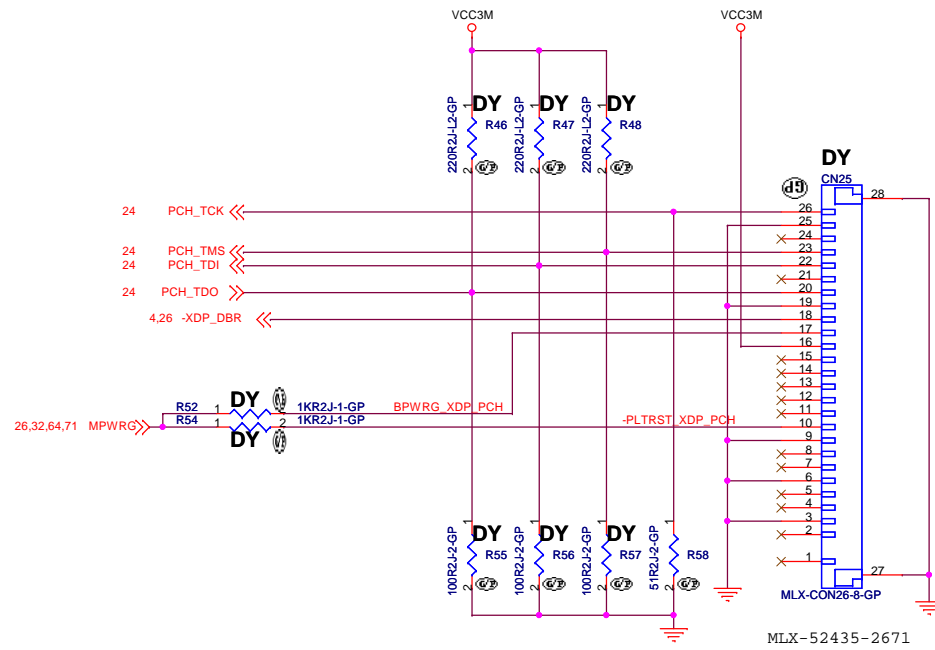Date: Friday, March 09, 2012
Sheet 4 of 101

12 M_A_DQ[63..0]

DDR SYSTEM MEMORY A

IVY-BRIDGE-GP-NF

Signal	Pin		Pin	Signal
M_A_DQ0	AG6	SA_DQ0		
M_A_DQ1	AJ6	SA_DQ1		
M_A_DQ2	AP11	SA_DQ2		
M_A_DQ3	AL6	SA_DQ3		
M_A_DQ4	AJ10	SA_DQ4		
M_A_DQ5	AJ8	SA_DQ5		
M_A_DQ6	AL8	SA_DQ6		
M_A_DQ7	AL7	SA_DQ7		
M_A_DQ8	AR11	SA_DQ8		
M_A_DQ9	AP6	SA_DQ9		
M_A_DQ10	AU6	SA_DQ10		
M_A_DQ11	AV9	SA_DQ11		
M_A_DQ12	AR6	SA_DQ12		
M_A_DQ13	AP8	SA_DQ13		
M_A_DQ14	AT13	SA_DQ14		
M_A_DQ15	AU13	SA_DQ15		
M_A_DQ16	BC7	SA_DQ16		
M_A_DQ17	BB7	SA_DQ17		
M_A_DQ18	BA13	SA_DQ18		
M_A_DQ19	BB11	SA_DQ19		
M_A_DQ20	BA7	SA_DQ20		
M_A_DQ21	BA9	SA_DQ21		
M_A_DQ22	BB9	SA_DQ22		
M_A_DQ23	AY13	SA_DQ23		
M_A_DQ24	AV14	SA_DQ24		
M_A_DQ25	AR14	SA_DQ25		
M_A_DQ26	AY17	SA_DQ26		
M_A_DQ27	AR19	SA_DQ27		
M_A_DQ28	BA14	SA_DQ28		
M_A_DQ29	AU14	SA_DQ29		
M_A_DQ30	BB14	SA_DQ30		
M_A_DQ31	BB17	SA_DQ31		
M_A_DQ32	BA45	SA_DQ32		
M_A_DQ33	AR43	SA_DQ33		
M_A_DQ34	AW48	SA_DQ34		
M_A_DQ35	BC48	SA_DQ35		
M_A_DQ36	BC45	SA_DQ36		
M_A_DQ37	AR45	SA_DQ37		
M_A_DQ38	AT48	SA_DQ38		
M_A_DQ39	AY48	SA_DQ39		
M_A_DQ40	BA49	SA_DQ40		
M_A_DQ41	AV49	SA_DQ41		
M_A_DQ42	BB51	SA_DQ42		
M_A_DQ43	AY53	SA_DQ43		
M_A_DQ44	BB49	SA_DQ44		
M_A_DQ45	AU49	SA_DQ45		
M_A_DQ46	BA53	SA_DQ46		
M_A_DQ47	BB55	SA_DQ47		
M_A_DQ48	BA55	SA_DQ48		
M_A_DQ49	AV56	SA_DQ49		
M_A_DQ50	AP50	SA_DQ50		
M_A_DQ51	AP53	SA_DQ51		
M_A_DQ52	AV54	SA_DQ52		
M_A_DQ53	AT54	SA_DQ53		
M_A_DQ54	AP56	SA_DQ54		
M_A_DQ55	AP52	SA_DQ55		
M_A_DQ56	AN57	SA_DQ56		
M_A_DQ57	AN53	SA_DQ57		
M_A_DQ58	AG56	SA_DQ58		
M_A_DQ59	AG53	SA_DQ59		
M_A_DQ60	AN55	SA_DQ60		
M_A_DQ61	AN52	SA_DQ61		
M_A_DQ62	AG55	SA_DQ62		
M_A_DQ63	AK56	SA_DQ63		

Clock / control signals:

Pin	Signal	Net	
AU36	SA_CK0	M_A_DDRCLK0_800M	M_A_DDRCLK0_800M 12
AV36	SA_CK#0	-M_A_DDRCLK0_800M	-M_A_DDRCLK0_800M 12
AY26	SA_CKE0	M_A_CKE0	M_A_CKE0 12
AT40	SA_CK1	M_A_DDRCLK1_800M	M_A_DDRCLK1_800M 12
AU40	SA_CK#1	-M_A_DDRCLK1_800M	-M_A_DDRCLK1_800M 12
BB26	SA_CKE1	M_A_CKE1	M_A_CKE1 12
BB40	SA_CS#0	-M_A_CS0	12
BC41	SA_CS#1	-M_A_CS1	12
AY40	SA_ODT0	M_A_ODT0	12
BA41	SA_ODT1	M_A_ODT1	12

-M_A_DQS[7..0] 12

Pin	Signal	Net
AL11	SA_DQS#0	-M_A_DQS0
AR8	SA_DQS#1	-M_A_DQS1
AV11	SA_DQS#2	-M_A_DQS2
AT17	SA_DQS#3	-M_A_DQS3
AV45	SA_DQS#4	-M_A_DQS4
AY51	SA_DQS#5	-M_A_DQS5
AT55	SA_DQS#6	-M_A_DQS6
AK55	SA_DQS#7	-M_A_DQS7

M_A_DQS[7..0] 12

Pin	Signal	Net
AJ11	SA_DQS0	M_A_DQS0
AR10	SA_DQS1	M_A_DQS1
AY11	SA_DQS2	M_A_DQS2
AU17	SA_DQS3	M_A_DQS3
AW45	SA_DQS4	M_A_DQS4
AV51	SA_DQS5	M_A_DQS5
AT56	SA_DQS6	M_A_DQS6
AK54	SA_DQS7	M_A_DQS7

M_A_A[15..0] 12

Pin	Signal	Net
BG35	SA_MA0	M_A_A0
BB34	SA_MA1	M_A_A1
BE35	SA_MA2	M_A_A2
BD35	SA_MA3	M_A_A3
AT34	SA_MA4	M_A_A4
AU34	SA_MA5	M_A_A5
BB32	SA_MA6	M_A_A6
AT32	SA_MA7	M_A_A7
AY32	SA_MA8	M_A_A8
AV32	SA_MA9	M_A_A9
BE37	SA_MA10	M_A_A10
BA30	SA_MA11	M_A_A11
BC30	SA_MA12	M_A_A12
AW41	SA_MA13	M_A_A13
AY28	SA_MA14	M_A_A14
AU26	SA_MA15	M_A_A15

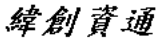| Net	Pin	Signal
12 M_A_BS0	BD37	SA_BS0
12 M_A_BS1	BF36	SA_BS1
12 M_A_BS2	BA28	SA_BS2
12 -M_A_CAS	BE39	SA_CAS#
12 -M_A_RAS	BD39	SA_RAS#
12 -M_A_WE	AT41	SA_WE#

GP

<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
CPU(3/8):DDR3 Channel-A

Size	Document Number		Rev
A3		Dasher-2	-1

Date: Friday, March 09, 2012   Sheet   5   of   101

U1D

DDR SYSTEM MEMORY B

13 M_B_DQ[63..0]

Signal	Pin	Port
M_B_DQ0	AL4	SB_DQ0
M_B_DQ1	AL1	SB_DQ1
M_B_DQ2	AN3	SB_DQ2
M_B_DQ3	AR4	SB_DQ3
M_B_DQ4	AK4	SB_DQ4
M_B_DQ5	AK3	SB_DQ5
M_B_DQ6	AN4	SB_DQ6
M_B_DQ7	AR1	SB_DQ7
M_B_DQ8	AU4	SB_DQ8
M_B_DQ9	AT2	SB_DQ9
M_B_DQ10	AV4	SB_DQ10
M_B_DQ11	BA4	SB_DQ11
M_B_DQ12	AU3	SB_DQ12
M_B_DQ13	AR3	SB_DQ13
M_B_DQ14	AY2	SB_DQ14
M_B_DQ15	BA3	SB_DQ15
M_B_DQ16	BE9	SB_DQ16
M_B_DQ17	BD9	SB_DQ17
M_B_DQ18	BD13	SB_DQ18
M_B_DQ19	BF12	SB_DQ19
M_B_DQ20	BF8	SB_DQ20
M_B_DQ21	BD10	SB_DQ21
M_B_DQ22	BD14	SB_DQ22
M_B_DQ23	BE13	SB_DQ23
M_B_DQ24	BF16	SB_DQ24
M_B_DQ25	BE17	SB_DQ25
M_B_DQ26	BE18	SB_DQ26
M_B_DQ27	BE21	SB_DQ27
M_B_DQ28	BE14	SB_DQ28
M_B_DQ29	BG14	SB_DQ29
M_B_DQ30	BG18	SB_DQ30
M_B_DQ31	BF19	SB_DQ31
M_B_DQ32	BD50	SB_DQ32
M_B_DQ33	BF48	SB_DQ33
M_B_DQ34	BD53	SB_DQ34
M_B_DQ35	BF52	SB_DQ35
M_B_DQ36	BD49	SB_DQ36
M_B_DQ37	BE49	SB_DQ37
M_B_DQ38	BD54	SB_DQ38
M_B_DQ39	BE53	SB_DQ39
M_B_DQ40	BF56	SB_DQ40
M_B_DQ41	BE57	SB_DQ41
M_B_DQ42	BC59	SB_DQ42
M_B_DQ43	AY60	SB_DQ43
M_B_DQ44	BE54	SB_DQ44
M_B_DQ45	BG54	SB_DQ45
M_B_DQ46	BA58	SB_DQ46
M_B_DQ47	AW59	SB_DQ47
M_B_DQ48	AW58	SB_DQ48
M_B_DQ49	AU58	SB_DQ49
M_B_DQ50	AN61	SB_DQ50
M_B_DQ51	AN59	SB_DQ51
M_B_DQ52	AU59	SB_DQ52
M_B_DQ53	AU61	SB_DQ53
M_B_DQ54	AN58	SB_DQ54
M_B_DQ55	AR58	SB_DQ55
M_B_DQ56	AK58	SB_DQ56
M_B_DQ57	AL58	SB_DQ57
M_B_DQ58	AG58	SB_DQ58
M_B_DQ59	AG59	SB_DQ59
M_B_DQ60	AM60	SB_DQ60
M_B_DQ61	AL59	SB_DQ61
M_B_DQ62	AF61	SB_DQ62
M_B_DQ63	AH60	SB_DQ63

	Pin	Port	Signal	
	BA34	SB_CK0	M_B_DDRCLK0_800M	M_B_DDRCLK0_800M 13
	AY34	SB_CK#0	-M_B_DDRCLK0_800M	-M_B_DDRCLK0_800M 13
	AR22	SB_CKE0	M_B_CKE0	M_B_CKE0 13
	BA36	SB_CK1	M_B_DDRCLK1_800M	M_B_DDRCLK1_800M 13
	BB36	SB_CK#1	-M_B_DDRCLK1_800M	-M_B_DDRCLK1_800M 13
	BF27	SB_CKE1	M_B_CKE1	M_B_CKE1 13
	BE41	SB_CS#0	-M_B_CS0	13
	BE47	SB_CS#1	-M_B_CS1	13
	AT43	SB_ODT0	M_B_ODT0	13
	BG47	SB_ODT1	M_B_ODT1	13

Pin	Port	Signal
AL3	SB_DQS#0	-M_B_DQS0
AV3	SB_DQS#1	-M_B_DQS1
BG11	SB_DQS#2	-M_B_DQS2
BD17	SB_DQS#3	-M_B_DQS3
BG51	SB_DQS#4	-M_B_DQS4
BA59	SB_DQS#5	-M_B_DQS5
AT60	SB_DQS#6	-M_B_DQS6
AK59	SB_DQS#7	-M_B_DQS7

-M_B_DQS[7..0] 13

Pin	Port	Signal
AM2	SB_DQS0	M_B_DQS0
AV1	SB_DQS1	M_B_DQS1
BE11	SB_DQS2	M_B_DQS2
BD18	SB_DQS3	M_B_DQS3
BE51	SB_DQS4	M_B_DQS4
BA61	SB_DQS5	M_B_DQS5
AR59	SB_DQS6	M_B_DQS6
AK61	SB_DQS7	M_B_DQS7

M_B_DQS[7..0] 13

M_B_A[15..0] 13

Pin	Port	Signal
BF32	SB_MA0	M_B_A0
BE33	SB_MA1	M_B_A1
BD33	SB_MA2	M_B_A2
AU30	SB_MA3	M_B_A3
BD30	SB_MA4	M_B_A4
AV30	SB_MA5	M_B_A5
BG30	SB_MA6	M_B_A6
BD29	SB_MA7	M_B_A7
BE30	SB_MA8	M_B_A8
BE28	SB_MA9	M_B_A9
BD43	SB_MA10	M_B_A10
AT28	SB_MA11	M_B_A11
AV28	SB_MA12	M_B_A12
BD46	SB_MA13	M_B_A13
AT26	SB_MA14	M_B_A14
AU22	SB_MA15	M_B_A15

Signal	Pin	Port
13 M_B_BS0	BG39	SB_BS0
13 M_B_BS1	BD42	SB_BS1
13 M_B_BS2	AT22	SB_BS2
13 -M_B_CAS	AV43	SB_CAS#
13 -M_B_RAS	BF40	SB_RAS#
13 -M_B_WE	BD45	SB_WE#

IVY-BRIDGE-GP-NF

GP

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
CPU(4/8):DDR3 Channel-B

Size	Document Number		Rev
A3	**Dasher-2**		-1

Date: Friday, March 09, 2012 | Sheet 6 of 101

POWER   6 OF 9

U1F

CORE SUPPLY

PEG IO AND DDR IO

QUIET RAILS

SVID

SENSE LINES

IVY-BRIDGE-GP-NF

VCCCPUCORE

need a Via to measure VCCIO_SEL voltage level.

no 0.01 ohm 1% 0402 RES

Z0 = 27.4 OHM
PU/PD < 1inch

VCC1R05B_VTT   VCC1R05B_VTT_CPU

R20   1   0R0306-PAD-GP
R87   1   0R0306-PAD-GP

VCC1R05B_VTT_CPU

TP38   TPAD14-OP-GP

R403   0R0306-PAD-GP

R21   75R2J-1-GP   R22   130R2J-GP

VCCPQE

VIDALERT_CPU   R23   43R2J-GP   SVID_ALERT   79
SVID_CLK   79
SVID_DATA 79

R24   10R2F-L-GP   R25   100R2F-L1-GP-U

VCC_SENSE_CPU   R27   0R2J-2-GP   VCCSENSE   79
VSS_SENSE_CPU   R26   0R2J-2-GP   VSSSENSE   79

VCC_SENSE_VTT   83
VSS_SENSE_VTT   83

R28   10R2F-L-GP   R29   100R2F-L1-GP-U

U1G

VCCGFXCORE_I

As for placement, please follow
"Huron River Platform Power Delivery Design Guide Rev1.0"
(Intel DocNo.439028)

GRAPHICS

DDR3 - 1.5V RAILS

VREF

SENSE LINES

QUIET RAILS

1.8V RAIL

SA RAIL

SENSE LINES

VCCSA VID lines

NEED SC2D2U4V2MX-1GP   Place near CPU

Place under CPU

As for placement, please follow
"Huron River Platform Power Delivery Design Guide Rev1.0"
(Intel DocNo.439028)

VCCGFXCORE_I

VCC1R8B

VCCSA

R30 100R2F-L1-GP-U

R31 0R2J-2-GP
R32 0R2J-2-GP

R33 100R2F-L1-GP-U

79 VCCGFX_SENSE_I
79 VSSGFX_SENSE_I

R34 0R0306-PAD-GP

VAXG_SENSE_CPU F45
VSSAXG_SENSE_CPU G45

VCC1R8B_CPU BB3
BC1
BC4

SM_VREF AY43

SA_DIMM_VREFDQ BE7
SB_DIMM_VREFDQ BG7

DDR3_VREF_DQ_SA_M3 12
DDR3_VREF_DQ_SB_M3 12

R668 1KR2F-3-GP
R669 1KR2F-3-GP

VCC1R5_VDDQ

R41 1KR2F-3-GP
R42 1KR2F-3-GP

VCC1R5_VDDQ

VCCDQ1 AM28
VCCDQ2 AN26

VCCDQ

R90 0R0306-PAD-GP

VCC1R5_VDDQ

C846

VDDQ_SENSE BC43
VSS_SENSE_VDDQ BA43

PN12 PWRNC
PN13 PWRNC

VCCSA_SENSE U10

VCCSA_SENSE 90

VCC1R05B_VTT_CPU

R1317 10KR2J-3-GP
R35 10KR2J-3-GP

VCCSA_VID0 D48
VCCSA_VID1 D49

VCCSA_VID0_CPU R1318 0R2J-2-GP
VCCSA_VID1_CPU R36 0R2J-2-GP

VCCSA_SEL0 90
VCCSA_SEL1 90

R1319 10KR2J-3-GP
R38 10KR2J-3-GP

		VCCSA_SEL0	VCCSA_SEL1	VCCSA
Sandy Bridge	LOW	0		0.90V
		1		0.80V
Ivy Bridge	High	0		0.725V
		1		0.675V

IVY-BRIDGE-GP-NF

<Core Design>

Title	
	CPU(6/8):Graphics Power
Size A3	Document Number **Dasher-2**	Rev -1

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U1H

VSS

IVY-BRIDGE-GP-NF

U1I

VSS

NCTF

NCTF TEST PIN :
A5,A57,BC61,BG5
BG57,C3,E1,E61

VSS_NCTF_1#A5
VSS_NCTF_2#A57
VSS_NCTF_3#BC61
VSS_NCTF_8#BG5
VSS_NCTF_9#BG57
VSS_NCTF_10#C3
VSS_NCTF_13#E1
VSS_NCTF_14#E61

VSS_NCTF_4
VSS_NCTF_5
VSS_NCTF_6
VSS_NCTF_7
VSS_NCTF_11
VSS_NCTF_12

IVY-BRIDGE-GP-NF

CPU(8/8):CFG/Reserved

VCC3B   VCC1R05B_VTT   VCC3M   VCC3M

DY   CN24   **DY**   CN25

MLX-52435-2671   MLX-52435-2671

MLX-CON26-8-GP   MLX-CON26-8-GP

DEBUG Interface for Processor.   **DEBUG Interface for PCH.**

XDP1 NOTE:"ASM" FOR PDV/SDV ONLY   **XDP2** NOTE:"ASM" FOR PDV/SDV ONLY

		ENABLE	DISABLE
TDO	R45	ASM	DY
TRST#	R59	ASM	ASM
DBRST#	R44	ASM	ASM
RESET#	R49	ASM	DY
CFG0	R51	ASM	DY
PWRGD	R53	ASM	DY
BPWRG	R50	ASM	DY
	CN24	ASM	DY

FVT Logic

		ENABLE	DISABLE
TDO	R46	220	DY
	R55	100	DY
TMS	R48	220	DY
	R57	100	DY
TDI	R47	220	DY
	R56	100	DY
TCK	R58	51	51
MPWRG	R52	ASM	DY
	R54	ASM	DY
	CN25	ASM	DY

FVT Logic

Signal labels (left, Processor):
XDP_TCK, XDP_TMS, XDP_TDI, -XDP_TRST, XDP_TDO, -XDP_DBR, -PLTRST_FAR, -XDP_CLK_100M, XDP_CLK_100M, BPWRG, CPU_CFG0, CPUPWRGD, -XDP_PRDY, -XDP_PREQ

-PLTRST_FAR_XDP, BPWRG_XDP, CPU_CFG0_XDP, CPUPWRGD_XDP

Signal labels (right, PCH):
PCH_TCK, PCH_TMS, PCH_TDI, PCH_TDO, -XDP_DBR, MPWRG

BPWRG_XDP_PCH, -PLTRST_XDP_PCH

Resistors: R44, R45, R46, R47, R48, R49, R50, R51, R52, R53, R54, R55, R56, R57, R58, R59

DM2

Signal	Pin	Pin	Signal	Signal
M_B_A0	98	A0		
M_B_A1	97	A1		
M_B_A2	96	A2		
M_B_A3	95	A3		
M_B_A4	92	A4		
M_B_A5	91	A5		
M_B_A6	90	A6		
M_B_A7	86	A7		
M_B_A8	89	A8		
M_B_A9	85	A9		
M_B_A10	107	A10/AP		
M_B_A11	84	A11		
M_B_A12	83	A12/BC#		
M_B_A13	119	A13		
M_B_A14	80	A14		
M_B_A15	78	A15		

REVERSE TYPE

6 M_B_A[15..0]

6 M_B_BS2
6 M_B_BS0
6 M_B_BS1
6 M_B_DQ[63..0]

RAS# 110 -M_B_RAS 6
WE#   113 -M_B_WE 6
CAS# 115 -M_B_CAS 6

S0# 114 -M_B_CS0 6
S1# 121 -M_B_CS1 6

CKE0 73 M_B_CKE0 6
CKE1 74 M_B_CKE1 6

CK0   101 M_B_DDRCLK0_800M 6
CK0# 103 -M_B_DDRCLK0_800M 6

CK1   102 M_B_DDRCLK1_800M 6
CK1# 104 -M_B_DDRCLK1_800M 6

BA2 79
BA0 109
BA1 108

DM0 11
DM1 28
DM2 46
DM3 63
DM4 136
DM5 153
DM6 170
DM7 187

SDA 200 SMB_DATA_3B 12,34,64,69
SCL 202 SMB_CLK_3B 12,34,64,69

VDDSPD 199

SA0 197
SA1 201

R64 1 10KR2J-3-GP 2

VCC3B_DIMM

C69 SC2D2U10V3KX-1GP
C70 SCD1U10V2KX-4GP

VCC1R5A_DIMM

EVENT# 198
RESET# 30 -DRAMRST 4,12
TEST 125

VDD 75,76,81,82,87,88,93,94,99,100,105,106,111,112,117,118,123,124

VSS 3,8,9,13,14,19,20,25,26,31,32,37,38,43,44,48,49,54,55,60,61,65,66,71,72,127,128,133,134,138,139,144,145,150,151,155,156,161,162,167,168,172,173,178,179,184,185,189,190,195,196

VCC1R5A_DIMM

Place one cap to each power pin and as close as possible

C71 C72 C73 C74 C75 C76 C77 C78 C79 C80 C81 SC1U10V2KX-1GP

C82 C83 C84 C85 C86 C87 C88 C89 C90 SC10U6D3V3MX-GP

VCC1R5A_DIMM

VCC0R75B_DIMM

C91 C92 C93 C94 C95 C96 C97 SC1U6D3V2KX-GP / SC10U6D3V3MX-GP

DY

Signal	Pin	Pin	Signal
M_B_DQ0	5	DQ0	
M_B_DQ1	7	DQ1	
M_B_DQ2	15	DQ2	
M_B_DQ3	17	DQ3	
M_B_DQ4	4	DQ4	
M_B_DQ5	6	DQ5	
M_B_DQ6	16	DQ6	
M_B_DQ7	18	DQ7	
M_B_DQ8	21	DQ8	
M_B_DQ9	23	DQ9	
M_B_DQ10	33	DQ10	
M_B_DQ11	35	DQ11	
M_B_DQ12	22	DQ12	
M_B_DQ13	24	DQ13	
M_B_DQ14	34	DQ14	
M_B_DQ15	36	DQ15	
M_B_DQ16	39	DQ16	
M_B_DQ17	41	DQ17	
M_B_DQ18	51	DQ18	
M_B_DQ19	53	DQ19	
M_B_DQ20	40	DQ20	
M_B_DQ21	42	DQ21	
M_B_DQ22	50	DQ22	
M_B_DQ23	52	DQ23	
M_B_DQ24	57	DQ24	
M_B_DQ25	59	DQ25	
M_B_DQ26	67	DQ26	
M_B_DQ27	69	DQ27	
M_B_DQ28	56	DQ28	
M_B_DQ29	58	DQ29	
M_B_DQ30	68	DQ30	
M_B_DQ31	70	DQ31	
M_B_DQ32	129	DQ32	
M_B_DQ33	131	DQ33	
M_B_DQ34	141	DQ34	
M_B_DQ35	143	DQ35	
M_B_DQ36	130	DQ36	
M_B_DQ37	132	DQ37	
M_B_DQ38	140	DQ38	
M_B_DQ39	142	DQ39	
M_B_DQ40	147	DQ40	
M_B_DQ41	149	DQ41	
M_B_DQ42	157	DQ42	
M_B_DQ43	159	DQ43	
M_B_DQ44	146	DQ44	
M_B_DQ45	148	DQ45	
M_B_DQ46	158	DQ46	
M_B_DQ47	160	DQ47	
M_B_DQ48	163	DQ48	
M_B_DQ49	165	DQ49	
M_B_DQ50	175	DQ50	
M_B_DQ51	177	DQ51	
M_B_DQ52	164	DQ52	
M_B_DQ53	166	DQ53	
M_B_DQ54	174	DQ54	
M_B_DQ55	176	DQ55	
M_B_DQ56	181	DQ56	
M_B_DQ57	183	DQ57	
M_B_DQ58	191	DQ58	
M_B_DQ59	193	DQ59	
M_B_DQ60	180	DQ60	
M_B_DQ61	182	DQ61	
M_B_DQ62	192	DQ62	
M_B_DQ63	194	DQ63	

6 -M_B_DQS[7..0]

Signal	Pin	Pin
-M_B_DQS0	10	DQS0#
-M_B_DQS1	27	DQS1#
-M_B_DQS2	45	DQS2#
-M_B_DQS3	62	DQS3#
-M_B_DQS4	135	DQS4#
-M_B_DQS5	152	DQS5#
-M_B_DQS6	169	DQS6#
-M_B_DQS7	186	DQS7#

6 M_B_DQS[7..0]

Signal	Pin	Pin
M_B_DQS0	12	DQS0
M_B_DQS1	29	DQS1
M_B_DQS2	47	DQS2
M_B_DQS3	64	DQS3
M_B_DQS4	137	DQS4
M_B_DQS5	154	DQS5
M_B_DQS6	171	DQS6
M_B_DQS7	188	DQS7

6 M_B_ODT0 116 ODT0
6 M_B_ODT1 120 ODT1

1 VREFDQ
126 VREFCA
2 VSS
77 NC
122 NC
206 GND
NP1 NP1

203 VTT
204 VTT
205 GND
NP2 NP2

DDR3-204P-35-GP

DDR3_VREF_DQ_SB
DDR3_VREF_DQ_SB
DDR3_VREF_CA

C98 SC2D2U6D3V3MX-1-GP
C99 SCD1U10V2KX-4GP

Place caps close to pin126 as possible

<Core Design>

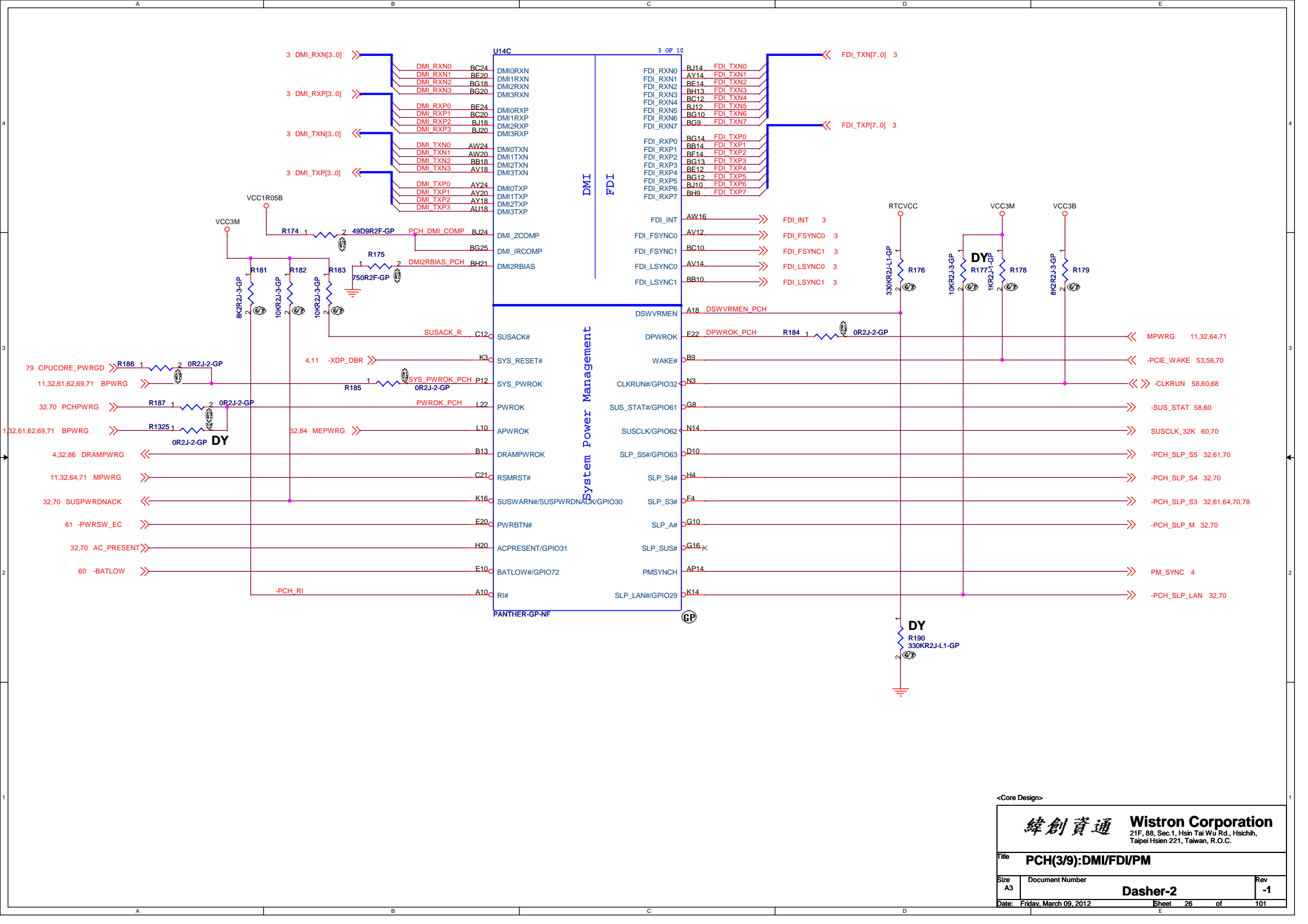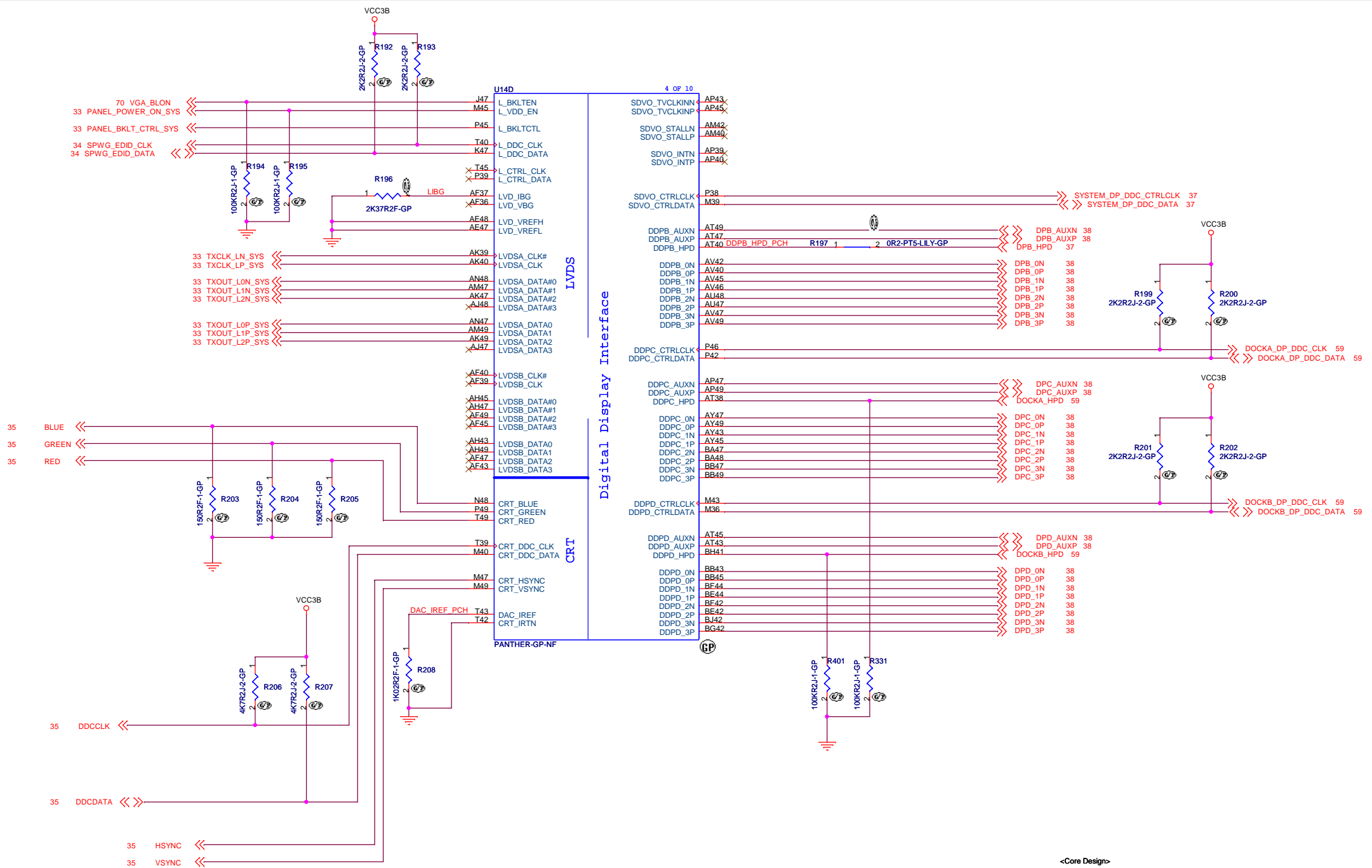**緯創資通 Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
DDR3 SODIMM-B

Size	Document Number	Rev
Custom	**Dasher-2**	**-1**

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<Core Design>

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Taipei Hsien 221, Taiwan, R.O.C.

Title			
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<Core Design>

Title
BLANK

Size
A3

Document Number

Dasher-2

Rev
-1

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TABLE

AMT	YES	NO	NO
RPAT	YES	YES	NO
U14	QM77	HM77	HM75

LOGIC

TABLE

SPKR	TCO TIMER SYSTEM REBOOT
HIGH	DISABLED(NO REBOOT)
LOW	ENABLED

WWAN mSATA

WWAN mSATA	Yes	No
C212	ASM	DY
C211	ASM	DY
C213	ASM	DY
C214	ASM	DY

LOGIC

DO NOT CHANGE THESE PARTS

DST310S 9P 20PPM

Place on Top side

Place on Bootom side
Do NOT move after fix

U14A
1 OF 10

PANTHER-GP-NF

<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: PCH(1/9):HDA/JTAG/SPI/SATA
Size: A2
Document Number
Dasher-2
Rev: -1
Date: Friday, March 09, 2012
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PCH(2/9):PCI-E/SMBUS/CLK

Wistron Corporation
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Dasher-2

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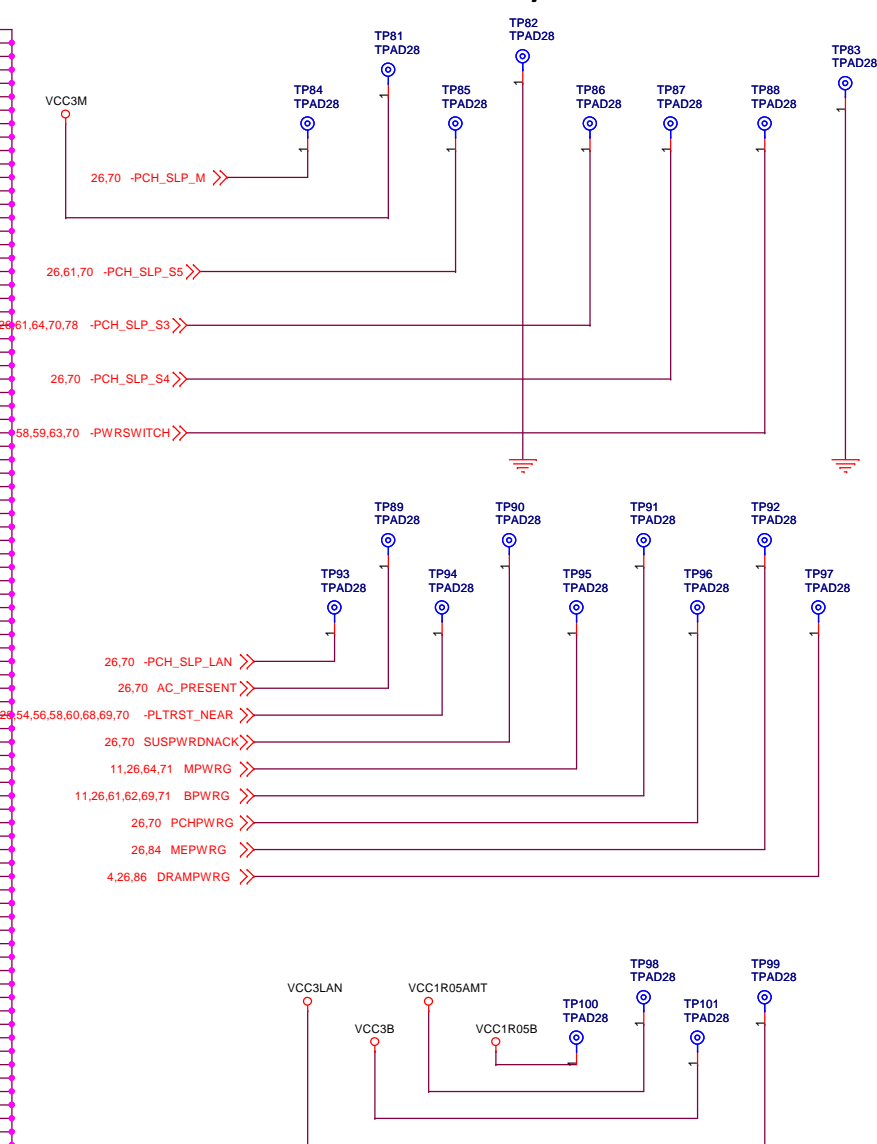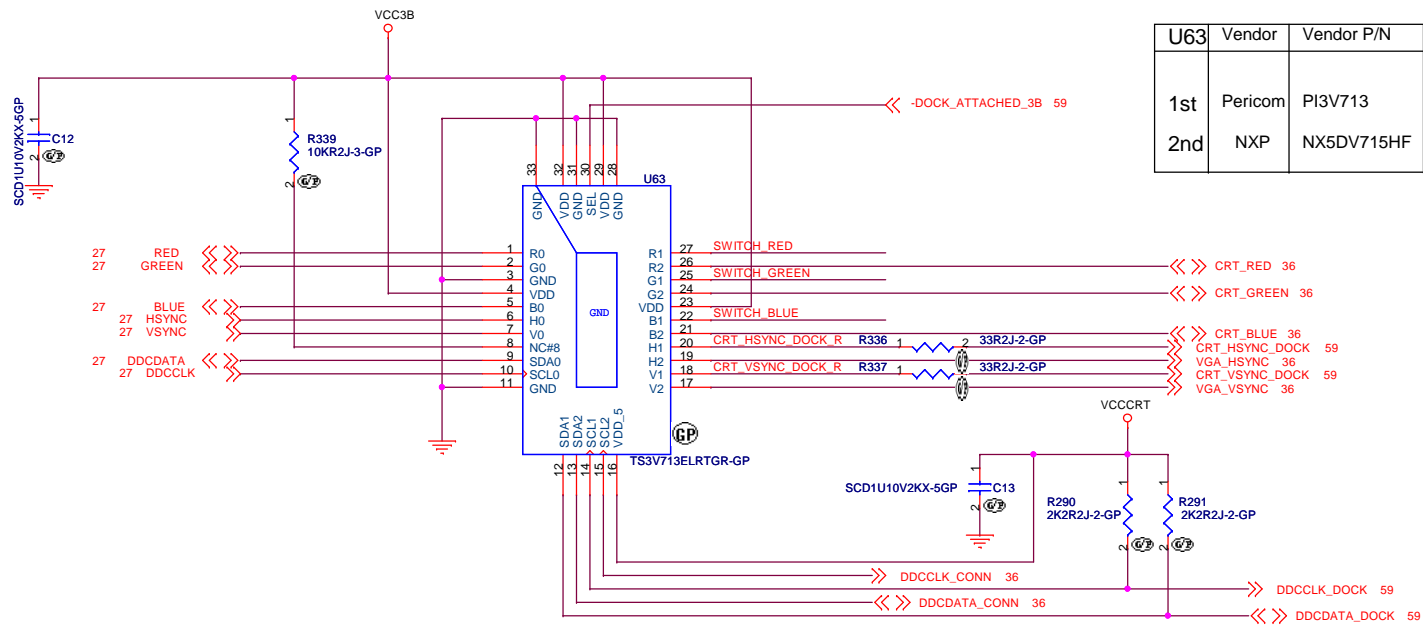
PCH(3/9):DMI/FDI/PM

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

<Core Design>

Title: PCH(3/9):DMI/FDI/PM

Size: A3

Document Number

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Rev: -1

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This page is an electronic schematic diagram (Sheet 27 of 101) titled "PCH(4/9):LVDS/CRT/DDI".

Component U14D — PANTHER-GP-NF — Digital Display Interface / LVDS / CRT

Left side signals:

- 70 VGA_BLON — J47 L_BKLTEN
- 33 PANEL_POWER_ON_SYS — M45 L_VDD_EN
- 33 PANEL_BKLT_CTRL_SYS — P45 L_BKLTCTL
- 34 SPWG_EDID_CLK — T40 L_DDC_CLK
- 34 SPWG_EDID_DATA — K47 L_DDC_DATA
- T45 L_CTRL_CLK
- P39 L_CTRL_DATA
- AF37 LVD_IBG
- AF36 LVD_VBG
- AE48 LVD_VREFH
- AE47 LVD_VREFL
- 33 TXCLK_LN_SYS — AK39 LVDSA_CLK#
- 33 TXCLK_LP_SYS — AK40 LVDSA_CLK
- 33 TXOUT_L0N_SYS — AN48 LVDSA_DATA#0
- 33 TXOUT_L1N_SYS — AM47 LVDSA_DATA#1
- 33 TXOUT_L2N_SYS — AK47 LVDSA_DATA#2
- AJ48 LVDSA_DATA#3
- 33 TXOUT_L0P_SYS — AN47 LVDSA_DATA0
- 33 TXOUT_L1P_SYS — AM49 LVDSA_DATA1
- 33 TXOUT_L2P_SYS — AK49 LVDSA_DATA2
- AJ47 LVDSA_DATA3
- AF40 LVDSB_CLK#
- AF39 LVDSB_CLK
- AH45 LVDSB_DATA#0
- AH47 LVDSB_DATA#1
- AF49 LVDSB_DATA#2
- AF45 LVDSB_DATA#3
- AH43 LVDSB_DATA0
- AH49 LVDSB_DATA1
- AF47 LVDSB_DATA2
- AF43 LVDSB_DATA3
- 35 BLUE — N48 CRT_BLUE
- 35 GREEN — P49 CRT_GREEN
- 35 RED — T49 CRT_RED
- T39 CRT_DDC_CLK
- M40 CRT_DDC_DATA
- M47 CRT_HSYNC
- M49 CRT_VSYNC
- DAC_IREF_PCH — T43 DAC_IREF
- T42 CRT_IRTN

Right side signals:

- AP43 SDVO_TVCLKINN
- AP45 SDVO_TVCLKINP
- AM42 SDVO_STALLN
- AM40 SDVO_STALLP
- AP39 SDVO_INTN
- AP40 SDVO_INTP
- P38 SDVO_CTRLCLK — SYSTEM_DP_DDC_CTRLCLK 37
- M39 SDVO_CTRLDATA — SYSTEM_DP_DDC_DATA 37
- AT49 DDPB_AUXN — DPB_AUXN 38
- AT47 DDPB_AUXP — DPB_AUXP 38
- AT40 DDPB_HPD — DDPB_HPD_PCH — R197 — 0R2-PT5-LILY-GP — DPB_HPD 37
- AV42 DDPB_0N — DPB_0N 38
- AV40 DDPB_0P — DPB_0P 38
- AV45 DDPB_1N — DPB_1N 38
- AV46 DDPB_1P — DPB_1P 38
- AU48 DDPB_2N — DPB_2N 38
- AU47 DDPB_2P — DPB_2P 38
- AV47 DDPB_3N — DPB_3N 38
- AV49 DDPB_3P — DPB_3P 38
- P46 DDPC_CTRLCLK — DOCKA_DP_DDC_CLK 59
- P42 DDPC_CTRLDATA — DOCKA_DP_DDC_DATA 59
- AP47 DDPC_AUXN — DPC_AUXN 38
- AP49 DDPC_AUXP — DPC_AUXP 38
- AT38 DDPC_HPD — DOCKA_HPD 59
- AY47 DDPC_0N — DPC_0N 38
- AY49 DDPC_0P — DPC_0P 38
- AY43 DDPC_1N — DPC_1N 38
- AY45 DDPC_1P — DPC_1P 38
- BA47 DDPC_2N — DPC_2N 38
- BA48 DDPC_2P — DPC_2P 38
- BB47 DDPC_3N — DPC_3N 38
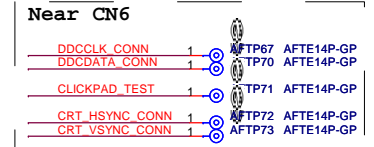- BB49 DDPC_3P — DPC_3P 38
- M43 DDPD_CTRLCLK — DOCKB_DP_DDC_CLK 59
- M36 DDPD_CTRLDATA — DOCKB_DP_DDC_DATA 59
- AT45 DDPD_AUXN — DPD_AUXN 38
- AT43 DDPD_AUXP — DPD_AUXP 38
- BH41 DDPD_HPD — DOCKB_HPD 59
- BB43 DDPD_0N — DPD_0N 38
- BB45 DDPD_0P — DPD_0P 38
- BF44 DDPD_1N — DPD_1N 38
- BF42 DDPD_1P — DPD_1P 38
- BE44 DDPD_2N — DPD_2N 38
- BE42 DDPD_2P — DPD_2P 38
- BJ42 DDPD_3N — DPD_3N 38
- BG42 DDPD_3P — DPD_3P 38

Resistors: R192, R193 (2K2R2J-2-GP), R194, R195 (100KR2J-1-GP), R196 (2K37R2F-GP / LIBG), R199, R200, R201, R202 (2K2R2J-2-GP), R203, R204, R205 (150R2F-1-GP), R206, R207 (4K7R2J-2-GP), R208 (1K02R2F-1-GP), R401, R331 (100KR2J-1-GP), R197 (0R2-PT5-LILY-GP)

VCC3B

Other left-bottom signals:
- 35 DDCCLK
- 35 DDCDATA
- 35 HSYNC
- 35 VSYNC

<Core Design>

Dasher2	
USB0	System Port 0 (USB3.0)
USB1	System Port 1 (USB3.0)
USB2	since Dock USB3.0
USB3	WWAN
USB4	Reserved
USB5	Express Card
USB6	Reserved
USB7	USB2.0 DOCKING
USB8	Reserved
USB9	System Port 9 (iPod)
USB10	FPR/TOUCH PAD
USB11	Bluetooth
USB12	WLAN
USB13	Camera

U14E

RSVD — RSVD1 AY7, RSVD2 AV7, RSVD3 AU3, RSVD4 BG4, RSVD5 AT10, RSVD6 BC8, RSVD7 AU2, RSVD8 AT4, RSVD9 AT1, RSVD10 AY3, RSVD11 AT5, RSVD12 AV3, RSVD13 AV1, RSVD14 BB1, RSVD15 BA3, RSVD16 BB5, RSVD17 BB3, RSVD18 BE8, RSVD19 BB7, RSVD20 BD4, RSVD21 BF6, RSVD22, RSVD23 AV5, RSVD24 AV10, RSVD25 AT8, RSVD26 AY5, RSVD27 BA2, RSVD28 AT12, RSVD29 BF3

TP1 BG26, TP2 BJ26, TP3 BH25, TP4 BJ16, TP5 BG16, TP6 AH38, TP7 AH37, TP8 AK43, TP9 AK45, TP10 C18, TP11 N30, TP12 H3, TP13 AH12, TP14 AM4, TP15 AM5, TP16 Y13, TP17 K24, TP18 L24, TP19 AB46, TP20 AB45
TP21 B21, TP22 M20, TP23 AY16, TP24 BG46

USB
USB3RN1 BE28, USB3RN2 BC30, USB3RN3 BE32, USB3RN4 BJ32, USB3RP1 BC28, USB3RP2 BE30, USB3RP3 BF32, USB3RP4 BG32, USB3TN1 AV26, USB3TN2 BB26, USB3TN3 AU28, USB3TN4 AY30, USB3TP1 AU26, USB3TP2 AY26, USB3TP3 AV28, USB3TP4 AW30

42 USB3P0_RXN, 42 USB3P1_RXN, 59 USB3P2_RXN
42 USB3P0_RXP, 42 USB3P1_RXP, 59 USB3P2_RXP
42 USB3P0_TXN, 42 USB3P1_TXN, 59 USB3P2_TXN
42 USB3P0_TXP, 42 USB3P1_TXP, 59 USB3P2_TXP

PCI
PIRQA# K40, PIRQB# K38, PIRQC# H38, PIRQD# G38
REQ1#/GPIO50 C46, REQ2#/GPIO52 C44, REQ3#/GPIO54 E40
GNT1#/GPIO51 D47, GNT2#/GPIO53 E42, GNT3#/GPIO55 F46
PIRQE#/GPIO2 G42, PIRQF#/GPIO3 G40, PIRQG#/GPIO4 C42, PIRQH#/GPIO5 D44
PME# K10, PLTRST# C6
CLKOUT_PCI0 H49, CLKOUT_PCI1 H43, CLKOUT_PCI2 J48, CLKOUT_PCI3 K42, CLKOUT_PCI4 H40

PIRQD_PCH, PIRQC_PCH, PIRQB_PCH, PIRQA_PCH
REQ1_PCH
42 -BDC_PRESENCE
GNT1_PCH, GNT2_PCH, GNT3_PCH
34 -LCD_PRESENCE
PIRQF_PCH, PIRQG_PCH
24 -RTC_DETECT

USBP0N C24, USBP0P A24, USBP1N B25, USBP1P A25, USBP2N A26, USBP2P C26, USBP3N K28, USBP3P H28, USBP4N E28, USBP4P D28, USBP5N C28, USBP5P A28, USBP6N C29, USBP6P B29, USBP7N N28, USBP7P M28, USBP8N L30, USBP8P K30, USBP9N G30, USBP9P E30, USBP10N C30, USBP10P A30, USBP11N K32, USBP11P G32, USBP12N E32, USBP12P C32, USBP13N A32, USBP13P

USBP0- 42, USBP0+ 42, USBP1- 42, USBP1+ 42, USBP2- 59, USBP2+ 59, USBP3- 53, USBP3+ 53, USBP5- 56, USBP5+ 56, USBP7- 59, USBP7+ 59, USBP9- 42, USBP9+ 42, USBP10- 64, USBP10+ 64, USBP11- 42, USBP11+ 42, USBP12- 53, USBP12+ 53, USBP13- 34, USBP13+ 34

USBRBIAS# C33 PCH_USBRBIAS R233 1 22D6R2F-L1-GP
USBRBIAS B33

OC0#/GPIO59 A14, OC1#/GPIO40 K20, OC2#/GPIO41 B17, OC3#/GPIO42 C16, OC4#/GPIO43 L16, OC5#/GPIO9 A16, OC6#/GPIO10 D14, OC7#/GPIO14 C14

-USB_PORT0_OC0 42, -USB_PORT1_OC1 42, -USB_PORT2_OC3 59, SMB_3B_EN 69, -USB_PORT9_OC5 42, DRAMRST_GATE 4,12

PANTHER-GP-NF

VCC3B
R209 8K2R2J-3-GP, R210, R211 DY, R212 DY, R213, R214, R215
R218, R219, R220, R221 DY, R222, R223, R224

VCC3M
R225, R226, R227, R228, R229, R230, R231, R232 (10KR2J-3-GP)

R262 1 DY 2 8K2R2J-3-GP
INTEGRATED PULL UP
Minimize this stub!
R1326 22R2J-2-GP
R234 1 22R2J-2-GP LPCCLK_CRYPT_33M_R
R235 1 22R2J-2-GP LPCCLK_DEBUG_33M_R DY
R236 1 22R2J-2-GP LPCCLK_EC_33M_R DY
R237 1 22R2J-2-GP PCICLK_FB_33M_R

68 LPCCLK_CRYPT_33M
58 LPCCLK_DEBUG_33M
60 LPCCLK_EC_33M
25 PCICLK_FB_33M

RFC14 SC33P50V2JN-3GP DY, RFC15 SC33P50V2JN-3GP DY, RFC16 SC33P50V2JN-3GP DY

-PLTRST_PCH

U15
VCC 5, NC#1 1, IN_A 2, OUT_Y 4, GND 3
TC7SG17FE-GP
VCC3M

32,54,56,58,60,68,69,70 -PLTRST_NEAR
R238 1 2 33R2J-2-GP
R239 1 2 33R2J-2-GP
4,11,49,53 -PLTRST_FAR
C229 SC100P50V2JN-3GP, C230 SC100P50V2JN-3GP

USB Port Figure(Top View)
FAN
USB Port0
USB Port1
USB Port9
Left / Right / Front

GPIO8	INTEGRATED CLOCKING
HIGH	DISABLE(BTM)
LOW	ENABLE(FCIM)

GPIO15	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY

GPIO34	LPM CAPABILITY
HIGH	LPM CAPABLE PLANAR
LOW	LPM NON-SUPPORT PLANAR

U14F 6 OF 10

GPIO

Pin	Signal		Signal	Pin
T7	BMBUSY#/GPIO0		TACH4/GPIO68	C40
A42	TACH1/GPIO1		TACH5/GPIO69	B41
H36	TACH2/GPIO6		TACH6/GPIO70	C41
E38	TACH3/GPIO7		TACH7/GPIO71	A40
C10	GPIO8			
C4	LAN_PHY_PWR_CTRL/GPIO12			
G2H	GPIO15		A20GATE	P4
U2	SATA4GP/GPIO16		PECI	AU16
D40	TACH0/GPIO17		RCIN#	P5
T5	SCLOCK/GPIO22		PROCPWRGD	AY11
E8	GPIO24		THRMTRIP#	AY10
E16	GPIO27		INIT3_3V#	T14
P8	GPIO28		DF_TVS	AY1
K1	STP_PCI#/GPIO34		TS_VSS1	AH8
K4	GPIO35		TS_VSS2	AK11
V8	SATA2GP/GPIO36		TS_VSS3	AH10
M5	SATA3GP/GPIO37		TS_VSS4	AK10
N2	SLOAD/GPIO38		NC_1	P37
M3	SDATAOUT0/GPIO39			
V13	SDATAOUT1/GPIO48		VSS_NCTF_15#BG2	BG2
V3	SATA5GP/GPIO49/TEMP_ALERT#		VSS_NCTF_16#BG48	BG48
D6	GPIO57		VSS_NCTF_17#BH3	BH3

CPU/MISC

NCTF

			VSS_NCTF_18#BH47	BH47
TPAD14-OP-GP	TP10	A4	VSS_NCTF_1#A4	VSS_NCTF_19#BJ4	BJ4
TPAD14-OP-GP	TP12	A44	VSS_NCTF_2#A44	VSS_NCTF_20#BJ44	BJ44
TPAD14-OP-GP	TP14	A45	VSS_NCTF_3#A45	VSS_NCTF_21#BJ45	BJ45
TPAD14-OP-GP	TP16	A46	VSS_NCTF_4#A46	VSS_NCTF_22#BJ46	BJ46
TPAD14-OP-GP	TP18	A5	VSS_NCTF_5#A5	VSS_NCTF_23#BJ5	BJ5
TPAD14-OP-GP	TP20	A6	VSS_NCTF_6#A6	VSS_NCTF_24#BJ6	BJ6
TPAD14-OP-GP	TP22	B3	VSS_NCTF_7#B3	VSS_NCTF_25#C2	C2
TPAD14-OP-GP	TP24	B47	VSS_NCTF_8#B47	VSS_NCTF_26#C48	C48
TPAD14-OP-GP	TP26	BD1	VSS_NCTF_9#BD1	VSS_NCTF_27#D1	D1
TPAD14-OP-GP	TP28	BD49	VSS_NCTF_10#BD49	VSS_NCTF_28#D49	D49
TPAD14-OP-GP	TP30	BE1	VSS_NCTF_11#BE1	VSS_NCTF_29#E1	E1
TPAD14-OP-GP	TP32	BE49	VSS_NCTF_12#BE49	VSS_NCTF_30#E49	E49
TPAD14-OP-GP	TP34	BF1	VSS_NCTF_13#BF1	VSS_NCTF_31#F1	F1
TPAD14-OP-GP	TP36	BF49	VSS_NCTF_14#BF49	VSS_NCTF_32#F49	F49

				Signal	Pin	TP	
					BG2	TP6	TPAD14-OP-GP
					BG48	TP7	TPAD14-OP-GP
					BH3	TP8	TPAD14-OP-GP
					BH47	TP9	TPAD14-OP-GP
					BJ4	TP11	TPAD14-OP-GP
					BJ44	TP13	TPAD14-OP-GP
					BJ45	TP15	TPAD14-OP-GP
					BJ46	TP17	TPAD14-OP-GP
					BJ5	TP19	TPAD14-OP-GP
					BJ6	TP21	TPAD14-OP-GP
					C2	TP23	TPAD14-OP-GP
					C48	TP25	TPAD14-OP-GP
					D1	TP27	TPAD14-OP-GP
					D49	TP29	TPAD14-OP-GP
					E1	TP31	TPAD14-OP-GP
					E49	TP33	TPAD14-OP-GP
					F1	TP35	TPAD14-OP-GP
					F49	TP37	TPAD14-OP-GP

PANTHER-GP-NF

NCTF TEST PIN:
A4,A44,A45,A46,A5,A6,B3,B47,
BD1,BD49,BE1,BE49,BF1,BF49,
BG2,BG48,BH3,BH47,BJ4,BJ44,
BJ45,BJ46,BJ5,BJ6,C2,C48,D1,
D49,E1,E49,F1,F49

VCC3B VCC3M VCC3B VCC1R8B

R244 R245 R246 R247 R1327 R249 R251 R240
R252 R253 R254 R255
10KR2J-3-GP 1KR2J-1-GP
DY DY

R256 1 2 10KR2J-3-GP
R258 2 390R2J-1-GP
R1328 2K2R3J-1-GP
R1329 1 2 1KR2J-1-GP

R304 2 0R2J-2-GP
R319 1 0R2J-2-GP
R322 1 0R2J-2-GP
R326 1 0R2J-2-GP

VCC3B
DY R289 10KR2J-3-GP

VCC3M
R241 1KR2J-1-GP DY R260 10KR2J-3-GP R243 10KR2J-3-GP
R610 10KR2J-3-GP

R298 R297 R261
10KR2J-3-GP 10KR2J-3-GP 1KR2J-1-GP

VCC3B
R263 R264 R265 R266
DY 10KR2J-3-GP DY
R267 R268 R269 R270
DY 10KR2J-3-GP DY

60 -EC_SCI
49 LANPHYPC
24 SATA_BAY_DTCT
53 -MSATA_DTCT

TACH2 TACH3 GPIO8_PCH GPIO15_PCH SATA4GP_PCH TACH0_PCH GPIO24_PCH GPIO28_PCH LPM_CAPABLE_ID GPIO35_PCH GPIO36_PCH GPIO37_PCH

BMBUSY#

-INT_MIC_DTCT 34
-MIC_HW_EN
-WAN_DTCT 53
KBGA20 60
-KBRC 60
CPUPWRGD 4,11
-THERMTRIP 4
-PROC_IVY_DF_TVS
-PROC_IVY 4
THRMTRIP_PCH

PLANARID0
PLANARID1
PLANARID2
PLANARID3

PLANAR ID: 0110 for SVT

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **PCH(6/9):GPIO/NCTF/RSVD**

Size A3	Document Number	Rev -1
	Dasher-2	

Date: Friday, March 09, 2012 Sheet 29 of 101

POWER

U14G

VCC CORE

Pin	Signal
AA23	VCCCORE1
AC23	VCCCORE2
AD21	VCCCORE3
AD23	VCCCORE4
AF21	VCCCORE5
AF23	VCCCORE6
AG21	VCCCORE7
AG23	VCCCORE8
AG24	VCCCORE9
AG26	VCCCORE10
AG27	VCCCORE11
AG29	VCCCORE12
AJ23	VCCCORE13
AJ26	VCCCORE14
AJ27	VCCCORE15
AJ29	VCCCORE16
AJ31	VCCCORE17

VCCIO

Pin	Signal
AN19	VCCIO28
BJ22	VCCAPLLEXP
AN16	VCCIO15
AN17	VCCIO16
AN21	VCCIO17
AN26	VCCIO18
AN27	VCCIO19
AP21	VCCIO20
AP23	VCCIO21
AP24	VCCIO22
AP26	VCCIO23
AT24	VCCIO24
AN33	VCCIO25
AN34	VCCIO26

FDI

Pin	Signal
BH29	VCC3_3_3
AP16	VCCVRM2
BG6	VCCAFDIPLL
AP17	VCCIO27
AU20	VCCDMI2

CRT

Pin	Signal
U48	VCCADAC
U47	VSSADAC

LVDS

Pin	Signal
AK36	VCCALVDS
AK37	VSSALVDS
AM37	VCCTX_LVDS1
AM38	VCCTX_LVDS2
AP36	VCCTX_LVDS3
AP37	VCCTX_LVDS4

HVCMOS

Pin	Signal
V33	VCC3_3_6
V34	VCC3_3_7

DMI

Pin	Signal
AT16	VCCVRM3
AT20	VCCDMI1
AB36	VCCCLKDMI

DFT / SPI

Pin	Signal
AG16	VCCDFTERM1
AG17	VCCDFTERM2
AJ16	VCCDFTERM3
AJ17	VCCDFTERM4
V1	VCCSPI

PANTHER-GP-NF

Left column power pad resistors (0R0306-PAD-GP):
- R271: VCC1R05B → VCC1R05B_PCHIO
- R272: VCC1R8B → VCC1R8B_PCH
- R273: VCC1R05B_VTT → VCC1R05B_VTT_PCH
- R274: VCC3B → VCC3B_PCH
- R327: VCC1R5B → VCC1R5B_PCH

R344 0R0306-PAD-GP

VCC1R05B_PCHCORE
- C231 SC10U6D3V3MX-GP
- C232 SC1U10V2KX-1GP
- C443 SC1U10V2KX-1GP

L8 IND-1UH-100-GP
VCCAPLLEXP_PCH

DY C240 SC10U6D3V3MX-GP

VCC1R05B_PCHIO
SC10U6D3V3MX-GP
- C242 SC10U6D3V3MX-GP
- C243 SC1U10V2KX-1GP
- C244 SC1U10V2KX-1GP
- C245 SC1U10V2KX-1GP
- C246 SC1U10V2KX-1GP

VCC3B_PCH
C250 SCD1U10V2KX-4GP

DY
L9 IND-1UH-100-GP
R275 0R0306-PAD-GP VCCAFDIPLL_PCH / VCCIO_PCH

DY
C252 SC10U6D3V3MX-GP

Right side:
L6 BLM18PG181SN1D-GP → VCC3B_PCH
VCCADAC_PCH
- C233 SCD01U25V2KX-3GP
- C234 SCD1U10V2KX-4GP
- C235 SC10U6D3V5KX-2GP

L7 IND-D1UH-19-GP → VCC1R8B_PCH
42A0149AA
VCCTX_LVDS_PCH
- C237 SCD01U25V2KX-3GP
- C238 SCD01U25V2KX-3GP
- C239 SC22U6D3V5MX-2GP

VCC3B_PCH
C241 SCD1U10V2KX-4GP

VCC1R5B_PCH

VCC1R05B_VTT_PCH
VCC1R05B_VCCCLKDMI R345 0R0306-PAD-GP → VCC1R05B
- C247 SC1U10V2KX-1GP
- C248 SC1U10V2KX-1GP
C249 SCD1U10V2KX-4GP
VCC1R8B_PCH

VCC3LAN
VCC3LAN_VCCSPI R409 0R0306-PAD-GP
C251 SCD1U10V2KX-4GP

U14J — POWER

Blocks within the IC: USB, Clock and Miscellaneous, PCI/GPIO/LPC, SATA, MISC, HDA, CPU, RTC

Left-side pins:
- AD49 VCCACLK — VCCACLK_PCH
- T16 VCCDSW3_3 — VCCDSW3_3_PCH
- V12 DCPSUSBYP — DCPSUSBYP_PCH
- T38 VCC3_3_5
- BH23 VCCAPLLDMI2 — VCCAPLLDMI2_PCH
- AL29 VCCIO14
- AL24 DCPSUS3 — DCPSUS_PCH
- AA19 VCCASW1
- AA21 VCCASW2
- AA24 VCCASW3
- AA26 VCCASW4
- AA27 VCCASW5
- AA29 VCCASW6
- AA31 VCCASW7
- AC26 VCCASW8
- AC27 VCCASW9
- AC29 VCCASW10
- AC31 VCCASW11
- AD29 VCCASW12
- AD31 VCCASW13
- W21 VCCASW14
- W23 VCCASW15
- W24 VCCASW16
- W26 VCCASW17
- W29 VCCASW18
- W31 VCCASW19
- W33 VCCASW20
- N16 DCPRTC — DCPRTC_PCH
- Y49 VCCVRM4
- BD47 VCCADPLLA — VCCADPLLA_PCH
- BF47 VCCADPLLB — VCCADPLLB_PCH
- AF17 VCCIO7
- AF33 VCCDIFFCLKN1
- AF34 VCCDIFFCLKN2
- AG34 VCCDIFFCLKN3
- AG33 VCCSSC
- V16 DCPSST — DCPSST_PCH
- T17 DCPSUS1
- V19 DCPSUS2 — DCPSUS_PCH
- BJ8 V_PROC_IO
- A22 VCCRTC

Right-side pins:
- N26 VCCIO29 — VCCIO_USB_PCH
- P26 VCCIO30
- P28 VCCIO31
- T27 VCCIO32
- T29 VCCIO33
- T23 VCCSUS3_3_7
- T24 VCCSUS3_3_8
- V23 VCCSUS3_3_9
- V24 VCCSUS3_3_10
- P24 VCCSUS3_3_6
- T26 VCCIO34
- M26 V5REF_SUS — V5REF_SUS_PCH
- AN23 DCPSUS4
- AN24 VCCSUS3_3_1
- P34 V5REF — V5REF_PCH
- N20 VCCSUS3_3_2
- N22 VCCSUS3_3_3
- P20 VCCSUS3_3_4
- P22 VCCSUS3_3_5
- AA16 VCC3_3_1
- W16 VCC3_3_8
- T34 VCC3_3_4
- AJ2 VCC3_3_2
- AF13 VCCIO5 — VCCIO_SATA_PCH
- AH13 VCCIO12
- AH14 VCCIO13
- AF14 VCCIO6
- AK1 VCCAPLLSATA — VCCAPLLSATA_PCH
- AF11 VCCVRM1
- AC16 VCCIO2
- AC17 VCCIO3
- AD17 VCCIO4
- T21 VCCASW22
- V21 VCCASW23
- T19 VCCASW21
- P32 VCCSUSHDA

PANTHER-GP-NF

Power nets / labels: VCC1R05B_CLKGEN, VCC1R05B, VCC3M_PCH, VCC3B_PCH, VCC3M, VCC3M_PCH, VCC5M, VCC5B, VCC1R05AMT_PCH, VCC1R05AMT, VCC1R5B_PCH, VCC1R05B_VTT_PCH, RTCVCC

Components (partial):
- R276 0R2J-2-GP
- R278 0R0402-PAD-1-GP
- R277 0R0306-PAD-GP
- R282 0R0306-PAD-GP
- R284 0R0306-PAD-GP
- R281 0R0306-PAD-GP
- R279 100R2J-2-GP
- R280 100R2J-2-GP
- R283 0R0306-PAD-GP
- R285 0R0306-PAD-GP
- C257 SCD1U10V2KX-5GP
- C254 SCD1U10V2KX-5GP
- C253 SC1U10V2KX-1GP
- C255 SC10U6D3V3MX-GP
- C258 SCD1U10V2KX-5GP
- C256 SC1U10V2KX-1GP
- C259 SCD1U10V2KX-5GP
- C260 SCD1U10V2KX-5GP
- C261 SC22U6D3V5MX-2GP
- C262 SC22U6D3V5MX-2GP
- C263 SC1U10V2KX-1GP
- C264 SC1U10V2KX-1GP
- C265 SC1U10V2KX-1GP
- C266 SC1U10V2KX-1GP
- C267 SC1U10V2KX-1GP
- C268 SC1U10V2KX-1GP
- C269 SCD1U10V2KX-5GP
- C270 SCD1U10V2KX-5GP
- C271 SCD1U10V2KX-5GP
- C272 SC1U10V3KX-3GP
- C273 SC1U10V2KX-1GP
- C274 SC10U6D3V3MX-GP
- C275 SC1U10V2KX-1GP
- C276 SC1U10V2KX-1GP
- C277 SC1U10V2KX-1GP
- C278 SC1U10V2KX-1GP
- C279 SC1U10V2KX-1GP
- C280 SCD1U10V2KX-5GP
- C281 SC1U10V2KX-1GP
- C282 SC4D7U6D3V3KX-GP
- C283 SCD1U10V2KX-4GP
- C284 SCD1U10V2KX-4GP
- C285 SC1U10V2KX-1GP
- C286 SCD1U10V2KX-4GP
- C287 SCD1U10V2KX-4GP
- C288 SC1U10V2KX-1GP
- TC3 ST220U2D5VBM-4GP
- TC4 ST220U2D5VBM-4GP
- L10 IND-1UH-100-GP
- L11 IND-10UH-193-GP
- L12 IND-1UH-100-GP
- L13 IND-10UH-193-GP
- D18 RB461FGT106-GP
- D19 RB461FGT106-GP

"DY" markers appear at several locations.

Title		
	PCH (8/9):Power	
Size A3	Document Number **Dasher-2**	Rev -1

TEST PAD FOR METS/APS

U14H 8 OF 10

PANTHER-GP-NF

U14I 9 OF 10

PANTHER-GP-NF

TP81 TPAD28
TP82 TPAD28
TP83 TPAD28
TP84 TPAD28
TP85 TPAD28
TP86 TPAD28
TP87 TPAD28
TP88 TPAD28

VCC3M

26,70 -PCH_SLP_M
26,61,70 -PCH_SLP_S5
26,61,64,70,78 -PCH_SLP_S3
26,70 -PCH_SLP_S4
58,59,63,70 -PWRSWITCH

TP89 TPAD28
TP90 TPAD28
TP91 TPAD28
TP92 TPAD28
TP93 TPAD28
TP94 TPAD28
TP95 TPAD28
TP96 TPAD28
TP97 TPAD28

26,70 -PCH_SLP_LAN
26,70 AC_PRESENT
26,54,56,58,60,68,69,70 -PLTRST_NEAR
26,70 SUSPWRDNACK
11,26,64,71 MPWRG
11,26,61,62,69,71 BPWRG
26,70 PCHPWRG
26,84 MEPWRG
4,26,86 DRAMPWRG

TP98 TPAD28
TP99 TPAD28
TP100 TPAD28
TP101 TPAD28

VCC3LAN
VCC3B
VCC1R05AMT
VCC1R05B

Title: PCH (9/9):GND
Dasher-2
Size A3
Rev -1
Date: Friday, March 09, 2012
Sheet 32 of 101

supplier	Vendor P/N	Wistron P/N
Pericom	PIHDMI412-BZHE	71.03412.B0G
TI	TS3DV421RUAR	71.03421.003

TXOUT_L0N_SYS R1330 0R2-PT5-LILY-GP TXOUT_L0N
TXOUT_L0P_SYS R1331 0R2-PT5-LILY-GP TXOUT_L0P
TXOUT_L1N_SYS R1332 0R2-PT5-LILY-GP TXOUT_L1N
TXOUT_L1P_SYS R1333 0R2-PT5-LILY-GP TXOUT_L1P
TXOUT_L2N_SYS R1334 0R2-PT5-LILY-GP TXOUT_L2N
TXOUT_L2P_SYS R1335 0R2-PT5-LILY-GP TXOUT_L2P
TXCLK_LN_SYS R1336 0R2-PT5-LILY-GP TXCLK_LN
TXCLK_LP_SYS R1337 0R2-PT5-LILY-GP TXCLK_LP

1mA

VCC1R8B_BRG

VCC3_BRG

U68 DY
1 IN OUT 5
2 GND
3 EN NR 4
TPS79318DBVR-1-GP

C941 SC1U10V2KX-1GP
DY C939 SCD01U25V2KX-3GP
DY C940 SC2D2U6D3V2MX-GP

VCC1R8B_BRG

DY U69

27 TXOUT_L0N_SYS 38 D0+A VDD 2
27 TXOUT_L0P_SYS 37 D0-A VDD 8
27 TXOUT_L1N_SYS 36 D1+A VDD 16
27 TXOUT_L1P_SYS 35 D1-A VDD 18
27 TXOUT_L2N_SYS 34 D2+A VDD 20
27 TXOUT_L2P_SYS 33 D2-A VDD 30
27 TXCLK_LN_SYS 32 D3+A VDD 40
27 TXCLK_LP_SYS 31 D3-A VDD 42

TXOUT_L0N_LPM 29 D0+B D0+ 3 TXOUT_L0N 34
TXOUT_L0P_LPM 28 D0-B D0- 4 TXOUT_L0P 34
TXOUT_L1N_LPM 27 D1+B D1+ 6 TXOUT_L1N 34
TXOUT_L1P_LPM 26 D1-B D1- 7 TXOUT_L1P 34
TXOUT_L2N_LPM 25 D2+B D2+ 11 TXOUT_L2N 34
TXOUT_L2P_LPM 24 D2-B D2- 12 TXOUT_L2P 34
TXCLK_LN_LPM 23 D3+B D3+ 14 TXCLK_LN 34
TXCLK_LP_LPM 22 D3-B D3- 15 TXCLK_LP 34

1 VSS
5 VSS
10 VSS
13 VSS
17 VSS
19 VSS
21 VSS SEL 9
39 VSS
41 VSS
43 VSS
PI3HDMI412FT-BZHE-1-GP

DY C942 SC1K P50V2KX-1GP
DY C943 SC1K P50V2KX-1GP
DY C944 SC1K P50V2KX-1GP
DY C945 SC1K P50V2KX-1GP

VCC3_BRG

DY R1341 10KR2J-3-GP
VCC1R8B_BRG
DY R1340 10KR2J-3-GP

Q128 DY
LTC015EEB-FS8-GP
Q127 DY
LTC015EEB-FS8-GP

64,70 DUPLICITY_ENABLE

DY C318 SCD1U10V2KX-4GP

VCC3_BRG

U70 DY
1 B1 SELECT 6
2 GND VCC 5
3 B0 A 4
NLASB3157DFT2G-1-GP

62 BACKLIGHT_ON_LPM DUPLICITY_ENABLE 64,70
BACKLIGHT_ON 34

70 BACKLIGHT_ON_SYS R1338 0R2-PT5-LILY-GP

DY C946 SCD01U25V2KX-3GP

VCC3_BRG

U71 DY
1 B1 SELECT 6
2 GND VCC 5
3 B0 A 4
NLASB3157DFT2G-1-GP

70 PANEL_POWER_ON_LPM DUPLICITY_ENABLE 64,70
PANEL_POWER_ON 71

27 PANEL_POWER_ON_SYS R1339 0R2-PT5-LILY-GP

DY C947 SCD01U25V2KX-3GP

VCC3_BRG

U72 DY
1 B1 SELECT 6
2 GND VCC 5
3 B0 A 4
NLASB3157DFT2G-1-GP

62 PANEL_BKLT_CTRL_LPM DUPLICITY_ENABLE 64,70
PANEL_BKLT_CTRL 34

27 PANEL_BKLT_CTRL_SYS R1342 0R2-PT5-LILY-GP

DY C948 SCD01U25V2KX-3GP

DY CN26

TXOUT_L2P_LPM 1 2
TXOUT_L2N_LPM 3 4
5 6 TXCLK_LN_LPM
TXOUT_L1N_LPM 7 8 TXCLK_LP_LPM
TXOUT_L1P_LPM 9 10
11 12
TXOUT_L0P_LPM 13 14 MIC_DATA_DUP 34
TXOUT_L0N_LPM 15 16 MIC_CLK_DUP 34
17 18 -SE_PWR_BTN 62
61 -PE2SE_REQ 19 20 PS_HOLD 62
HRS-CONN20D-10-GP

LCD Connector

PolySW

LCD Connector

For Camera SYS

Near CN3

System LED Connector Pin	LED Subcard Connector Pin
26 VCC3_BRG	5 -> MIC
25 VCC3SW	6 -> Hall Sensor
24 VCC3M	7 -> LED

WIDE (10MIL) PATTERN

WIDE (10mil) PATTERN

U63	Vendor	Vendor P/N	Wistron P/N
1st	Pericom	PI3V713	73.3V713.A0I
2nd	NXP	NX5DV715HF	73.05715.003
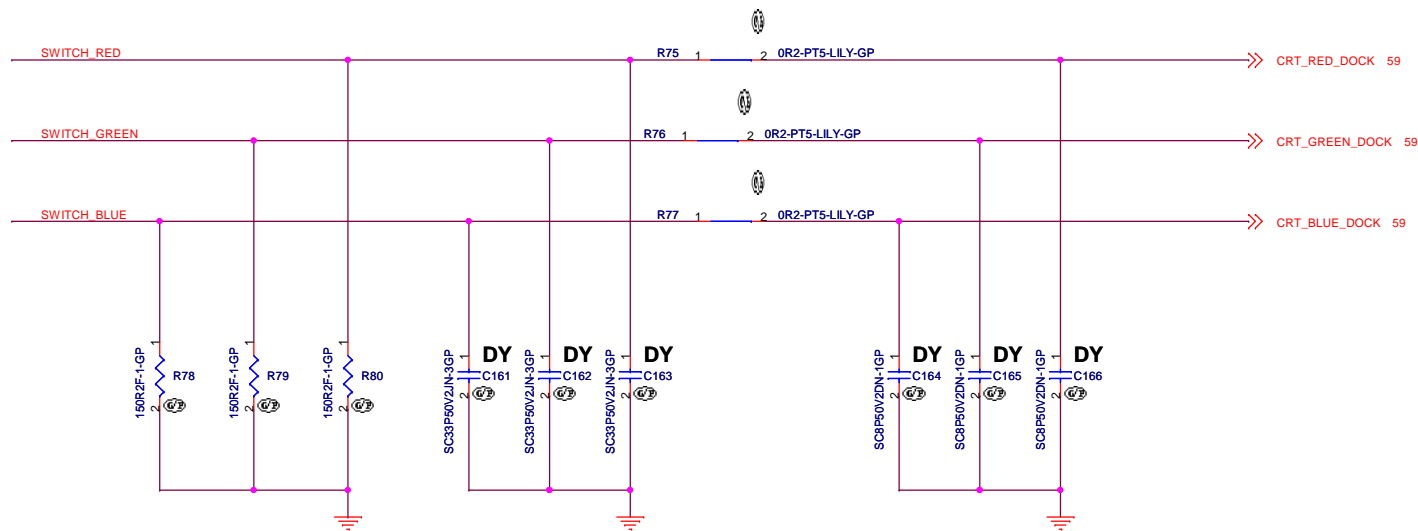
GND GUARDING

EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)

SPACING=20MIL

ZO=50 OHM

ZO=75 OHM

CRT CONN

Near CN6

Title
Ext CRT Interface

Size A3	Document Number		Rev -1
		Dasher-2	
Date: Friday, March 09, 2012		Sheet 36 of	101

Display Port Connector Schematic (Dasher-2, Sheet 37)

VCC3B
VCC5B

C181
SCD1U10V2KX-4GP

DY

R93
2K2R2J-2-GP

R94
2K2R2J-2-GP

U7
CBT3257ABQ-GP

16 VCC
2 1B1 — 38 SYSTEM_DP_AUXP
5 2B1 — 38 SYSTEM_DP_AUXN
11 3B1
14 4B1
3 1B2
6 2B2 — 27 SYSTEM_DP_DDC_CTRLCLK
10 3B2 — 27 SYSTEM_DP_DDC_DATA
13 4B2

1A 4
2A 7
3A 9
4A 12
OE# 15
S 1 — DVI_DONGLE_DTCT
GND 8
GND 17

VCC3B

R96
100KR2J-1-GP

DY

VCC3B
VCC5B

R95
1MR2J-1-GP

Q6
LSK3541G1ET2L-GP
G S D

27 DPB_HPD — DP_HPD_CONN
DP_AUXP_CONN
DP_AUXN_CONN

R97
100KR2J-1-GP

100KR2J-1-GP
R100

VCC3B
POLYSW 6V 1.5A NANOSMDC150F
VCC3VIDEO_DP

F8
FUSE-1D5A6V-10GP

DY

U10
TPCF8002-1-GP

SCD47U6D33V3KX-GP
C185

SC100U6D33V6MX-GP
C186

U11
RCLAMP0522P-TCT-GP-U
RCLAMP0522P-GP
place near DP connector

71,94 VCC3B_DRV

470KR2J-2-GP
R101
C187
SCD1U25V3KX-GP

DY

To prevent leakage current from DP monitor

VCC3VIDEO_DP

AFTE14P-GP
AFTP76

AFTE14P-GP AFTP74

SKT1
SKT-DISPLAY20P-14-GP

21
1
22 NP1
3
5
4
6
38 SYSTEM_DP0P
38 SYSTEM_DP0N
7
9
8
10
38 SYSTEM_DP1P
38 SYSTEM_DP1N
11
13
12
14
15
17
16
18
38 SYSTEM_DP2P
38 SYSTEM_DP2N
19
23
20 NP2
24

DP_HPD_CONN
DVI_DONGLE_DTCT
SYSTEM_DP3P 38
SYSTEM_DP3N 38
DP_AUXP_CONN
DP_AUXN_CONN

U8
RCLAMP0524PATCT-GP
1 L1#1 L1#8 8
2 L2#2 L2#7 7
G1 GND GND G2
3 L3#3 L3#6 6
4 L4#4 L4#5 5
place near DP connector

U9
RCLAMP0524PATCT-GP
1 L1#1 L1#8 8
2 L2#2 L2#7 7
G1 GND GND G2
3 L3#3 L3#6 6
4 L4#4 L4#5 5
place near DP connector

SYSTEM_DP3N
SYSTEM_DP3P

SCD1U10V2KX-4GP
C182

VARISTOR-27V-2-GP
U64

VARISTOR-27V-2-GP
U65

1MR2J-L2-GP
R99 1MR2J-GP
R98 5M1R2J-GP

place near DP connector
U64,U65 AVR-M1005C120MTAAB

Near SKT1
DP_HPD_CONN — AFTP78 AFTE14P-GP
DVI_DONGLE_DTCT — AFTP79 AFTE14P-GP
N180657675 — AFTP80 AFTE14P-GP

<Core Design>

Title: **Display Port Connector**
Size: Custom
Document Number
Rev: -1
Dasher-2
Date: Friday, March 09, 2012
Sheet 37 of 101

System DP Connector

27 DPB_AUXP	C100 1 SCD1U10V2KX-4GP	SYSTEM_DP_AUXP 37
27 DPB_AUXN	C101 1 SCD1U10V2KX-4GP	SYSTEM_DP_AUXN 37
27 DPB_0P	C102 1 SCD1U10V2KX-4GP	SYSTEM_DP0P 37
27 DPB_0N	C104 1 SCD1U10V2KX-4GP	SYSTEM_DP0N 37
27 DPB_1P	C106 1 SCD1U10V2KX-4GP	SYSTEM_DP1P 37
27 DPB_1N	C108 1 SCD1U10V2KX-4GP	SYSTEM_DP1N 37
27 DPB_2P	C110 1 SCD1U10V2KX-4GP	SYSTEM_DP2P 37
27 DPB_2N	C112 1 SCD1U10V2KX-4GP	SYSTEM_DP2N 37
27 DPB_3P	C114 1 SCD1U10V2KX-4GP	SYSTEM_DP3P 37
27 DPB_3N	C116 1 SCD1U10V2KX-4GP	SYSTEM_DP3N 37

Docking DP Connector A

27 DPC_AUXP	C118 1 SCD1U10V2KX-4GP	DOCKA_DP_AUXP 59
27 DPC_AUXN	C119 1 SCD1U10V2KX-4GP	DOCKA_DP_AUXN 59
27 DPC_0P	C120 1 SCD1U10V2KX-4GP	DOCKA_DP0P 59
27 DPC_0N	C121 1 SCD1U10V2KX-4GP	DOCKA_DP0N 59
27 DPC_1P	C122 1 SCD1U10V2KX-4GP	DOCKA_DP1P 59
27 DPC_1N	C123 1 SCD1U10V2KX-4GP	DOCKA_DP1N 59
27 DPC_2P	C124 1 SCD1U10V2KX-4GP	DOCKA_DP2P 59
27 DPC_2N	C125 1 SCD1U10V2KX-4GP	DOCKA_DP2N 59
27 DPC_3P	C126 1 SCD1U10V2KX-4GP	DOCKA_DP3P 59
27 DPC_3N	C127 1 SCD1U10V2KX-4GP	DOCKA_DP3N 59

Place Near Docking Connector

Docking DP Connector B

27 DPD_AUXP	C128 1 SCD1U10V2KX-4GP	DOCKB_DP_AUXP 59
27 DPD_AUXN	C129 1 SCD1U10V2KX-4GP	DOCKB_DP_AUXN 59
27 DPD_0P	C130 1 SCD1U10V2KX-4GP	DOCKB_DP0P 59
27 DPD_0N	C131 1 SCD1U10V2KX-4GP	DOCKB_DP0N 59
27 DPD_1P	C132 1 SCD1U10V2KX-4GP	DOCKB_DP1P 59
27 DPD_1N	C133 1 SCD1U10V2KX-4GP	DOCKB_DP1N 59
27 DPD_2P	C134 1 SCD1U10V2KX-4GP	DOCKB_DP2P 59
27 DPD_2N	C135 1 SCD1U10V2KX-4GP	DOCKB_DP2N 59
27 DPD_3P	C136 1 SCD1U10V2KX-4GP	DOCKB_DP3P 59
27 DPD_3N	C137 1 SCD1U10V2KX-4GP	DOCKB_DP3N 59

Place Near Docking Connector

BLANK

VCC3B

2
R1347
0R0306-PAD-GP
1

VCC3B_HDD

VCC3B_HDD

DY

C289
SCD01U25V2KX-3GP
2

C290
SC10U10V5KX-2GP
2

SATA CONN

VCC5B_HDD

VCC5B

CN8

23	23	V33	P1
24	24	V33	P2
		V33	P3

R1348
2   1
0R0306-PAD-GP

		V5	P7
		V5	P8
		V5	P9

VCC5B_HDD

DY

C293
SCD01U25V2KX-3GP
2

C294
SC10U10V5KX-2GP
2

S1	GND	V12	P13
S4	GND	V12	P14
S7	GND	V12	P15
P4	GND		
P5	GND		
P6	GND	A+	S2
P10	GND	A-	S3
P12	GND		
		B+	S6
P11	DAS/DSS	B-	S5

PN11
PWRNC CN10
PWRNC CN9
PWRNC

70   -HDD_DTCT

SATA0_TXP   24
SATA0_TXN   24

SATAB+_CONN SCD01U16V2KX-3GP 1   2 C292
SATAB-_CONN SCD01U16V2KX-3GP 1   2 C291

SATA0_RXP   24
SATA0_RXN   24

SKT-SATA7P-15P-48-GP

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<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
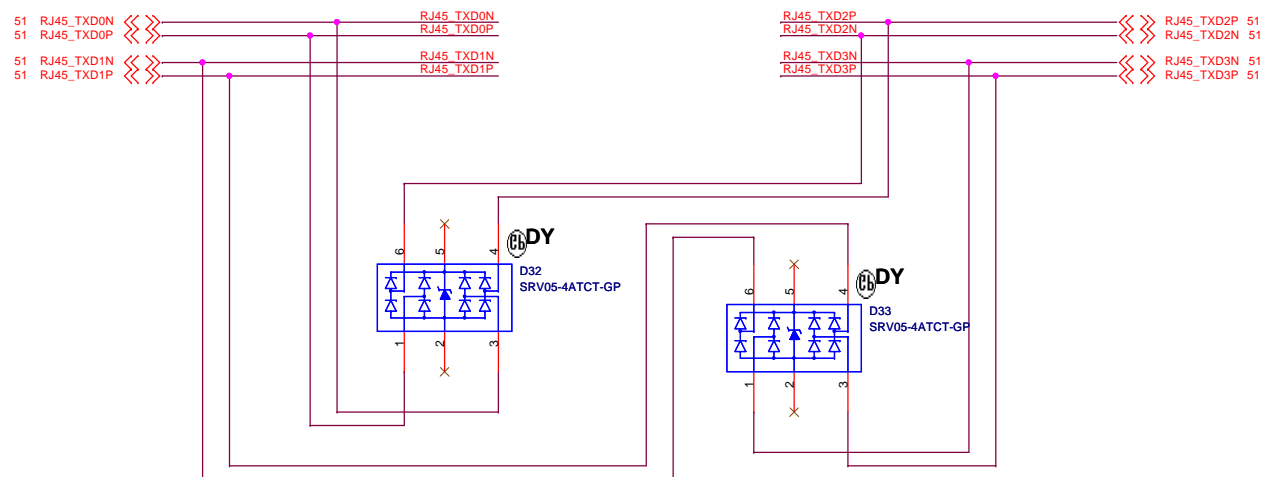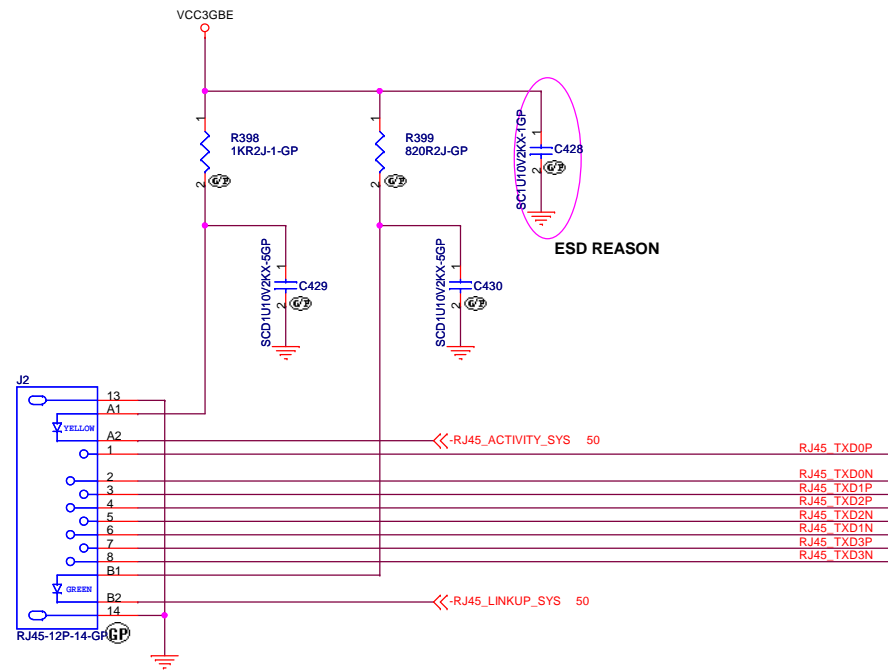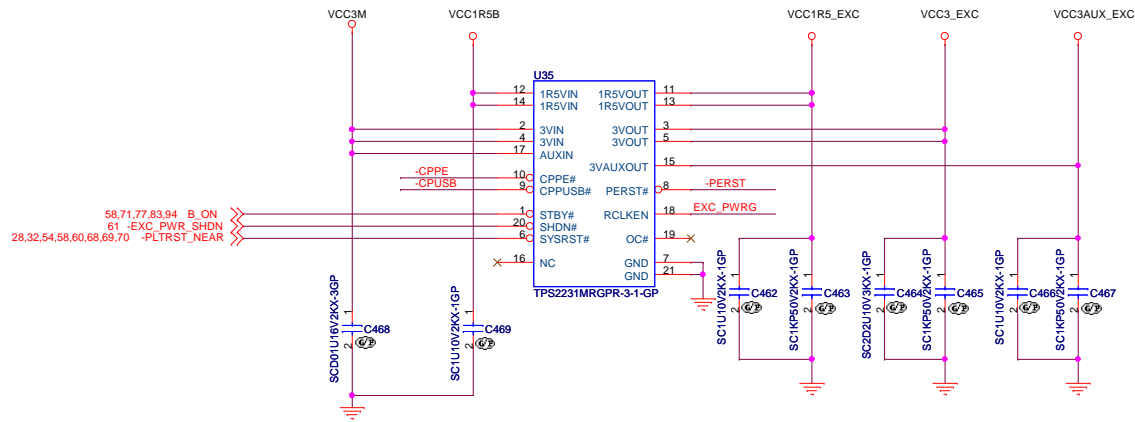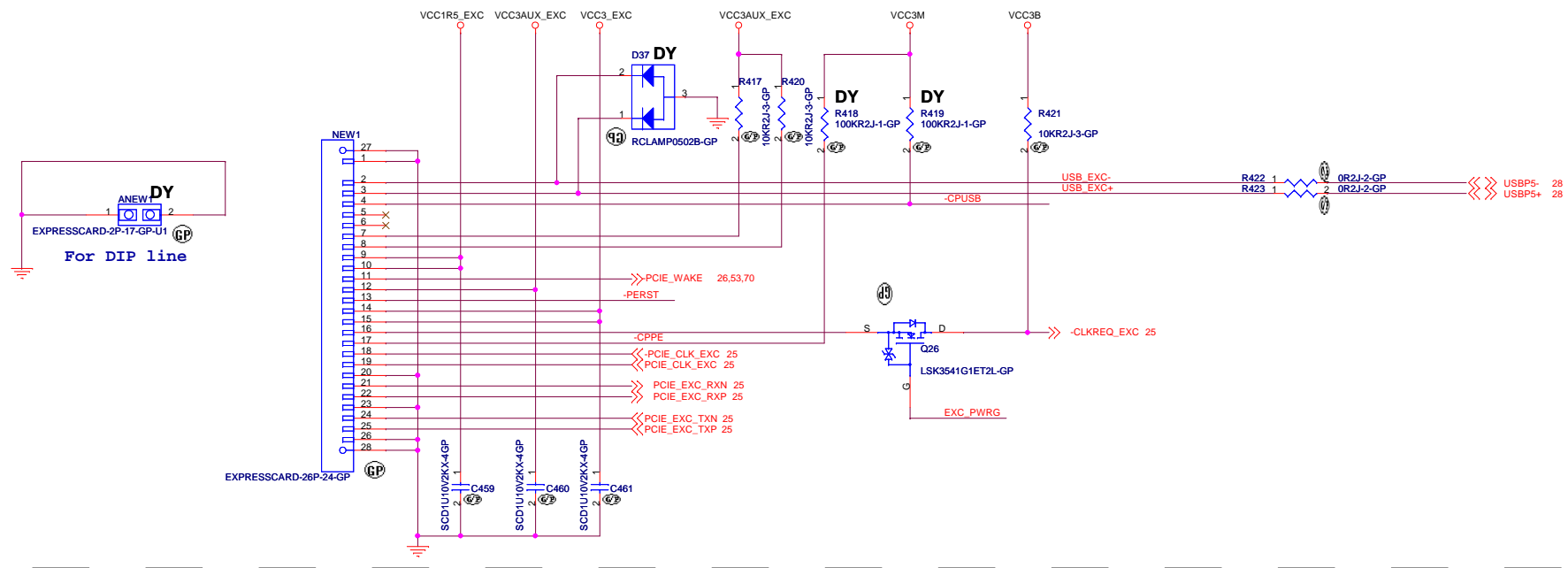Taipei Hsien 221, Taiwan, R.O.C.

Title
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Size	Document Number		Rev
A3		Dasher-2	-1

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USB3.0 port0

U23,U18

| USB3.0 Enable | TI TPS2069DGN | 74.02069.079 |
| | GMT G548A1F51U | 74.00548.A79 |

L35,L38,L41,L42

| 1st:TDK TCM1210S-500-2P | 68.01210.20E |
| 2nd:Murata DLP11SA350HL2L | 68.11350.201 |

Dual Layout

R152   0R2J-2-GP
L35 DY
TCM1210S-500-2P-T200-GP
R287   0R2J-2-GP

USB3P0_RXN 28
USB3P0_RXP 28

USBP0+_TVS   USBP0-_TVS
U76
AOZ8904CIL-GP
VCC5M

VCC5M
U23
EN   OC#
IN#3   OUT#6
IN#2   OUT#7
GND   OUT#8
        GND
TPS2069DGNR-GP
USB_ON1
-USB_PORT0_OC0 28
C949
SCD1U10V2KX-6GP

Should use non-discharge part.

USB_PWR0

WIDE PATTERN (MIN 500MA)
PLACE NEAR USB CONNECTOR

28 USBP0-   R973   0R3-PT5-LILY-GP   USBP0-_TVS
28 USBP0+   R974   0R3-PT5-LILY-GP   USBP0+_TVS

C306 C307 TC5
SCD1U10V2KX-4GP
ST160U6D3V8M-1-GP

SKT2
VBUS   SSRX-
        SSRX+
D-
D+   SSTX-
        SSTX+
GND
GND   PGND
GND   GND
SKT-USB13-9-GP-U

USB3P0_RXN_R
USB3P0_RXP_R
USB3P0_TXN_L_C
USB3P0_TXP_L_C

USB3.0 CONNECTOR

C225
SCD1U10V2KX-4GP
L38
TCM1210S-500-2P-T200-GP
C226
SCD1U10V2KX-4GP

USB3P0_TXN_C_1   USB3P0_TXN 28
USB3P0_TXP_C_1   USB3P0_TXP 28

U25
ESD3V3U4LC-GP
USB3P0_TXP_L_C   USB3P0_TXP_L_C
USB3P0_TXN_L_C   USB3P0_TXN_L_C
USB3P0_RXP_R   USB3P0_RXP_R
USB3P0_RXN_R   USB3P0_RXN_R

USB3.0 port1

Near J1
USB_PWR1   AFTP7   AFTE14P-GP
            AFTP10 AFTE14P-GP

VCC5M
U18
EN   OC#
IN#3   OUT#6
IN#2   OUT#7
GND   OUT#8
        GND
TPS2069DGNR-GP
USB_ON1
-USB_PORT1_OC1 28
C950
SCD1U10V2KX-6GP

Should use non-discharge part.

USB_PWR1

Dual Layout
R323   0R2J-2-GP
TCM1210S-500-2P-T200-GP
L41 DY
R324   0R2J-2-GP

USB3P1_RXN 28
USB3P1_RXP 28

USBP1+_TVS   USBP1-_TVS
U22
AOZ8904CIL-GP
VCC5M

28 USBP1-   R294   0R3-PT5-LILY-GP   USBP1-_TVS
28 USBP1+   R299   0R3-PT5-LILY-GP   USBP1+_TVS

C300 C301 C302
SCD1U10V2KX-4GP
ST160U6D3V8M-1-GP

J1
VBUS   SSRX-
        SSRX+
D-
D+   SSTX-
        SSTX+
GND
GND   PGND
GND   GND
SKT-USB13-36-GP-U

USB3P1_RXN_R
USB3P1_RXP_R
USB3P1_TXN_L_C
USB3P1_TXP_L_C

USB3.0 CONNECTOR

WIDE PATTERN (MIN 500MA)
PLACE NEAR USB CONNECTOR

C236
SCD1U10V2KX-4GP
L42
TCM1210S-500-2P-T200-GP
C296
SCD1U10V2KX-4GP

USB3P1_TXN_1   USB3P1_TXN 28
USB3P1_TXP_C_1 USB3P1_TXP 28

U77
ESD3V3U4ULC-GP
USB3P1_TXP_L_C   USB3P1_TXP_L_C
USB3P1_TXN_L_C   USB3P1_TXN_L_C
USB3P1_RXP_R   USB3P1_RXP_R
USB3P1_RXN_R   USB3P1_RXN_R

U25,U77

1st:Infineon ESD3V3U4ULC	83.3V3U4.0A0
2nd:NXP IP4294CZ10-TBR	83.04294.0A0
3rd:AMC AZ1045-04F DFN2510P 10E	83.01045.AA0

USB port9(Always On USB)

USBP9-   R295 DY 0R2J-2-GP   USBP9-_MUX
USBP9+   R296 DY 0R2J-2-GP   USBP9+_MUX

VCC5M
DY
C847 SC4D7U10V5KX-1-GP
C303 SCD1U10V2KX-4GP

USB_PWR9

WIDE PATTERN (MIN 500MA)
PLACE NEAR USB CONNECTOR

Near SKT3
USB_PWR9   AFTP84 AFTE14P-GP
USBP9-_TVS AFTP81 AFTE14P-GP
USBP9+_TVS AFTP82 AFTE14P-GP
            AFTP83 AFTE14P-GP

R292   0R3-PT5-LILY-GP
R293   0R3-PT5-LILY-GP

U20
IN   OUT
FAULT#   DM_IN
ILIM_SEL   DP_IN
NC#9   DM_OUT
DSC   DP_OUT
CTL1   ILIM0
CTL2   ILIM1
CTL3   GND
            GND
TPS2541RTER-GP
28 -USB_PORT9_OC5
USBP9- 28
USBP9+ 28
70 USB_ON2
70 AO_USB_SEL
R992
37K4R2F-1-GP

Should use discharge part.

C297 C298 C299
SCD1U10V2KX-4GP
SC4D7U10V5KX-3-GP
ST160U6D3V8M-1-GP

SKT3
SKT-USB8-53-GP

Always on port

USBP9-_TVS
USBP9+_TVS

C299, C303,TC5
| 1st NEC Tokin TEPSLB20J157M | 77.C1571.09L |
| 2nd SANYO 6TPE150MAZB | 77.21571.111 |

Always On USB Part List (U20)

	~SDV	FVT~
TI	TPS2541RTER (74.02541.073)	TPS2541RTER(74.02541.073)
		TPS2541A(74.02541.A73)

U76,U22,U19

| 1st:AOS AOZ8904CIL | 83.08904.0AE |
| 2nd:Little Fuse SP3002-04HTG | 84.03200.0AF |

USBP9+_TVS   USBP9-_TVS
U19
AOZ8904CIL-GP
VCC5M

Bluetooth Module

VCC3M
F10
POLYSW-D5A6V-1-GP

CN9
HRS-CONN14D-GP-U1
AFTP89 AFTE14P-GP
VCC3BT_C
USBP11+ 28
USBP11- 28
28 -BDC_PRESENCE
53,61 BDC_ON
34 LED_BDC_IN
AFTP88
AFTE14P-GP
C305
SC2D2U6D3V3MX-1-GP

Near CN9
-BDC_PRESENCE   AFTP85 AFTE14P-GP
BDC_ON   AFTP87 AFTE14P-GP
VCC3BT_C   AFTP91 AFTE14P-GP
                 AFTP90 AFTE14P-GP
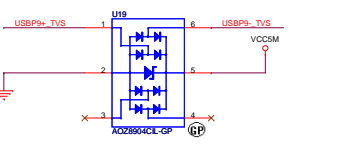
PSIT Config.

	PSIT-1	PSIT-2	PSIT-3	PSIT-4	PSIT-5	PSIT-6
L35,L41	No ASM	No ASM	No ASM	No ASM	No ASM	No ASM
L38,L42	TDK	No ASM	No ASM	TDK	No ASM	Murata
U25,U77	Infineon	NXP	AMC	NXP	Infineon	AMC
R152,287,323,324	ASM	ASM	ASM	ASM	ASM	ASM
R320,321,325,328	No ASM	ASM	ASM	No ASM	ASM	No ASM

AUDIO ALC3202

Dasher-2

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

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Sheet 43 of 101
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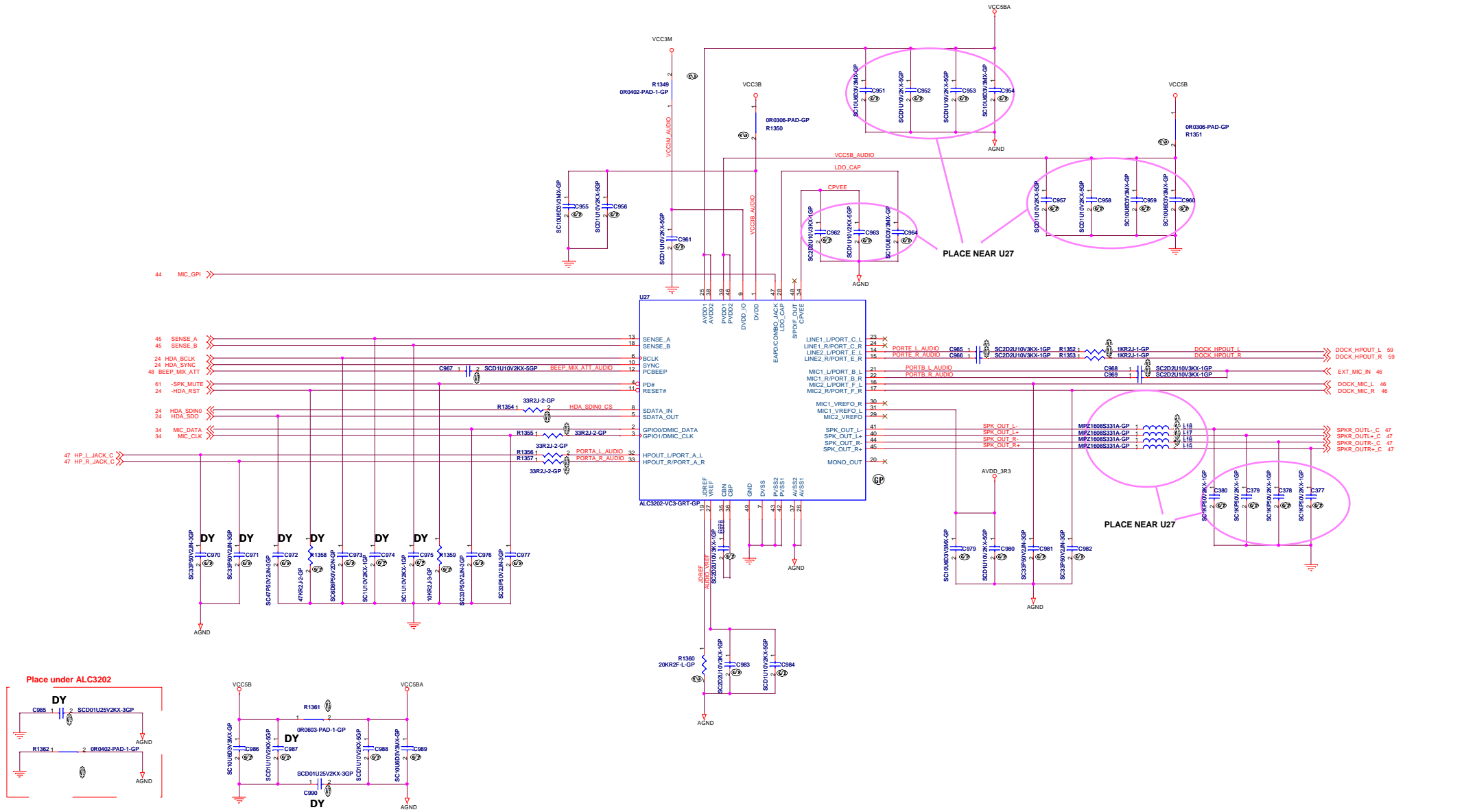
NEAR HEADPHONE CONN

WIDE PATTERN

AFTP16
AFTE14P-GP

47 HP_L_JACK
L19 1 MMZ1005Y152CT-GP

47 HP_R_JACK
L20 1 MMZ1005Y152CT-GP

AFTP17
AFTE14P-GP

DY   DY

R349   R350
220R2J-L2-GP 220R2J-L2-GP

C390   C391
SC1KP50V2KX-1GP   SC1KP50V2KX-1GP

AGND

WIDE AND SHORT PATTERN

SC1KP50V2KX-1GP
C389

AGND

R1363 1 2 22KR2J-GP   MIC_GPI 43

SC10U6D3V3MX-GP
C392

AGND

VCC3_BRG

10KR2J-3-GP
R352 1
AFTP18
AFTE14P-GP

AFTP20
AFTE14P-GP

D24
2

45 HP_JACK_DOCK
1
DAN222GTL-GP
3   HP_JACK_IN 61

R353
470KR2J-2-GP

JK1
3 AGND
1 HP_L_JACK_JK1
5
2 HP_R_JACK_JK1
6 HP_JACK_SYS
7
4 MIC_JACK_2

AFTP22
AFTE14P-GP

HP_JACK_SYS 45

MIC_JACK_2 47

PHONE-JK354-GP-U

AFTP19
AFTE14P-GP

D25   D26   D27
RSB5D6SMT2R-GP RSB5D6SMT2R-GP RSB5D6SMT2R-GP

DY   DY   DY

R354
100KR2J-1-GP

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title
Audio Connector

Size Document Number   Rev
A3   -1
      Dasher-2

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R355 1 2 39K2R2F-L-GP SENSE_A 43

44 HP_JACK_SYS R356 1 2 22KR2J-GP G LSK3541G1ET2L-GP Q13 D S AGND

VCC3B VCC3B

R360 10KR2J-3-GP R361 100KR2J-1-GP

59 -DOCK_MICIN_DTCT Q16 DY R1 R2 LTC015EEB-FS8-GP C395 SC1KP50V2KX-1GP

DOCK_MIC_EN R362 1 2 22KR2J-GP G LSK3541G1ET2L-GP Q15 D S AGND

R359 1 2 20KR2F-L-GP SENSE_B 43

R363 1 2 39K2R2F-L-GP

VCC3B VCC3B

R364 10KR2J-3-GP R365 100KR2J-1-GP

59 -DOCK_HPOUT_DTCT Q18 DY R1 R2 LTC015EEB-FS8-GP C396 SC1KP50V2KX-1GP

R366 1 2 22KR2J-GP G LSK3541G1ET2L-GP Q17 D S AGND

HP_JACK_DOCK 44

AVDD_3R3

SC1U10V2IKX-1GP

C991

R370
2K2R2J-2-GP

L21
BK1608HS102-T-GP

L22
MMZ1005Y152CT-GP

47 MIC_JACK_3

R372 1KR2J-1-GP

EXT_MIC_IN 43

C400
SC100P50V2JN-3GP

R373
470KR2J-2-GP

AGND

NEAR EXT MIC CONN

C992 1 2 SCD01U25V2KX-3GP

AGND

C993 1 2 SCD01U25V2KX-3GP

AGND

R1365 1 2 0R2J-2-GP

DY

AGND

59 DOCK_MIC_IN_L

R406 2K2R2J-2-GP

C809 SC2D2U10V3KX-1GP

DOCK_MIC_L 43

C810 SC2D2U10V3KX-1GP

DOCK_MIC_R 43

R997
270R2J-L-GP

VCC5_BRG

CN10
ACES-CON4-29-GP

SPKR_OUTL+ 1 5
SPKR_OUTL- 2
SPKR_OUTR- 3
SPKR_OUTR+ 4 6

C401 SC470P50V2KX-3GP
C402 SC470P50V2KX-3GP
C403 SC470P50V2KX-3GP
C404 SC470P50V2KX-3GP

Near CN10
SPKR_OUTL+ AFTP92 AFTE14P-GP
SPKR_OUTL- AFTP93 AFTE14P-GP
SPKR_OUTR- AFTP94 AFTE14P-GP
SPKR_OUTR+ AFTP95 AFTE14P-GP

U78 DY
TS5A22364DGSR-1-GP
1 V+   NO2 10
2 NO1 COM2 9
3 COM1 NC#8 8
4 NC#4 IN2 7
5 IN1 GND 6

53 SPKR_OUTL+_L
43 SPKR_OUTL+_C
SPKR_OUTL+
R1366 0R2-PT5-LILY-GP
SPKR_OUTL-
R1367 0R2-PT5-LILY-GP
SPKR_OUTL-_L 53
SPKR_OUTL-_C 43
DUP_SW_AUDIO 34,70

U79 DY
TS5A22364DGSR-1-GP
1 V+   NO2 10
2 NO1 COM2 9
3 COM1 NC#8 8
4 NC#4 IN2 7
5 IN1 GND 6

53 SPKR_OUTR+_L
43 SPKR_OUTR+_C
SPKR_OUTR+
R1368 0R2-PT5-LILY-GP
SPKR_OUTR-
R1369 0R2-PT5-LILY-GP
SPKR_OUTR-_L 53
SPKR_OUTR-_C 43
DUP_SW_AUDIO 34,70

C994 SCD01U25V2KX-3GP DY

VCC3_BRG

U80 DY
TS5A22364DGSR-1-GP
1 V+   NO2 10
2 NO1 COM2 9
3 COM1 NC#8 8
4 NC#4 IN2 7
5 IN1 GND 6

53 HP_R_JACK_L
44 HP_R_JACK
43 HP_R_JACK_C
R1370 0R2-PT5-LILY-GP
R1371 0R2-PT5-LILY-GP
HP_L_JACK_L 53
HP_L_JACK 44
HP_L_JACK_C 43
DUP_SW_AUDIO 34,70

C995 SCD01U25V2KX-3GP DY

VCC5_BRG

U81 DY
TS5A22364DGSR-1-GP
1 V+   NO2 10
2 NO1 COM2 9
3 COM1 NC#8 8
4 NC#4 IN2 7
5 IN1 GND 6

R1372 0R2-PT5-LILY-GP
MIC_L 53
MIC_JACK_2 44
MIC_JACK_3 46
DUP_SW_AUDIO 34,70

C19 SCD01U25V2KX-3GP DY

<Core Design>

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **AUDIO SPEAKER**
Size A3
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61 EC_SPKR

D28
A K
RB521SM-30T2R-GP

24 PCH_SPKR

RB521SM-30T2R-GP
D29
A K
RB521SM-30T2R-GP

R374
33KR2J-3-GP

BEEP_MIX_ATT 43

R375
10KR2J-3-GP

C304
SC100P50V2JN-3GP

VCC3B

R376
10KR2J-3-GP

61 -BEEP_ENABLE

D30
A K

RB521SM-30T2R-GP
D31
A K
RB521SM-30T2R-GP

MUTE

LSK3541G1ET2L-GP

G

Q21

R377
100KR2J-1-GP

R378
27KR2J-L1-GP

61 -MUTE

1 2

G

Q22
LSK3541G1ET2L-GP

<Core Design>

Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

Title
Audio BEEP

Size
A4

Document Number
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AMT	YES	NO
U30	82579LM	82579V

DY

DY

L23

L23			
1st	TDK	FLF3215T-4R7M	68.4R71D.10Q
2nd	Murata	LQH32PN4R7NN0	68.4R750.20H
3rd	Taiyo Yuden	BRL3225T4R7M	68.4R71E.10I

X4

X4			
1st	KDS	DSX321G 25M 18P 30PPM	82.30020.B11
2nd	H.ELE	HSX321S 25M 18P 30PPM	82.30020.B21
3rd	TXC	7V25020001 25M 18P 30PPM	41U6141AA

<Core Design>

緯創資通　**Wistron Corporation**
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Title
GBE Lewisville

Size A3

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VCC3GBE VCC3GBE

R392
4K7R2J-2-GP

-DOCK_ATTACHED_AUX 59

C416 C417 C418 C419
SCD1U10V2KX-4GP SCD1U10V2KX-4GP SCD1U10V2KX-4GP SCD1U10V2KX-4GP

U31

Pin	Signal		Pin	Signal
27	VDD			
4	VDD		17	SEL
10	VDD		5	NC#5
18	VDD			
38	VDD			
56	VDD			
50	VDD			

49	MDI_3-	2	A0	48	0B1	DOCK_MDI_3-	59
49	MDI_3+	3	A1	46	0B2	SYS_MDI_3-	51
49	MDI_2-	7	A2	47	1B1	DOCK_MDI_3+	59
49	MDI_2+	8	A3	45	1B2	SYS_MDI_3+	51
49	MDI_1-	11	A4	43	2B1	DOCK_MDI_2-	59
49	MDI_1+	12	A5	41	2B2	SYS_MDI_2-	51
49	MDI_0-	14	A6	42	3B1	DOCK_MDI_2+	59
49	MDI_0+	15	A7	40	3B2	SYS_MDI_2+	51
				37	4B1	DOCK_MDI_1-	59
49	-RJ45_ACTIVITY	19	LED0	35	4B2	SYS_MDI_1-	51
49,61	-RJ45_LINKUP	20	LED1	36	5B1	DOCK_MDI_1+	59
		54	LED2	34	5B2	SYS_MDI_1+	51
				32	6B1	DOCK_MDI_0-	59
59	-RJ45_ACTIVITY_DOCK	22	0LED1	30	6B2	SYS_MDI_0-	51
52	-RJ45_ACTIVITY_SYS	25	0LED2	31	7B1	DOCK_MDI_0+	59
59	-RJ45_LINKUP_DOCK	23	1LED1	29	7B2	SYS_MDI_0+	51
52	-RJ45_LINKUP_SYS	26	1LED2				
		52	2LED1				
		51	2LED2				

THERMAL_PAD

GND 21 24 28 1 6 9 13 16 44 39 33 55 53 49 57

PI3L500-AZFEX-GP

		Vendor P/N	Wistron P/N
1st	Pericom	PI3L500AZFEX	73.3L500.003
2nd	TI	TS3L500AERHUR	73.3L500.A0V
3rd	ST	STMUX1800LQTR	73.01800.003

<Core Design>

緯創資通 **Wistron Corporation**
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Taipei Hsien 221, Taiwan, R.O.C.

Title
GBE LAN Switch

Size A4

Document Number
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XF1
XFORM-24P-72-GP

Pin	Signal	Pin	Signal
1	TD+#1	24	TX+#24
2	TD-#2	23	TX-#23
5	TD+#5	20	TX+#20
6	TD-#6	19	TX-#19
7	TD+#7	18	TX+#18
8	TD-#8	17	TX-#17
11	TD+#11	14	TX+#14
12	TD-#12	13	TX-#13
3	TDCT#3	16	TXCT#16
4	TDCT#4	15	TXCT#15
9	TDCT#9	21	TXCT#21
10	TDCT#10	22	TXCT#22

50 SYS_MDI_0+
50 SYS_MDI_0-
50 SYS_MDI_1+
50 SYS_MDI_1-
50 SYS_MDI_2+
50 SYS_MDI_2-
50 SYS_MDI_3+
50 SYS_MDI_3-

RJ45_TXD0P 52
RJ45_TXD0N 52
RJ45_TXD1P 52
RJ45_TXD1N 52
RJ45_TXD2P 52
RJ45_TXD2N 52
RJ45_TXD3P 52
RJ45_TXD3N 52

TDCT_GBE

C420
SC1U10V2KX-1GP

TXCT16_GBE
TXCT15_GBE
TXCT21_GBE
TXCT22_GBE

C420 AND 0OHM RESISTOR
SHOULD BE PLACED AS
CLOSE TO MAGENTICS
AS POSSIBLE

C421 C422 C423 C424 C425 C426
SCD1U10V2KX-5GP

ESD REASON

PATTERN MUST BE
SHORT AND WIDE

R393 R394 R395 R396
75R5J-GP

R397
1MR6F-GP

C427
SC1500P2KV8KX-3GP

HIGH VOLTAGE 1500PF
CAP IS OPTIONAL

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
GBE MAGNETICS

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VCC3GBE

R398
1KR2J-1-GP

R399
820R2J-GP

C428
SCT1U10V2KX-1GP

ESD REASON

C429
SCD1U10V2KX-5GP

C430
SCD1U10V2KX-5GP

J2
13
A1
A2
YELLOW
1
2
3
4
5
6
7
8
B1
B2
14
GREEN
RJ45-12P-14-GP

-RJ45_ACTIVITY_SYS   50

RJ45_TXD0P
RJ45_TXD0N
RJ45_TXD1P
RJ45_TXD2P
RJ45_TXD2N
RJ45_TXD1N
RJ45_TXD3P
RJ45_TXD3N

-RJ45_LINKUP_SYS   50

51 RJ45_TXD0N
51 RJ45_TXD0P

51 RJ45_TXD1N
51 RJ45_TXD1P

RJ45_TXD0N
RJ45_TXD0P

RJ45_TXD1N
RJ45_TXD1P

RJ45_TXD2P
RJ45_TXD2N

RJ45_TXD3N
RJ45_TXD3P

RJ45_TXD2P 51
RJ45_TXD2N 51

RJ45_TXD3N 51
RJ45_TXD3P 51

D32
SRV05-4ATCT-GP

D33
SRV05-4ATCT-GP

<Core Design>

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緯創資通

Title
RJ45 CONNECTOR

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WWAN On/Off CONN

VCC3M
VCC3WAN VCC1R5B

SW1
AFTP102
AFTE14P-GP
R400
100KR2J-1-GP
R402
0R0306-PAD-GP

NP1
NP2
DY
C432
SCD1U10V2KX-4GP

GLOBAL_WL_DISABLE 61

SW-SLIDE3P-14-GP

AFTP103
AFTE14P-GP

C431
SC1U10V2KX-1GP
RFC22
SC47P50V2JN-3GP
DY

CN11

SIM Card slot
C437
SC4D7U10V5KX-1GP

RFC21
SC47P50V2JN-3GP
C433
SC1U10V2KX-1GP
C434
SC1U10V2KX-1GP
C435
SC1U10V2KX-1GP

1.5V   REFCLK+ 13   SPKR_OUTR+_L 47
3.3V   REFCLK- 11   SPKR_OUTR-_L 47
+1.5V   PERN0 23   SATA2_RXP 24
+1.5V   PERP0 25   SATA2_RXN 24
+3.3V   PETN0 31   SATA2_TXN 24
            PETP0 33   SATA2_TXP 24
+3.3VAUX   USB_D- 36   USB_WWAN-
            USB_D+ 38   USB_WWAN+
            SMB_CLK 30   WANSCL_LPMTXD 69
            SMB_DATA 32   WANSDA_LPMRXD 69

AFTP101
AFTE14P-GP

SKT5
RST VCC
GND VPP
GND
GND CLK
GND DATA

C996
SC1U10V2KX-1GP

RESERVED#3
RESERVED#5
RESERVED#8
RESERVED#10
RESERVED#12
RESERVED#14
RESERVED#16
RESERVED#17
RESERVED#19
RESERVED#20
RESERVED#37
RESERVED#39
RESERVED#41
RESERVED#43
RESERVED#45
RESERVED#47
RESERVED#49
RESERVED#51

WAKE#   MIC_L 47
CLKREQ#   -SE_DET 62,69
PERST#   -PLTRST_FAR 4,11,28,49

R1373
0R2-PT5-LILY-GP

VCC5_HY

UIM_PWR
UIM_DATA
UIM_CLK
UIM_RESET
UIM_VPP

UIM_CLK_R R16 200R2F-L-GP

SKT-SIMM6-9-GP-U1

Near SKT5

UIM_PWR
UIM_DATA
UIM_CLK_R
UIM_RESET
UIM_VPP

AFTP96 AFTE14P-GP
AFTP97 AFTE14P-GP
AFTP98 AFTE14P-GP
AFTP99 AFTE14P-GP
AFTP100 AFTE14P-GP

D35
FTZ6D8EGT148-GP

61 -WWAN_DISABLE

29 -WWAN_DTCT
47 SPKR_OUTL-_L
47 SPKR_OUTL+_L

R464
0R2-PT5-LILY-GP

GND
GND
GND
GND
GND
GND
GND
GND
GND
GND
GND
GND
GND
GND

D34
RCLAMP0502B-GP
DY

R1374
0R2J-2-GP

34,70 DUP_SW_Camera

U82
DY
OE# VCC
S
D+   1D+
D-   1D-
          2D+
GND   2D-

TS3USB221ERSER-GP

VCC3_BRG

-SE_RST 62

C997
SC1U10V2KX-1GP
DY

VCC3WAN

Q24
PDTC115TE-GP

-LED_WIRELESS 34
LED_WWAN_CONN

47 HP_L_JACK_L
47 HP_R_JACK_L

LED_WWAN#
LED_WLAN#
LED_WPAN#

NP1
NP2

70 MSATA_DTCT_EN   R457 33KR2J-3-GP
29 -MSATA_DTCT

TYCO-CONN52-GP
20.80966.052

USBP3- 28
USBP3+ 28

USBP_DUP+ 34
USBP_DUP- 34

CN11 and CN12
1st: 20.80966.052(TYCO)
2nd: 62.10043.751(ARGOSY)

VCC3WLAN VCC1R5B

R407
0R0306-PAD-GP

RFC24
SC47P50V2JN-3GP
DY

C438
SC1U10V2KX-1GP

CN12

RFC33
SC56P50V2JN-2GP
RFC23
SC47P50V2JN-3GP
DY
C439
SC1U10V2KX-1GP
DY
C440
SCD1U10V2KX-5GP
C441
SCD1U10V2KX-5GP

RFC32
SC56P50V2JN-2GP

1.5V   REFCLK+ 13   PCIE_CLK_WLAN 25
3.3V   REFCLK- 11   -PCIE_CLK_WLAN 25
+1.5V   PERN0 23   PCIE_WLAN_RXN 25
+1.5V   PERP0 25   PCIE_WLAN_RXP 25
+3.3V   PETN0 31   PCIE_WLAN_TXN 25
            PETP0 33   PCIE_WLAN_TXP 25
+3.3VAUX   USB_D- 36   USBP12- 28
            USB_D+ 38   USBP12+ 28
            SMB_CLK 30
            SMB_DATA 32

C442
SC1U10V2KX-1GP

VCC3B
R408
10KR2J-3-GP

RESERVED#3
RESERVED#5
RESERVED#8
RESERVED#10
RESERVED#12
RESERVED#14
RESERVED#16
RESERVED#17
RESERVED#19
RESERVED#20
RESERVED#37
RESERVED#39
RESERVED#41
RESERVED#43
RESERVED#45
RESERVED#47
RESERVED#49
RESERVED#51

WAKE#   -PCIE_WAKE 26,56,70
CLKREQ#   -CLKREQ_WLAN 25
PERST#   -PLTRST_FAR 4,11,28,49

61 -WLAN_RF_KILL

VCC3WLAN

Q25
PDTC115TE-GP

LED_WLAN_CONN

25 CL_CLK_WLAN
25 CL_DATA_WLAN
25 -CL_RST_WLAN
42,61 BDC_ON

GND
GND
GND
GND
GND
GND
GND
GND
GND
GND
GND
GND
GND
GND

D36
RCLAMP0502B-GP
DY

LED_WWAN#
LED_WLAN#
LED_WPAN#

NP1
NP2

TYCO-CONN52-GP
20.80966.052

<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
MINI CARD SLOT
Size A3
Document Number
Dasher-2
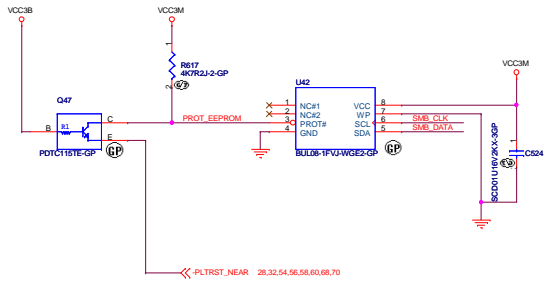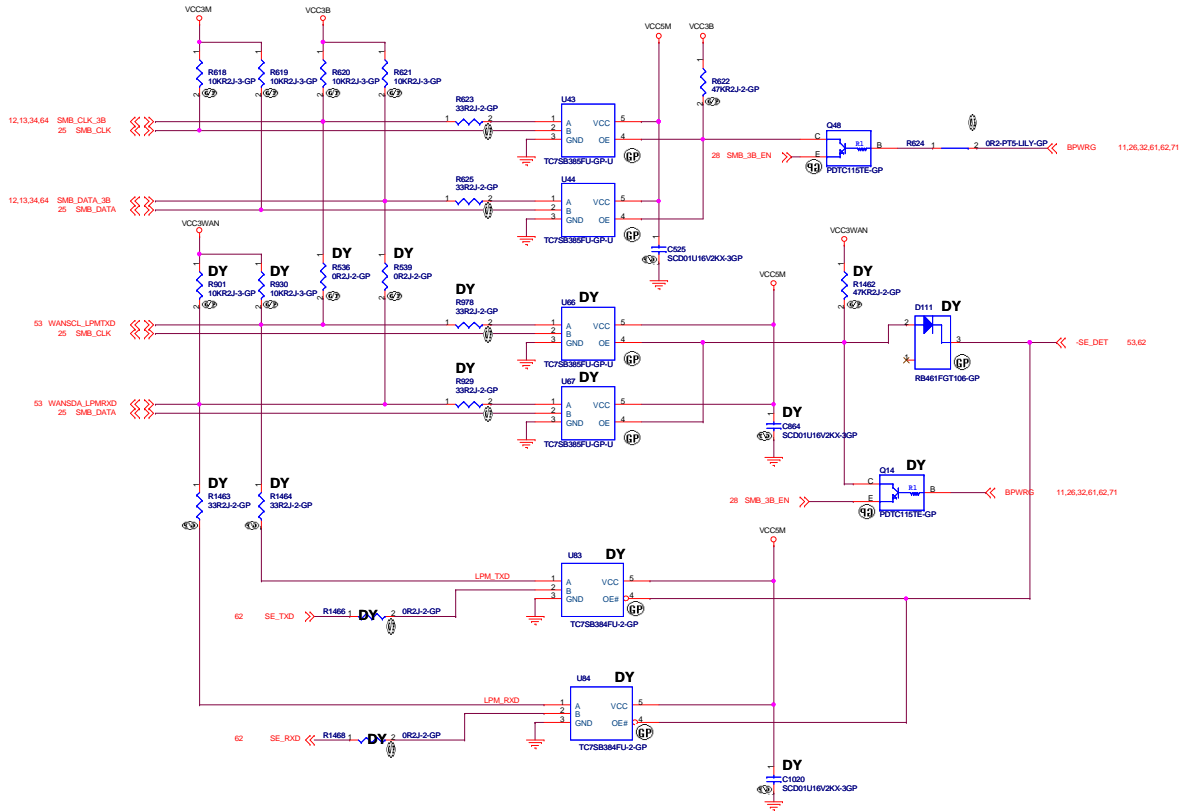Rev -1
Date: Friday, March 09, 2012   Sheet 53 of 101

MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MSD0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12			XD_CE#
MFIO13		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_R/B
MFCD0#	SDDC#		XDCD0#
MFCD1#		MSINS#	XDCD1#

U33

R5U220-QFN48P-2-GP

PU is placed on PCH page.

VCC3_MC

AFTP113
AFTE14P-GP
1

ST47U6D3VAM-GP
TC10

SCD01U25V2KX-3GP
C458

VCC3_MC

DY
R416
10KR2J-3-GP

SKT6

Pin	Signal		Signal	Pin
4	VDD		CMD	2
			CLK	5
			CD	10
7	DAT0		WP	12
8	DAT1		EMPTY	14
9	DAT2			
1	CD/DAT3		VSS	3
			VSS	6
NP1	NP1		GND	13
NP2	NP2		CD/WP/GND	11

MFIO08   54
SD_MCC_CLK   54
-SD_DET   54
MFIO00   54

SC15P50V2JN-2-GP
EMC5

AFTP112
AFTE14P-GP
1

CARD-PUSH-14P-1-GP

SD SLOT

54   MFIO02
54   MFIO01
54   MFIO11
54   MFIO10

SC15P50V2JN-2-GP
EMC1

SC15P50V2JN-2-GP
EMC2

SC15P50V2JN-2-GP
EMC3

SC15P50V2JN-2-GP
EMC4

Near SKT6

Signal		Part	
MFIO02	1	AFTP104	AFTE14P-GP
MFIO01	1	AFTP105	AFTE14P-GP
MFIO11	1	AFTP106	AFTE14P-GP
MFIO10	1	AFTP107	AFTE14P-GP
MFIO08	1	AFTP108	AFTE14P-GP
SD_MCC_CLK	1	AFTP109	AFTE14P-GP
-SD_DET	1	AFTP110	AFTE14P-GP
MFIO00	1	AFTP111	AFTE14P-GP

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
Media Card Slot

Size A4	Document Number *Dasher-2*	Rev -1

U35	TI	TPS2231MRGP-3	45K0234BA
	Rohm	BD4156MUV-GTR	74.04156.A73
	Rohm	BD4157MUV-GE2	74.04157.A73

For DIP line

VCC1R5_EXC VCC3AUX_EXC VCC3_EXC VCC3AUX_EXC VCC3M VCC3B

USB_EXC- USBP5- 28
USB_EXC+ USBP5+ 28

-CPUSB

PCIE_WAKE 26,53,70
-PERST
-CPPE
-CLKREQ_EXC 25
-PCIE_CLK_EXC 25
PCIE_CLK_EXC 25
PCIE_EXC_RXN 25
PCIE_EXC_RXP 25
PCIE_EXC_TXN 25
PCIE_EXC_TXP 25

EXC_PWRG

VCC3M VCC1R5B VCC1R5_EXC VCC3_EXC VCC3AUX_EXC

U35 TPS2231MRGPR-3-1-GP

58,71,77,83,94 B_ON
61 -EXC_PWR_SHDN
28,32,54,58,60,68,69,70 -PLTRST_NEAR

BLANK

Debug card connector

Golden Finger for Debug Board

Put "easy-to -access" place
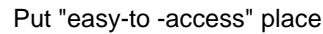
Debug	Enable	Disable
CN14	ASM	DY

4MB
SO8	Marcronix	MX25L3206EM2I-12G	72.25320.C01
	Winbond	W25Q032BVSSIG	72.25Q32.A01
	Eon	EN25QH32-104HIP	72.02532.B01
	Numonyx	N25Q032A13ESEC0	72.25Q32.C01

8MB
SO8	Marcronix	MX25L6406EM2I-12G	72.25640.D01
	Winbond	W25Q064CVSSIG	72.25Q64.B01
	Eon	EN25QH64-104HIP	72.02564.001
	Numonyx	N25Q064A13ESEC0	72.25Q64.G01
	Winbond	W25Q64FVSSIG	72.25Q64.F01

SO8 and WSON8 are both supported!

Dual Layout

GF1

	Yes	No
R874	ASM	DY
R823	ASM	DY
R834	ASM	DY
R835	ASM	DY
R235	ASM	DY

Logic

(BOTTOM VIEW)
TOP VIEW

(26) (25)···(15) (14)

13 12 ··· 2 1

Configuration Table

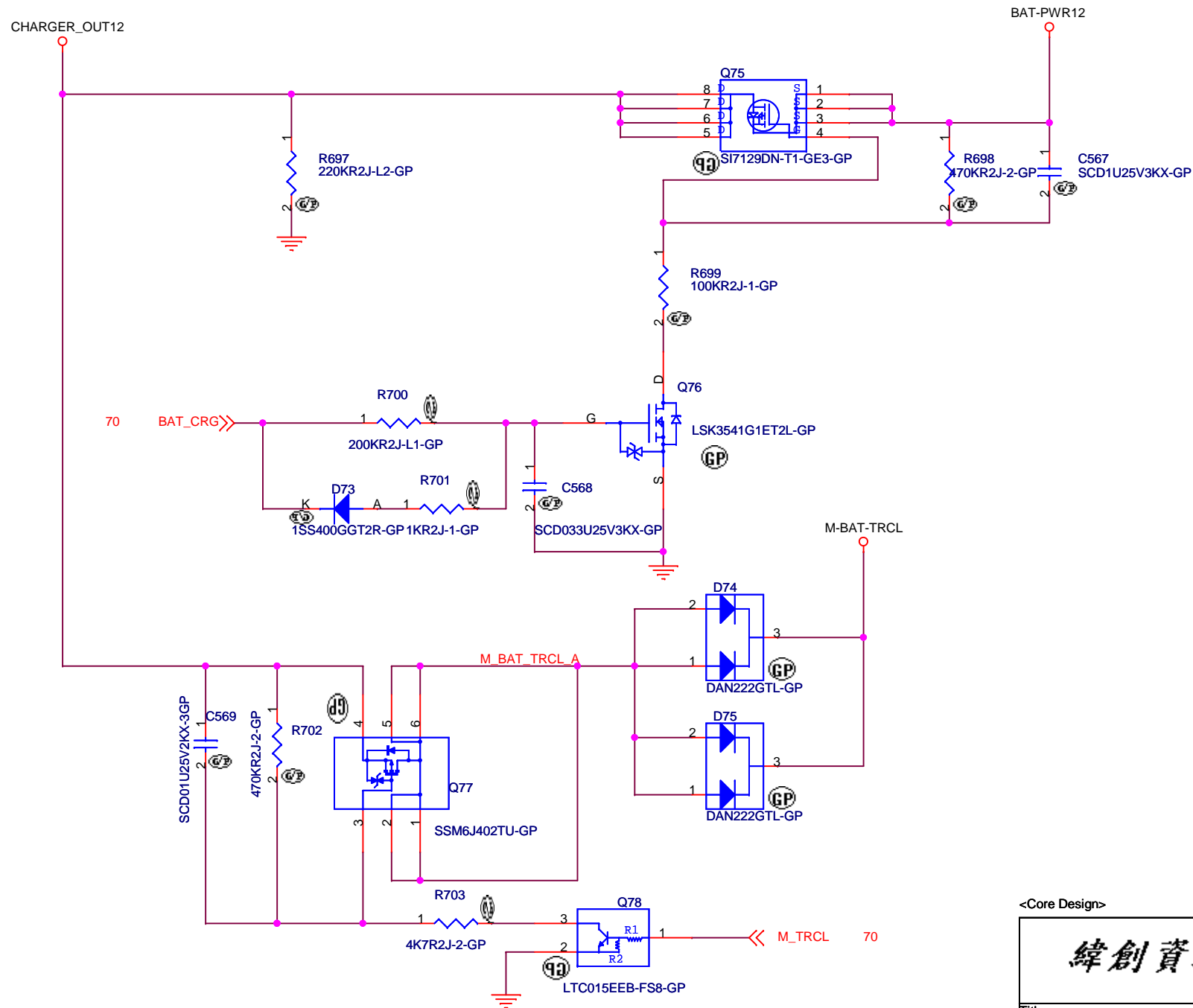|	CONFIG-1 16MB	CONFIG-2 12MB	CONFIG-3 8MB
SPI1	16MB	8MB	8MB
SPI2	DY	4MB	DY
R429	DY	ASM	DY
R431	DY	ASM	DY
R433	DY	ASM	DY
R1375	DY	ASM	DY
R1376	DY	ASM	DY
C471	DY	ASM	DY
R149(P.24)	DY	ASM	DY
R122	DY	ASM	DY

Logic

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **SPI&LPC debug card**

Size A3

Document Number: **Dasher-2**

Rev: **-1**

Date: Friday, March 09, 2012   Sheet 58 of 101

DOCKING CONNECTOR

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

緯創資通

<Core Design>

Title: DOCKING CONNECTOR

Size: A2
Document Number: Dasher-2
Rev: -1

Date: Friday, March 09, 2012
Sheet 59 of 101

Embedded Controller (2/3)

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

緯創資通

Document Number: Dasher-2
Size: A2
Rev: -1
Date: Friday, March 09, 2012
Sheet 61 of 101

Keyboard Connector

Keyboard Connector Top View

```
HOTKEY#        IPDCLK

DRV4           to R521
```

Near CN17
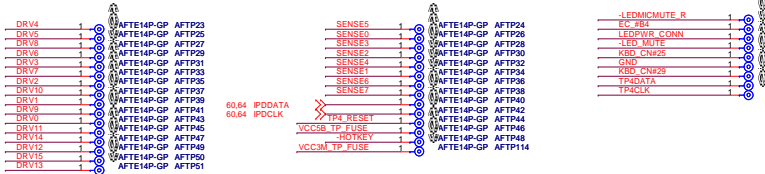
KBD Table1

		case1	case2	case3	case4
CS12 KBD w/o BL		v	v	v	
CS12 KBD w BL			v	v	v
CS09					v

Parts#	parts	case1	case2	case3	case4
R108	10k_5%_0402	DY	DY	ASM	ASM
R104	0_5%_0402	ASM	ASM	DY	DY
R89	100k_5%_0402	ASM	ASM	ASM	ASM
R105	1k_5%_0402	ASM	DY	ASM	ASM
R111	10k_5%_0402	ASM	ASM	ASM	ASM
R110	10k_5%_0402	DY	DY	ASM	ASM
R92	0_5%_0402	DY	DY	ASM	ASM
R91	390_5%_0402	DY	ASM	ASM	ASM
R107	0_5%_0402	DY	ASM	DY	DY
R103	0_5%_0402	ASM	ASM	DY	DY
R106	0_5%_0402	DY	ASM	ASM	ASM
R112	0_5%_0402	DY	ASM	ASM	DY
R109	100k_5%_0402	DY	ASM	ASM	DY
R191	0_5%_0402	ASM	ASM	DY	ASM
R217	0_5%_0402	DY	DY	ASM	DY
R300	0_5%_0402	DY	DY	ASM	ASM
C16	47uF_6.3V_0805	DY	DY	DY	DY
C15	10uF_6.3V_0603	DY	ASM	ASM	DY
C14	10uF_6.3V_0603	DY	ASM	ASM	DY
Q1	LTC015TE	DY	DY	ASM	ASM
Q8	LTA015TE	DY	DY	ASM	DY
Q7	RW1E015RP	DY	DY	ASM	DY
F5	1.5A/6A Fuse	DY	DY	DY	DY
	1.5A/6V PolySW	DY	ASM	ASM	DY
R314	100K_5%_0402	DY	ASM	DY	DY

KBD Table2

Pre-DV	case4
SDV	case2
FVT	case2
PreSIT	case2
SIT	case2

← Logic

Fingerprint Reader / Touch PAD

BLANK

Title		
BLANK		

Size A	Document Number	Rev
	Dasher-2	**-1**

| Date: | Tuesday, February 21, 2012 | Sheet | 65 | of | 101 |

FAN Control Schematic

For SV/LV

CN19
5
1
2
3
4
6

AFTP137
AFTE30-GP

Near CN19

ACES-CON4-GP-U1

FAN_ID   62

Near CN19

AFTE30-GP   AFTP53
AFTP52   AFTE14P-GP

CN20

DY

For ULV

6
1
2
3
4
5
7

VDD_FAN

ACES-CON5-8-GP

AFTP138
AFTE14P-GP

Near CN19

VCC5B

R543
0R0306-PAD-GP

VCC5B_FAN

F15
FUSE-2A32V-7-GP

VCC5B_FANFUSE

SCD01U16V2KX-3GP

DY

C499

Q37
6   1
5   2
4   3

SSM6J402TU-GP

R544
1KR2J-1-GP

R545
100R2J-2-GP

Q38
1
R1
R2
2

LTC014EEB-FS8-GP

62,70   FAN_ON

C500
SCD1U16V2KX-3GP

FAN_FRQ_Q53

Q39
1   R1   3
R2
2

LTC015EEB-FS8-GP

VCC3B

R546
1KR2J-1-GP

FAN_FRQ   70

SV

FAN_ID	Thermal module
1	DELTA or Toshiba
0	AVC

ULV

FAN_ID	Thermal module
1	Toshiba
0	AVC or DELTA

VCC3M

VCC3M_GSEN R547 1 10R3J-3-GP VCC3_ACC

Q40
LTA014EEB-FS8-GP
R1
R2
3

61 -GSENSE_ON

DY
R548
100KR2J-1-GP

SC10U10V5KX-2GP C501 SCD1U10V2KX-4GP C502

ANALOG_AGND

Place close to EC

GSENSE_Y_R   R549   GSENSE_Y 61
56KR2J-L1-GP
C503   C504
SCD1U10V2KX-4GP   SCD1U10V2KX-4GP
ANALOG_AGND

61 GSENSE_TST

U38
ST   VDD RES
GND   VOUTZ 8
GND   VOUTY 10
GND   VOUTX 12
NC#1
NC#4   NC#11 11
NC#9   NC#13 13
        NC#16 16
LIS34ALTR-1-GP

R551
100KR2J-1-GP

R552
0R2J-2-GP

ANALOG_AGND

GSENSE_X_R   R550   GSENSE_X 61
56KR2J-L1-GP
C505   C506
SCD1U10V2KX-4GP   SCD1U10V2KX-4GP
ANALOG_AGND

DY
R553
GSENSE_Z_R   1   GSENSE_Z 61
56KR2J-L1-GP
C507   C508
SCD1U10V2KX-4GP   SCD1U10V2KX-4GP
ANALOG_AGND   **DY**   **DY**

**Width = 6 mil & Spacing = 10 mil
for three Output traces**

U38

Primary	STMicro LIS34AL	41R0828AA
Second	Kionix KXTC8-2850	74.KXTC8.0BZ
Third		

Layout Comment :

(1) Avoid routing under DCDC switching area.

VCC3B

VCC3B

R609
10KR2J-3-GP

C517
SCD1U10V2KX-4GP

U40

Pin	Signal		Signal	Pin
10	VPS	LRESET#	16	-PLTRST_NEAR 28,32,54,56,58,60,69,70
24	VPS	LFRAME#	22	-LPC_FRAME 24,58,60
1	GPIO1	LPCPD#	28	
2	GPIO2	SERIRQ	27	IRQSER 24,58,60
6	GPIO3			
9	GPIO4	LCLK	21	LPCCLK_CRYPT_33M 28
15	GPIO5			

26,58,60 -CLKRUN

3	VNC	LAD0	26	LPC_AD0
7	PP	LAD1	23	LPC_AD1
		LAD2	20	LPC_AD2
5	NC#5	LAD3	17	LPC_AD3
8	NC#8			
12	NC#12			
13	NC#13			
14	NC#14	GND1	4	
19	NC#19	GND2	11	
25	NC#25	GND3	18	

LPC_AD[3..0] 24,58,60

ST33ZP24AR28PVSC-GP

BU	After SDV
ST19NP18ER28PVMO (71.19N18.T0W)	ST33ZP24AR28PVxx xx="OG" for SDV(71.03324.A0W), "RC" for FVT, PreSIT (FW 1.2.C.0)(71.03324.C0W) SC for SIT (not PreSIT) FW 1.2.D.0(71.03324.D0W)

EEPROM

	Vendor	U42	Part Number
1st	ROHM	BUL08-1FVJ-W	72.BUL08.A0Q
2nd	NXP	PCA24S08ADP	72.24S08.A0Q
3rd	Sanyo	LE26CAP08TT	72.26C08.00R

PTC Placement List

	PTC Position
RT11	VCC3M_DC/DC_High_side_FET (Q88)
RT6	VCC5M_DC/DC_High_side_FET (Q86 or Q87)
RT9	DOCK_PWR20_IN_FET (Q30)
RT3	DCIN_PWR20F_FET (Q51)
RT5	CHARGER_OUT12_FET (Q75)
RT10	M_BAT_PWR_A_FET (Q62)
RT4	S_BAT_PWR_A_FET (Q65)
RT2	VCCCPUCORE_Driver-1 (U47)
RT7	VCCCPUCORE_Driver-2 (U49)
RT1	VCCGFXCORE_Driver (U50)
RT8	CPU_Die (U1)

These pins are used for VCC3SW force off.

U39B
TB62D515FG-GP
2 OF 1

Left pin labels (inputs):
99 VREGIN20
65 VINT20
64 VCC5M
103 VCC3SW
54 VDD15
100 BAT_VOLT — RINKAN_BATVOLT
59 SW_CTRL_EN#
102 SW_OFF#
48 RD1_ON — 64 3FP_ON
47 RD2_ON — 56,58,77,83,94 B_ON
46 RD3_ON — 70 VCCLAN_ON
45 RD4_ON — 77 VCC3WAN_HY_ON
44 RD5_ON — 70 VCC3WLAN_ON
43 RD6_ON — 63,64,70 HY_BRIDGE_POWER_EN
42 RD7_ON
49 3P_ON — 33 PANEL_POWER_ON
50 VCC5B — VCC5B_THINK
56 5MUBAY — VCC5M_5MBAY
68 CP25OUT — CP28OUT2
66 VCPIN25
30 DISCHARGE — 59,60 DISCHARGE
97 SHUTDWNIN#
98 TH_DTCT — TH_DET

Right pin labels (outputs/drivers):
63 CP15OUT2 — RINKAN_CP12OUT2
62 CP15OUT1 — RINKAN_CP12OUT1
58 RD1_DRV — 3FP_DRV 64
60 RD2_DRV — B_DRV 94
61 RD3_DRV — VCC3LAN_DRV 91
67 RD4_DRV — VCC3WAN_HY_DRV 96
69 RD5_DRV — VCC3WLAN_DRV 96
71 RD6_DRV — HY_BRIDGE_DRV 96
72 RD7_DRV
55 3P_DRV — VCC3P_DRV 34
53 3B_DRV — VCC3B_DRV 37,94
51 5B_DRV — VCC5B_DRV 94
57 UBAY_DRV — VCC5MUBAY_DRV 59
76 M1_DRV — M1_DRV 73
78 M2_DRV — M2_DRV 73
75 S1_DRV — S1_DRV 59
77 S2_DRV — S2_DRV 59
73 BAT_DRV — BAT_DRV 73
74 DCIN_DRV — DCIN_DRV 72
105 M_PGS — MPWRG_R
104 B_PGS
96 PWRSHUTDWN# — -PWRSHUTDOWN 59,72,74,78

Bottom pins:
119, 11, 41, 70, 52, 101 — DGND1 DGND2 DGND3 PGND1 PGND2 AGND

Signals (right side):
MPWRG 11,26,32,64
BPWRG 11,26,32,61,62,69
-PWRSHUTDOWN 59,72,74,78
3M_PWRG 78
5M_PWRG 78

62 -SHUTDOWN
-PWRSHUTDOWN

Component references (partial):
R1512 100KR2J-1-GP, R1513 100KR2J-1-GP
TP116 TPAD14-OP-GP, TP117 TPAD14-OP-GP
R1517 1D78MR2F-GP, R1518 20R5J-GP, R1519 20R5J-GP
R1520 1D24MR2F-GP, R1521 0R2J-2-GP
C1036 SC470P50V2KX-3GP
C1035 SC2D2U25V5KX-1-GP
R1511 10R3J-3-GP
C1027 SCD1U25V3KX-GP
R1514 0R0306-PAD-GP, R1515 0R0306-PAD-GP, R1516 0R0306-PAD-GP
C1028 SC1U10V3KX-3GP, C1029 SC1U25V3KX-1-GP
C1034 SC1U25V3KX-1-GP, C1030 SC1U25V3KX-1-GP
D115, D116, D117 RB521SM-30T2R-GP
C1031 SCD1U10V2KX-4GP, C1032 SC1U10V2KX-4GP, C1033 SCD1U25V3KX-GP
D118, D119, D120, D121 RB521SM-30T2R-GP
C1038 SCD01U25V2KX-3GP
C1037 SC1U25V3KX-1-GP
C1039 SCD22U50V5KX-3GP
R1522 100KR2J-1-GP, R1523 100R2J-2-GP
R1524 33KR2J-3-GP, R1526 2KR2J-1-GP, R1525 10KR2J-3-GP
R1528 0R2J-2-GP
R1529 100KR2J-1-GP, R37 51KR2J-1-GP
R1532 0R2J-2-GP
R1533 33KR2J-3-GP
R1530 0R2J-2-GP, R1531 0R2J-2-GP
C1040 SC470P50V2KX-3GP
C1041 SCD1U10V2KX-4GP
RT1-RT11 NTC-540-GP
TH_RT1 through TH_RT11

Power rails:
VREGIN20, VINT20, VCC5M, VCC3SW, VDD15, VCC3M, VCC5B, VCC3B

Signals: RINKAN_VRGEIN20_D, RINKAN_VRGEIN20, RINKAN_VINT20, VCC3SW_R, VDD15_R1, AR_VDD15, R_VDD15_D, CP28OUT2, MPWRG_R

D56	
ESD5Z3.3T1G(OnSemi)	83.ESD5Z.0AF
uClamp3301H(SEMTECH)	83.03301.AAF

PLACE NEAR CONNECTOR

4.5A

DCIN_PWR20_F

DY

Keep these two signals as a pair routing !!

10ohm(R665), 1k(R667)
should be put near R-sense(R628)

AC Adapter	ACDC_ID	#135W_AC	#90W_AC	System Power Limit
170W	0.33V < ID <= 0.66V	L	L	90W
135W	ID <= 0.33V	L	L	90W
90W	2.64V < ID	H	L	90W
65W	1.32V < ID <= 1.98V	H	H	65W

PEAK SHIFT	YES	NO
R641	NO-ASM	ASM
R629	ASM	NO-ASM
Q58	ASM	NO-ASM
Q55	ASM	NO-ASM

Logic

BATTERY INPUT

Keep these two signals as a pair routing !!

Input cap: 10uF 10% 25V X5R 4pcs.
These MLCCs must be placed
symmetrically on Top and Bottom.

CHIP IND 4.7UH SPM6530T-4R7M

Output cap: 10uF 10% 25V X5R 4pcs.
These MLCCs must be placed
symmetrically on Top and Bottom.

Please R1314 and C842 near EC

CHARGER_OUT12

BAT-PWR12

Q75
SI7129DN-T1-GE3-GP

R697
220KR2J-L2-GP

R698
470KR2J-2-GP

C567
SCD1U25V3KX-GP

R699
100KR2J-1-GP

R700
200KR2J-L1-GP

Q76
LSK3541G1ET2L-GP

70 BAT_CRG

D73
1SS400GGT2R-GP

R701
1KR2J-1-GP

C568
SCD033U25V3KX-GP

M-BAT-TRCL

D74
DAN222GTL-GP

M_BAT_TRCL_A

D75
DAN222GTL-GP

C569
SCD01U25V2KX-3GP

R702
470KR2J-2-GP

Q77
SSM6J402TU-GP

R703
4K7R2J-2-GP

Q78
LTC015EEB-FS8-GP

R1

R2

M_TRCL   70

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
CHARGER SELECT

Size
A4

Document Number
Dasher-2

Rev
-1

Date: Friday, March 09, 2012   Sheet   75   of   101

VOUT = 0.249 (VBAT - 5)
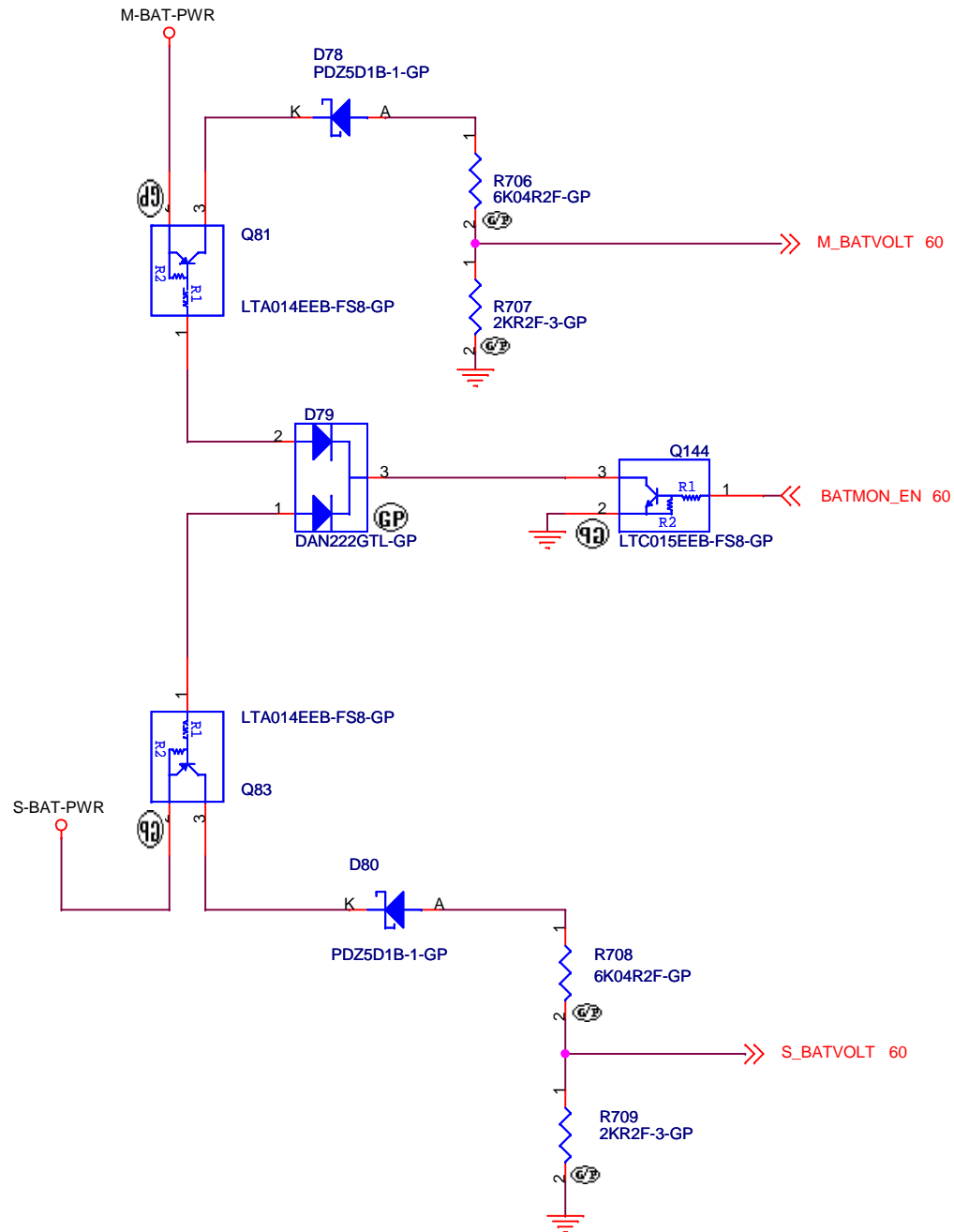
M-BAT-PWR

D78
PDZ5D1B-1-GP

K   A

R706
6K04R2F-GP

Q81

M_BATVOLT 60

R2   R1

LTA014EEB-FS8-GP

R707
2KR2F-3-GP

D79

Q144

R1

BATMON_EN 60

R2

DAN222GTL-GP

LTC015EEB-FS8-GP

LTA014EEB-FS8-GP

R1
R2

Q83

S-BAT-PWR

D80

K   A

R708
6K04R2F-GP

PDZ5D1B-1-GP

S_BATVOLT 60

R709
2KR2F-3-GP
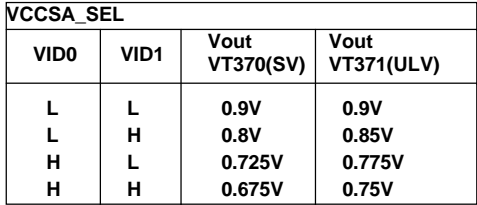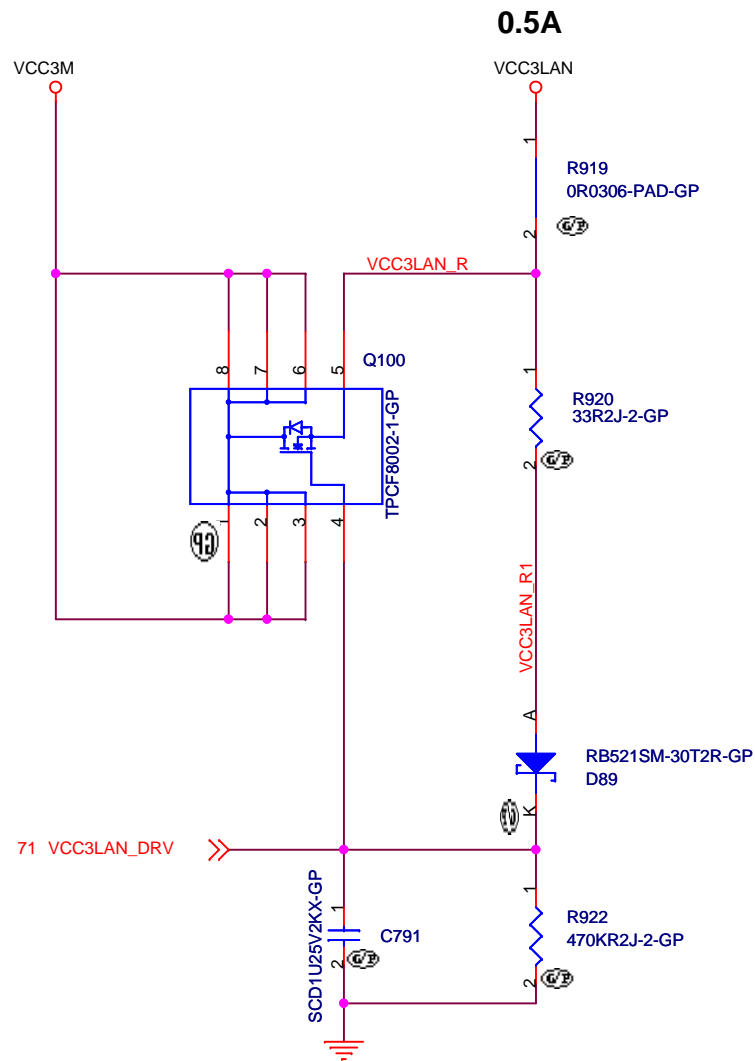
<Core Design>

緯創資通   **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
BATTERY MONITOR

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M1_ON 64,70 → R710 1 2 0R2J-2-GP → VCC5M_ON 78

R712 1 2 0R2J-2-GP → VCC3M_ON 78

R711 1 2 0R2J-2-GP → 1R8B_ON 89

B1_ON 70 → R713 1 2 0R2J-2-GP → B_ON 56,58,71,83,94

A1_ON 70 → R714 1 2 0R2J-2-GP → VCC1R5A_ON 85

IMVP_ON 70 → R927 1 2 0R2J-2-GP → VCORE_ON 79

VCC3B

R715
47KR2J-2-GP

B1_ON 70 → D81 2

VTT_PWRG 83 → 1

DAP222GTL-GP

SC1KP50V2KX-1GP

R774 1 2 0R2J-2-GP → VCCSA_ON 90

C571

WWAN_HYPWR_ON 70 → 2 D20 3 → VCC3WAN_HY_ON 71

B_ON → 1

DAN222GTL-GP

DY
R143
100KR2J-1-GP

R719 1 2 22KR2J-GP

B1_ON 70 → R720 1 2 1KR2J-1-GP

D83 K A
1SS400GGT2R-GP → 0R75B_ON 86

C572
SCD1U10V2KX-4GP

AOAC Duplicity	YES YES	YES NO	NO YES	NO NO
D20 R143	ASM DY	ASM DY	ASM DY	DY ASM

↑
Logic

This is a circuit schematic diagram (Dasher-2, Sheet 78 of 101, DC-DC VCC3M/VCC5M).

Title: **DC-DC VCC3M/VCC5M**
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<Core Design>

Configuration table:

	SIT-1 & SIT-3	Other config
C576/C577/C578 C581/C582/C583	10U 25V X5R K1206 T1.9 (78.10622.L6L)	10U 25V K1206 X5R 13T (78.10622.L5L)

Notes:
- Input cap: 10uF 10% 25V X5R 8pcs. These MLCCs must be placed symmetrically on Top and Bottom.
- Input cap: 10uF 10% 25V X5R 4pcs. These MLCCs must be placed symmetrically on Top and Bottom.
- 10UF 10% 25V X5R 4pcs
- Need common pad with Toshiba, Infineon, Vishay, etc

Inductor tables:

L27		
MPLCH0740L2R2 (NEC TOKIN)	68.2R21C.10T
SPM6540T-2R2M (TDK)	68.2R21E.10E

L26		
MPCH1055L1R3(NEC/TOKIN)	68.1R310.20A
FDU1050D-H-1R5M(TOKO)	68.1R51B.10K

18m-ohm
18m-ohm x 2pcs
6TPE330MIL SANYO
10m-ohm 4TPF470ML

18A
9A

Mode configuration table:

VCC5M	VCC3M	Auto-Skip	OOA	S0:Auto-Skip S3:OOA	S0:OOA S3:Auto-Skip
R747	R743	ASM	NO ASM	NO ASM	NO ASM
R748	R744	NO ASM	ASM	ASM	ASM
R751	R749	NO ASM	NO ASM	ASM	ASM
R1556	R1555	NO ASM	NO ASM	NO ASM	ASM
R1558	R1557	NO ASM	NO ASM	ASM	NO ASM
Q146	Q145	NO ASM	NO ASM	NO ASM	ASM
Q93	Q92	NO ASM	NO ASM	ASM	ASM

Title: DC-DC VCCCPUCORE

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VCC5M_OUT

VCC1R8B

R1612
0R0306-PAD-GP

R1613
10R2J-2-GP

L39
MPZ1608S300AT-GP

L40
MPZ1608S300AT-GP

C1099
SC2200P50V2KX-2GP

U50

A1 TS_FAULT#   VDDH C4
A4 VCC   VDDH C3
B1 VDD   VDDH C2
B2 ISENSE   VDDH C1
B3 PWM   BST   B4

E1 VSS   VX#D1 D1
E2 VSS   VX#D2 D2
E3 VSS   VX#D3 D3
E4 VSS   VX#D4 D4
G1 VSS
G2 VSS   VX#F1 F1
G3 VSS   VX#F2 F2
G4 VSS   VX#F3 F3
J1 VSS   VX#F4 F4
J2 VSS
J3 VSS   VX#H1 H1
J4 VSS   VX#H2 H2
                 VX#H3 H3
A2 GND   VX#H4 H4

VT1324SFCX-GP-U

79   -TS_FAULT2
VCC1R8B_GFXCORE
VCC1R8B_VDD_GFX
79   IPH2_1
79   PWM2_1

C1102
SCD22U10V2KX-1GP

C1107
SCD1U10V2KX-L-GP

C1108
SC4D7U6D3V3KX-GP

R816
0R0402-PAD-1-GP

GND_1326S_3

GND_1326S_3

C1104 SC1U10V3KX-3GP
C1100 SC1U10V3KX-3GP
C1105 SC10U6D3V3MX-L-GP
C1101 SC10U6D3V3MX-L-GP
C1103 SC10U6D3V3MX-L-GP
C1106 SC10U6D3V3MX-L-GP

VCCGFXCORE_I

Vx2_1

L29
IND-120NH-12-GP
CHIP IND 120NH M MPCH0730LR12

R812
100R2F-L1-GP-U

<Variant Name>

Title
VCCGFXCORE

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24 x 22uF MLCC for VCCGFXCORE_I

40 x 2.2uF + 3 x 2.2uF(NO_ASM)_0402 MLCC for VCCCPUCORE

3 x 10uF + 7 x 4.7u_0603 MLCC for VCCCPUCORE

1 x 22uF_0805 MLCC for VCCCPUCORE

3 x 22uF + 7 x 22uF_0603 MLCC(No ASM) for VCCCPUCORE

20 x 22uF + 5 x 4.7uF_0603 MLCC for VCCCPUCORE

BLANK

SPM5030T-R20M

9A

VCC1R05B_VTT

VCC5M_OUT

VCC5M_OUT_1R05B_VTT

L52
MPZ1608S300AT-GP

C938
SC2200P50V2KX-2GP

SC10U10V5KX-2GP C699
SC10U10V5KX-2GP C700
SCD1U16V2KX-3GP C701
SCD01U25V2KX-3GP C702

R819
10R3J-3-GP

VCC1R05B_VTT_AVDD

C704
SCD22U10V2KX-1GP

AGND_1R05B_VTT

VCC5M_OUT_1R05B_VTT

R818
100KR2J-1-GP
DY

U51

C4	VDD	VX#D1	D1	
C5	VDD	VX#D2	D2	
B3	AVDD	VX#D3	D3	
		VX#D4	D4	
		VX#D5	D5	

VCC1R05B_VTT_BIAS
VCC1R05B_VTT_RSEL

A1	BIAS	
A2	R_SEL/ILOAD	GND	C1
A4	VSENSE+	GND	C2
A3	VDES	GND	C3
A5	OE	
B2	IRIPL	AGND	B1
B4	TEMP	
B5	STAT	

VT356FCX-ADJ-007-GP

L30
IND-D2UH-11-GP

VCC1R05B_VTT_VX

SC22U6D3V5MX-2GP C706
SC22U6D3V5MX-2GP C707
SC22U6D3V5MX-2GP C708 **DY**
SC22U6D3V5MX-2GP C709
SC22U6D3V5MX-2GP C710
SC22U6D3V5MX-2GP C711
SC22U6D3V5MX-2GP C712
SCD1U16V2KX-3GP C717
SC6800P25V2KX-1GP C718

R1616
0R2-PT5-LILY-GP

56,58,71,77,94 B_ON

VCC1R05B_VTT_OE
VCC1R05B_VTT_IRIP

77 VTT_PWRG

0R2J-2-GP
R1617

R831
0R0402-PAD-1-GP

SCD01U16V2KX-3GP C705 **DY**

R824 R825 R826

44K2R2D-GP 6K49R2D-GP 25K5R2F-GP

VCC_SENSE_VTT 7

VCC1R05B_VTT_VDES

VENSE- trace routed differntially parallel to VSENSE+

R829
1MR2F-GP
DY

R828
39K2R2D-GP

C716
SC2200P50V2KX-2GP

VSS_SENSE_VTT 7

AGND_1R05B_VTT

R829:NO_ASM

```
1.10V R828 = 40.2K
1.05V R828 = 38.3K
1.00V R828 = 36.5K
VOUT = 1.212*R828/44.2K
```

SPM5030T-R20M

9A

DC-DC VCC1R05AMT

VT356FCX-ADJ-007-GP

VENSE- trace routed differntially parallel to VSENSE+

collect all AGND referenced
signals before droping to GND

Don't connect AGND and GND under the chip

Title
DC-DC VCC1R05AMT

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VCC5M_OUT_1R5A

VCC5M_OUT

VCC3M

DY
R847
100KR2J-1-GP

L45
MPZ1608S300AT-GP
L46
MPZ1608S300AT-GP

18A

VCC1R5A

R846
10R3J-3-GP

C813
SC10U10V5KX-2GP

C734
SC10U10V5KX-2GP

C735
SC10U10V5KX-2GP

C736
SCD1U10V2KX-3GP

C737
SCD01U25V2KX-3GP

VCC1R5A_AVDD

C738
SCD22U10V2KX-1GP

AGND_1R5A

U62
B3 AVDD
C4 VDD
C5 VDD
E4 VDD
E5 VDD

B2 IRIPL
B4 TEMP
A1 BIAS
A2 R_SEL/ILOAD
A5 OE
B5 STAT

VX#D1 D1
VX#D2 D2
VX#D3 D3
VX#D4 D4
VX#D5 D5
VSENSE+ A4
VDES A3

GND C1
GND C2
GND C3
GND E1
GND E2
GND E3
AGND B1

VT357FCX-ADJ-007-GP

MPC0730LR20C

VCC1R5A_VX

L32
IND-D2UH-6-GP

VCC1R5A_R

R969 0R0306-PAD-GP
R848 0R0306-PAD-GP
R849 0R0306-PAD-GP

VCC1R5A_R

VCC1R5A_IRIP
VCC1R5A_BIAS
VCC1R5A_RSEL
VCC1R5A_STAT

77 VCC1R5A_ON

C747
SCD01U16V2KX-3GP

R851 44K2R2D-GP
R852 6K49R2D-GP
R853 28KR2F-GP

DY
C818 SC22U6D3V5MX-2GP
C817 SC22U6D3V5MX-2GP
C739 SC22U6D3V5MX-2GP
C740 SC22U6D3V5MX-2GP
C741 SC22U6D3V5MX-2GP
DY
C742 SC22U6D3V5MX-2GP
C743 SC22U6D3V5MX-2GP
C744 SC22U6D3V5MX-2GP
C745 SCD1U16V2KX-1GP
C746 SC6800P25V2KX-2GP
C815 SC22U6D3V5MX-2GP
C816 SC22U6D3V5MX-2GP

AGND_1R5A

collect all AGND referenced
signals before droping to GND

SENSE_VCC1R5A   R850 0R3-PT5-LILY-GP

VCC1R5A_VDES

VENSE- trace routed differntially parallel to VSENSE+

R854 56K2R2D-GP
C748 SC2200P50V2KX-2GP

SENSE_VCC1R5A_GND   R855 0R3-PT5-LILY-GP

AGND_1R5A

Don't connect AGND and GND under the chip

R856
1 2
0R0402-PAD-1-GP

AGND_1R5A

<Core Design>

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Taipei Hsien 221, Taiwan, R.O.C.

Title
DC-DC VCC1R5A

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A3

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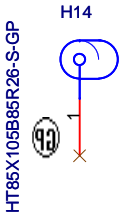Date: Friday, March 09, 2012   Sheet 85 of 101

1A

CONNECT TO
DIMM CONNECTOR

VCC1R5A   VCC0R75B

DDR3_VREF_CA

VCC0R75B

NEAR DIMM CONNECTOR

R857
10KR2F-2-GP

R1623
0R0402-PAD-1-GP

VCC3M

U54 Local symbol

VCC1R5A

R859 1   2 0R0306-PAD-GP VCC1R5A_IN_DDR3

10 VIN   VO   3
         REFOUT   6
2   VLDOIN PGOOD 9
5   VOSNS   PGND 4
7   EN   GND 8
1   REFIN   GND 11

R858 1   2 0R2J-2-GP   DRAMPWRG 4,26,32

77 0R75B_ON

VCC1R5A_SENSE

TPS51200DRCR-1-GP

SC1U10V3KX-3GP   SCD1U10V2KX-4GP   C755   C1123   SCD1U10V2KX-5GP   C749   R860 22R2J-2-GP   SC10U6D3V3MX-GP C308   C751 SC10U6D3V3MX-GP

0R75B_ON_R1

R861 1   10KR2F-2-GP

C756 1   2 SC1KP50V2KX-1GP

VCC3M

R862
10KR2J-3-GP

Q94
D
G   0R75B_ON_R   3
S
LSK3541G1ET2L-GP

Q95
R1
R2
1   0R75B_ON   77
LTC015EEB-FS8-GP

SCD01U16V2KX-3GP C752   SC10U6D3V3MX-GP C753   SC10U6D3V3MX-GP C754

DY

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<Variant Name>

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VCC5M_OUT

VCC5M_OUT

R1624
0R0306-PAD-GP

R1625
10R2J-2-GP

C1127
SC1KP50V2KX-1GP

AGND_1R8B

SCD01U25V2KX-3GP
C1124

SC10U6D3V3MX-L-GP
C1125

SC10U6D3V3MX-L-GP
C1126

L33

supplier	Vendor P/N	Wistron P/N
TDK	VLC5020T-2R2N	68.2R25A.10Z
Taiyo-Yuden	NRS5024T-2R2NMGJ	68.2R21E.10F

3A
VCC1R8B

U55

VCC
PVCC
PVCC
PVCC

77 1R8B_ON

EN   BST   8

R123
0R2J-2-GP

C1128
SCD1U10V2KX-L-GP

L33

SW#1   1
SW#2   2
SW#3   3
SW#4   4

IND-2D2UH-213-GP
CHIP IND 2.2UH N VLC5020T-2R2N

R1626

SENSE+_1R5V

1R8B_ITH   12 ITH

ADJ   11
GND   17

1R8B_ADJ

12K7R2F-GP

R1627
0R0402-PAD-1-GP

R1628
8K2R2J-3-GP

BD9139MUV-GE2-GP

GND

PGND
PGND
PGND

SC470P50V2KX-3GP

DY
C319

R1629
10KR2F-L1-GP

SCD01U25V2KX-3GP
C1130

SCD1U16V3KX-3GP
C1131

SC22U6D3V5MX-L3-GP
C1132

SC22U6D3V5MX-L3-GP
C1133

DY

C1129
SC6800P25V2KX-1GP

13 16 15 14

R124
2D2R3J-2-GP

DY

AGND_1R8B

R1630
0R0402-PAD-1-GP

AGND_1R8B
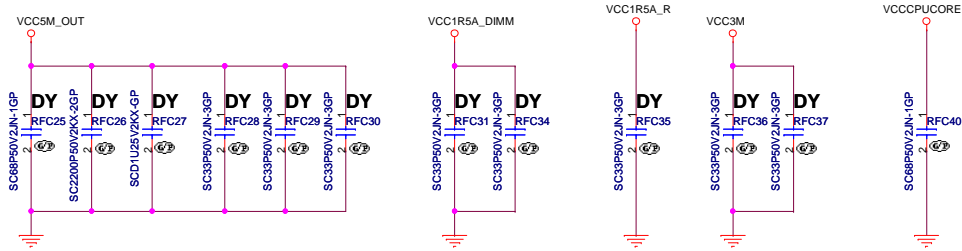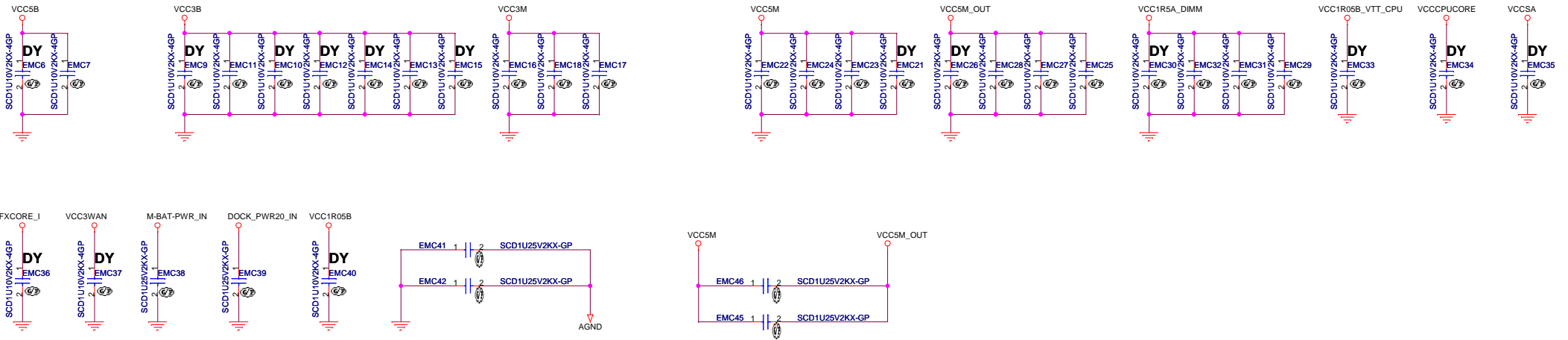
AGND_1R8B

<Core Design>

Wistron Corporation
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Title
DC-DC VCC1R8B

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VCC5M_OUT

C1134
SC2K2P50V3KX-GP

L47
MPZ1608S300AT-GP

VCCSA_SEL			
VID0	VID1	Vout VT370(SV)	Vout VT371(ULV)
L	L	0.9V	0.9V
L	H	0.8V	0.85V
H	L	0.725V	0.775V
H	H	0.675V	0.75V

VCC5M_OUT_SA

C1135 SCD1U10V2KX-L-GP
C1136 SC1U10V3KX-3GP
C1137 SC4D7U6D3V3KX-GP
C1138 SC22U6D3V5MX-L-3-GP

DY
R1632
37K4R2F-1-GP

DY
C1139
SCD01U25V2KX-3GP

6A
VCCSA

U56
VDD
PGM   B2   VX#C2   C2
OE   B3   VX#C3   C3
             VX#C4   C4
       SENSE+   A4   VCCSA_SENSE+
VID1   A2   SENSE-   A5
VID0   A1   STAT   B1
AGND   A3   GND   C1
VT370FCX-ADJ-1-GP

77 VCCSA_ON

8 VCCSA_SEL1
8 VCCSA_SEL0

VCCSA_L   L34 IND-D35UH-GP
CHIP IND 0.35UH SPM5030T-R35M

R1633
12K1R2F-L1-GP

C1149 SC4700P25V2KX-3-GP
C1150 SC3300P50V2KX-1GP
R1635
10KR2F-L1-GP

C1140 SC22U6D3V5MX-2GP
C1141 SC22U6D3V5MX-2GP
C1142 SC22U6D3V5MX-2GP
C1143 SC22U6D3V5MX-2GP
C1144 SC22U6D3V5MX-2GP
C1145 SC22U6D3V5MX-2GP
C1146 SC22U6D3V5MX-2GP
C1147 SCD1U10V2KX-4GP
C1148 SCD01U25V2KX-3GP

R1634
100R2F-L1-GP-U

VCCSA_SENSE 8

R1636
100R2F-L1-GP-U

VCCSA_SENCE- trace routed
differntially parallel to GND

R1637
0R0402-PAD-1-GP

AGND_SA

VCCSA_SENSE-

SAPWRG   61

R1638
0R0402-PAD-1-GP

Route to CPU GND pin
close to VCCSA_SENCE.

<Variant Name>

Title
DC/DC VCCSA

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0.5A

VCC3M

VCC3LAN

R919
0R0306-PAD-GP

VCC3LAN_R

Q100
TPCF8002-1-GP

R920
33R2J-2-GP

VCC3LAN_R1

RB521SM-30T2R-GP
D89

71 VCC3LAN_DRV

SCD1U25V2KX-GP
C791

R922
470KR2J-2-GP

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7.2A

VCC3M
VCC3B

Q102
SIS402DN-T1-GE3-GP

R933 0R0306-PAD-GP
R980 0R0306-PAD-GP

VCC3B_R

R937 270R2J-L-GP

D93 RB521SM-30T2R-GP

VCC3B_R1

37,71 VCC3B_DRV

C795 SCD1U25V3KX-GP

R939 470KR2J-2-GP

4A

VCC5M
VCC5B

Q103
TPCF8002-1-GP

R934 0R0306-PAD-GP

VCC5B_R

R938 100R2J-2-GP

VCC5B_R1

D94 RB521SM-30T2R-GP

71 VCC5B_DRV

C796 SCD047U25V2KX-GP

R940 470KR2J-2-GP

VCC3M
VCC1R5_VDDQ

R935 10KR2J-3-GP
R936 39R2J-L-GP

B_ON_VDDQ_R
VDDQ_R

Q104
LTC015EEB-FS8-GP
R1 R2

56,58,71,77,83 B_ON

Q105
LSK3541G1ET2L-GP

5A   **4A**

VCC1R5_VDDQ
VCC1R5B

VCC1R5A

R942 0R0306-PAD-GP
R943 0R0306-PAD-GP

VCC1R5_VDDQ_R

Q107
TPCA8056-H-GP

R946 47R2J-2-GP

B_DRV_VDDQ
VCC1R5_VDDQ_R1

R949 0R2-PT5-LILY-GP

D96 RB521SM-30T2R-GP

71   B_DRV

C802 SCD022U25V2KX-GP

6A

VCC1R05B

VCC1R05AMT

R944 0R0306-PAD-GP

Q108
TPCA8062-H-GP

VCC1R05B_R

R947 47R2J-2-GP

VCC1R05B_R1

D97 RB521SM-30T2R-GP

71   B_DRV

C803 SCD022U25V2KX-GP

VCC1R5_VDDQ
VCC1R5A_DIMM

C797 SCD1U10V2KX-4GP
C798 SCD1U10V2KX-4GP
C799 SCD1U10V2KX-4GP
C800 SCD1U10V2KX-4GP

<Core Design>

緯創資通 **Wistron Corporation**
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Taipei Hsien 221, Taiwan, R.O.C.

Title
LOAD SW B

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2.7A

2.7A

VCC3WAN   VCC3B

VCC3WLAN

VCC3M

R952
0R0306-PAD-GP

R953
0R3J-0-U-GP

DY

R954
0R0306-PAD-GP

VCC3WAN_R

VCC3WLAN_R

Q110
TPCF8002-1-GP

R1639
100R2J-2-GP

Q109
TPCF8002-1-GP

R1640
33R2J-2-GP

VCC3WAN_R1

VCC3WLAN_R1

D127
RB521SM-30T2R-GP

D128
RB521SM-30T2R-GP

71   VCC3WAN_HY_DRV

71   VCC3WLAN_DRV

SCD02U25V2KX-GP

C1151

R1641
470KR2J-2-GP

SCD4/TU25V0KX-1GP

C1152

R1642
470KR2J-2-GP

AOAC	YES	YES	No	No
Duplicity	YES	No	YES	No
Q110	ASM	ASM	ASM	DY
D127	ASM	ASM	ASM	DY
R1639	ASM	ASM	ASM	DY
R1641	ASM	ASM	ASM	DY
C1151	ASM	ASM	ASM	DY
R953	DY	DY	DY	ASM

Logic

1.1A

1.1A

1.1A

VCC5_HY

VCC5_BRG   VCC5B

VCC3_BRG   VCC3B

VCC5M

VCC5M

VCC3M

R1643
D01RL0816F-L-GP

DY

R1644
0R0306-PAD-GP

R1645
0R0306-PAD-GP

R1646
0R0306-PAD-GP

R1647
0R0306-PAD-GP

VCC5_HY_R

VCC5B_BRG_R

VCC3B_BRG_R

U85
TPCF8002-1-GP

R1648
100R2J-2-GP

DY

DY

U86
TPCF8002-1-GP

R1649
100R2J-2-GP

DY

DY

U87
TPCF8002-1-GP

R1650
100R2J-2-GP

DY

DY

VCC5_HY_R1

VCC5_BRG_R1

VCC3_BRG_R1

D129
RB521SM-30T2R-GP

DY

D130
RB521SM-30T2R-GP

DY

D131
RB521SM-30T2R-GP

DY

71   VCC3WAN_HY_DRV

71   HY_BRIDGE_DRV

71   HY_BRIDGE_DRV

SCD02U25V2KX-GP

DY

C1153

R1651
470KR2J-2-GP

DY

SCD02U25V2KX-GP

DY

C1183

R1652
470KR2J-2-GP

DY

SCD02U25V2KX-GP

DY

C1184

R1653
470KR2J-2-GP

DY

<Core Design>

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Title
LOAD SW WWAN & WLAN

Size	Document Number	Rev
Custom		-1

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ZZ.00PAD.911

H1
HOLE315R95-GP
GP

H2
HOLE315R95-GP
GP

H12
HOLE315R95-GP
GP

H13
HOLE315R95-GP
GP

ZZ.00PAD.921

H5
HOLE237R95-GP
GP

H6
HOLE237R95-GP
GP

H7
HOLE237R95-GP
GP

ZZ.00PAD.801

H3
HOLE256R142-GP
GP

H4
HOLE256R142-GP
GP

H10
HOLE256R142-GP
GP

H11
HOLE256R142-GP
GP

ZZ.SCREW.191

H14
HT85X105B85R26-S-GP

ZZ.0HOLE.XXX

H15 HOLE
H16 HOLE
H17 HOLE
H18 HOLE
H19 HOLE
H20 HOLE
H21 HOLE
H22 HOLE
H23 HOLE
H24 HOLE
H25 HOLE

34.49U26.001
GND pad for docking connector

SW2
SPRING-7-GP

34.49U23.001
GND pad for docking connector

SW3
SPRING-9-GP
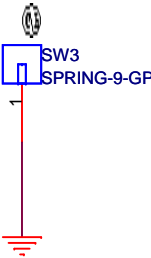
34.15J03.001
spring for EMI

SW4
SPRING-43-GP-U

34.15J03.001
spring for RF (SW5~SW7)

SW5 DY
SPRING-43-GP-U

SW6 DY
SPRING-43-GP-U

SW7 DY
SPRING-43-GP-U

Page	Parts	LPM2.0	LPM1.1	Disable
29	R289	ASM	ASM	DY
	R260	DY	DY	ASM
33	U69	ASM	ASM	DY
	C942	ASM	ASM	DY
	C943	ASM	ASM	DY
	C944	ASM	ASM	DY
	C945	ASM	ASM	DY
	R1340	ASM	ASM	DY
	R1341	ASM	ASM	DY
	Q127	ASM	ASM	DY
	Q128	ASM	ASM	DY
	C318	ASM	ASM	DY
	U68	ASM	ASM	DY
	C939	ASM	ASM	DY
	C940	ASM	ASM	DY
	C941	ASM	ASM	DY
	U70	ASM	ASM	DY
	U71	ASM	ASM	DY
	U72	ASM	ASM	DY
	C946	ASM	ASM	DY
	C947	ASM	ASM	DY
	C948	ASM	ASM	DY
	R1338	DY	DY	ASM
	R1339	DY	DY	ASM
	R1342	DY	DY	ASM
	R1330	DY	DY	ASM
	R1331	DY	DY	ASM
	R1332	DY	DY	ASM
	R1333	DY	DY	ASM
	R1334	DY	DY	ASM
	R1335	DY	DY	ASM
	R1336	DY	DY	ASM
	R1337	DY	DY	ASM
	CN26	ASM	ASM	DY
34	U73	ASM	ASM	DY
	U74	ASM	ASM	DY
	U75	ASM	ASM	DY
	R1343	DY	DY	ASM
	R1344	DY	DY	ASM
	R1345	DY	DY	ASM
	R1346	DY	DY	ASM

LOGIC

Page	Parts	LPM2.0	LPM1.1	Disable
47	U78	ASM	ASM	DY
	U79	ASM	ASM	DY
	U80	ASM	ASM	DY
	U81	ASM	ASM	DY
	C994	ASM	ASM	DY
	C995	ASM	ASM	DY
	C19	ASM	ASM	DY
	R1366	DY	DY	ASM
	R1367	DY	DY	ASM
	R1368	DY	DY	ASM
	R1369	DY	DY	ASM
	R1370	DY	DY	ASM
	R1371	DY	DY	ASM
	R1372	DY	DY	ASM
53	U82	ASM	ASM	DY
	R404	DY	DY	ASM
	R405	DY	DY	ASM
	R1374	ASM	ASM	DY
	R1373	DY	DY	ASM
61	R1450	DY	DY	ASM
	R1420	ASM	ASM	DY
	R313	ASM	ASM	DY
	R117	DY	DY	ASM
62	Q131	ASM	ASM	DY
	D109	ASM	ASM	DY
	R1441	ASM	ASM	DY
	R1424	ASM	ASM	DY
	R1452	DY	DY	ASM
	R172	ASM	ASM	DY
	R1453	DY	DY	ASM
63	Q19	ASM	ASM	DY
64	Q11	DY	DY	DY
69	U83	ASM	ASM	DY
	U84	ASM	ASM	DY
	R1463	ASM	ASM	DY
	R1464	ASM	ASM	DY
	R1466	ASM	ASM	DY
	R1468	ASM	ASM	DY
	R901	DY	DY	Ref to P101
	R930	DY	DY	Ref to P101
	R536	DY	DY	Ref to P101
	R539	DY	DY	Ref to P101
	C1020	ASM	ASM	DY
	D111	Ref P101	Ref P101	DY
	U42	ASM	ASM	Ref to P101
	Q47	ASM	ASM	Ref to P101
	R617	ASM	ASM	Ref to P101
	C524	ASM	ASM	Ref to P101
	Q48	ASM	ASM	Ref to P101

LOGIC

Page	Parts	LPM2.0	LPM1.1	Disable
70	R1490	ASM	ASM	DY
	R118	DY	ASM	DY
	R119	ASM	DY	DY
	R120	ASM	DY	DY
	R121	DY	ASM	DY
71	D118	ASM	ASM	DY
	D119	ASM	ASM	DY
	R37	ASM	ASM	DY
77	D20	ASM	ASM	Ref to P77
	R143	DY	DY	Ref to P77
96	R953	DY	DY	Ref p96 AOAC table
	Q110	ASM	ASM	Ref p96 AOAC table
	R1639	ASM	ASM	Ref p96 AOAC table
	R1641	ASM	ASM	Ref p96 AOAC table
	D127	ASM	ASM	Ref p96 AOAC table
	C1151	ASM	ASM	Ref p96 AOAC table
	U85	ASM	ASM	DY
	R1643	ASM	ASM	DY
	R1648	ASM	ASM	DY
	R1651	ASM	ASM	DY
	D129	ASM	ASM	DY
	C1153	ASM	ASM	DY
	R1645	DY	DY	ASM
	U86	ASM	ASM	DY
	R1649	ASM	ASM	DY
	R1652	ASM	ASM	DY
	D130	ASM	ASM	DY
	C1183	ASM	ASM	DY
	R1647	DY	DY	ASM
	U87	ASM	ASM	DY
	R1650	ASM	ASM	DY
	R1653	ASM	ASM	DY
	D131	ASM	ASM	DY
	C1184	ASM	ASM	DY

LOGIC

RF decoupling caps

named as RFCxxx

Long power trace EMI decoupling caps

		SV	ULV
Master IC	U48	VT1318M	VT1318M
# of slave for CPU		2	1
Slave for CPU	U47	VT1324S	VT1324S
	U49	VT1324S	no stuff
Inductor for CPU	L28	BPW10040	no stuff
	L14	no stuff	MPCH0730LR12
# of slave for GPU		1	1
Slave for GPU	U50	VT1324S	VT1324S
Inductor for GPU	L29	MPCH0730LR12	MPCH0730LR12

		SV	ULV
	C1090	4.7uF	no stuff
	R1589	10 1.0%	no stuff
	C1089	0.1uF	no stuff
	C1082	0.22uF	no stuff
	C1083	1uF	no stuff
	C1080	1uF	no stuff
	C1084	10uF	no stuff
	C1079	10uF	no stuff
	C1081	10uF	no stuff
	C1085	10uF	no stuff
	L50	MPZ1608S300A	no stuff
	L51	MPZ1608S300A	no stuff
	C1078	2200pF	no stuff

			SV		ULV	
R_SEL[0]	pin 38	R1574	715	0.5%	280	1.0%
R_SEL[1]	pin 37	R1575	887	0.5%	825	0.5%
R_SEL[2]	pin 36	R1576	0	5.0%	0	5.0%
R_SEL[3]	pin 35	R1577	196	0.5%	196	0.5%
R_SEL[4]	pin 44	R1571	825	0.5%	825	0.5%
R_SEL[5]	pin 32	R1579	475	1.0%	402	0.5%
R_SEL[6]	pin 48	R1567	21.5	1.0%	21.5	1.0%
R_REF	pin 34	R1578	20K	0.5%	20K	0.5%
ROSC	pin 39	R1572	75K	1.0%	61.9K	1.0%

			SV		ULV	
LL_R1_CPU	pin 15 - pin 16	R1599	10K	0.5%	10K	0.5%
LL_R2_CPU	pin 16 - pin 17	R1600	20K	0.5%	7.5K	0.5%
	pin 16 - pin 17	R1601	787	1.0%	453	1.0%
LL_C1_CPU	pin 15 - pin 16	C1086	56pF		33pF	
LL_C2_CPU	pin 16 - pin 17	C1087	22pF		22pF	
LL_RLEAD_CPU	pin 15 - pin 16	R1595	30.1K	1.0%	15K	1.0%
LL_CLEAD_CPU	pin 15 - pin 16	C1091	220pF		680pF	
LL_RLAG_CPU	pin 16 - pin 17	R1596	no stuff		no stuff	
LL_CLAG_CPU	pin 16 - pin 17	C1092	no stuff		no stuff	
RDES_CPU	pin 17 - pin 18	R1592	845	0.5%	487	0.5%
RINT_CPU	pin 18 - pin 19	R1593	1.3K	1.0%	2.74K	1.0%
CINT_CPU	pin 18 - pin 19	C1088	4700PF		3300pF	
RPH11	IPH1_1 - pin 18	R1598	499	1.0%	750	1.0%
RPH12	IPH1_2 - pin 18	R1594	499	1.0%	no stuff	1.0%
IPHF11_R	pin 11	R1582	1.96K	1.0%	1.96K	1.0%
IPHF11_C	pin 11	C1076	10pF		no stuff	
IPHF12_R	pin 10	R1581	1.96K	1.0%	no stuff	1.0%
IPHF12_C	pin 10	C1075	10pF		no stuff	
R_MRAMP1	pin 12	R1584	15.8K	1.0%	13K	1.0%
R_MRAMP1_PU	pin 12	R1587	60.4K	1.0%	43.2K	1.0%
R_PWM2	Pin 6	R4	no stuff		0	5.0%
LL_R1_GPU	pin 24 - pin 23	R1609	10K	0.5%	10K	0.5%
LL_R2_GPU	pin 23 - pin 22	R1610	7.5K	0.5%	7.87K	0.5%
	pin 23 - pin 22	R1611	237	1.0%	0	5.0%
LL_C1_GPU	pin 24 - pin 23	C1094	22pF		22pF	
LL_C2_GPU	pin 23 - pin 22	C1095	22pF		22pF	
LL_RLEAD_GPU	pin 24 - pin 23	R1606	30K	1.0%	30K	1.0%
LL_CLEAD_GPU	pin 24 - pin 23	C1097	1200pF		1000pF	
LL_RLAG_GPU	pin 23 - pin 22	R1607	no stuff		no stuff	
LL_CLAG_GPU	pin 23 - pin 22	C1098	no stuff		no stuff	
RDES_GPU	pin 22 - pin 21	R1604	665	0.5%	665	0.5%
RINT_GPU	pin 21 - pin 20	R1605	3.24K	1.0%	3.24K	1.0%
CINT_GPU	pin 21 - pin 20	C1096	10nF		10nF	
RPH21	IPH2_1 - pin 21	R1608	1K	1.0%	1K	1.0%
IPHF21_R	pin 28	R1580	1.96K	1.0%	1.96K	1.0%
IPHF21_C	pin 28	C1077	no stuff		no stuff	
R_MRAMP2	pin 27	R1586	15.4K	1.0%	15.4K	1.0%
R_MRAMP2_PU	pin 27	R1590	56.2K	1.0%	56.2K	1.0%

Vcore Coutput

	SV		ULV	
C687	22uF	0805	22uF	0805
C669	22uF	0603	22uF	0603
C671	22uF	0603	22uF	0603
C673	4.7uF	0603	22uF	0603
C675	22uF	0603	22uF	0603
C676	22uF	0603	22uF	0603
C678	22uF	0603	22uF	0603
C695	4.7uF	0603	22uF	0603
C681	4.7uF	0603	22uF	0603
C682	4.7uF	0603	22uF	0603
C683	22uF	0603	22uF	0603
C750	22uF	0603	22uF	0603
C686	22uF	0603	22uF	0603
C691	22uF	0603	22uF	0603
C670	22uF	0603	22uF	0603
C694	22uF	0603	22uF	0603
C698	4.7uF	0603	22uF	0603
C1112	22uF	0603	22uF	0603
C1113	22uF	0603	22uF	0603
C672	22uF	0603	22uF	0603
C1157	22uF	0603	22uF	0603
C674	22uF	0603	22uF	0603
C1160	22uF	0603	22uF	0603
C689	22uF	0603	22uF	0603
C1167	22uF	0603	22uF	0603
C680	22uF	0603	22uF	0603
C679	4.7uF	0603	10uF	0603
C677	4.7uF	0603	10uF	0603
C1156	4.7uF	0603	10uF	0603
C684	10uF	0603	10uF	0603
C693	4.7uF	0603	10uF	0603
C1159	10uF	0603	10uF	0603
C692	10uF	0603	10uF	0603
C1158	4.7uF	0603	10uF	0603
C1161	4.7uF	0603	10uF	0603
C1162	4.7uF	0603	10uF	0603
C690	N/A	0603	N/A	0603
C1109	N/A	0603	N/A	0603
C1110	N/A	0603	N/A	0603
C1111	N/A	0603	N/A	0603
C1163	N/A	0603	N/A	0603
C1164	22uF	0603	N/A	0603
C1165	N/A	0603	N/A	0603
C1166	22uF	0603	N/A	0603
C1168	N/A	0603	N/A	0603
C1169	22uF	0603	N/A	0603
C444	2.2uF	0402	2.2uF	0402
C445	2.2uF	0402	2.2uF	0402
C446	2.2uF	0402	2.2uF	0402
C447	2.2uF	0402	2.2uF	0402
C448	2.2uF	0402	2.2uF	0402
C449	2.2uF	0402	2.2uF	0402
C450	2.2uF	0402	N/A	0402
C451	2.2uF	0402	2.2uF	0402
C452	2.2uF	0402	2.2uF	0402
C453	2.2uF	0402	2.2uF	0402

	SV		ULV	
C477	2.2uF	0402	2.2uF	0402
C486	2.2uF	0402	2.2uF	0402
C518	2.2uF	0402	2.2uF	0402
C520	2.2uF	0402	2.2uF	0402
C521	2.2uF	0402	2.2uF	0402
C522	2.2uF	0402	N/A	0402
C533	2.2uF	0402	2.2uF	0402
C830	2.2uF	0402	2.2uF	0402
C831	2.2uF	0402	2.2uF	0402
C832	2.2uF	0402	2.2uF	0402
C1172	N/A	0402	N/A	0402
C1173	2.2uF	0402	N/A	0402
C1174	2.2uF	0402	N/A	0402
C1175	2.2uF	0402	N/A	0402
C1176	N/A	0402	N/A	0402
C1177	N/A	0402	N/A	0402
C1178	2.2uF	0402	2.2uF	0402
C1179	2.2uF	0402	2.2uF	0402
C1180	2.2uF	0402	2.2uF	0402
C1181	2.2uF	0402	2.2uF	0402
C1182	2.2uF	0402	2.2uF	0402
C1185	2.2uF	0402	2.2uF	0402
C1186	2.2uF	0402	2.2uF	0402
C1187	2.2uF	0402	2.2uF	0402
C1188	2.2uF	0402	2.2uF	0402
C1189	2.2uF	0402	2.2uF	0402
C1190	2.2uF	0402	2.2uF	0402
C1191	2.2uF	0402	2.2uF	0402
C1192	2.2uF	0402	2.2uF	0402
C1193	2.2uF	0402	2.2uF	0402
C1194	2.2uF	0402	2.2uF	0402
C1195	2.2uF	0402	2.2uF	0402
C1196	2.2uF	0402	2.2uF	0402

<Core Design>

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Title: *VT1318M TABLE*

Size A2 | Document Number: Dasher-2 | Rev -1

	Capability											
Config	1	2	3	4	5	6	7	8	9	10	11	12
AOAC	Yes	Yes	Yes	Yes	No	No	No	No	Yes	No	Yes	No
Anti-Theft	Yes	Yes	No	No	Yes	Yes	No	No	Yes	Yes	No	No
External EEPROM	Yes	No	Yes	No	Yes	No	Yes	No	Yes	Yes	Yes	Yes
Duplicity	No	No	No	No	No	No	No	No	Yes	Yes	Yes	Yes
U66	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
U67	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
C864	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R978	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R929	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R901	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R930	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R1462	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
Q14	ASM	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
Q48	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R624	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R536	DY	DY	DY	DY	ASM	ASM	DY	DY	DY	DY	DY	DY
R539	DY	DY	DY	DY	ASM	ASM	DY	DY	DY	DY	DY	DY
U42	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
Q47	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R617	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
C524	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
D111	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R1463	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1464	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
U83	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
U84	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
C1020	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1466	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1468	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM

Logic

Duplicity	Y	Y	N	N
Battery Authentification	Y	N	Y	N
--> External EEPROM	Y	Y	Y	N

Logic